



Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from,Europe,America and south Asia,supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of “Quality Parts,Customers Priority,Honest Operation,and Considerate Service”,our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip,ALPS,ROHM,Xilinx,Pulse,ON,Everlight and Freescale. Main products comprise IC,Modules,Potentiometer,IC Socket,Relay,Connector.Our parts cover such applications as commercial,industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



Contact us

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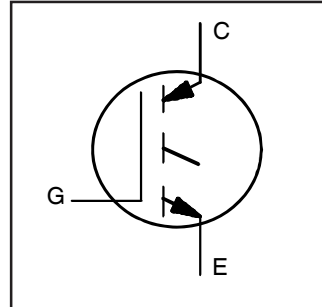
Email & Skype: info@chipsmall.com Web: www.chipsmall.com

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China



Features

- 100% Tested at Probe ①
- Available in Chip Pack, Unsaun wafer, Sawn on Film ②
- Low $V_{CE(on)}$ Trench IGBT Technology
- Low Switching Losses
- Maximum Junction temperature 175 °C
- 5 μ s SCSOA
- Square RBSOA
- Positive $V_{CE(on)}$ Temperature Coefficient.
- Tighter Distribution of Parameters



600V
 $I_{C(nom)} = 8.0A$
 $V_{CE(on)}$ typ. = 1.55V @ 8.0A
 @ $T_J = 25^\circ C$
 Motor Control IGBT
 Short Circuit Rated
 150mm Wafer

Benefits

- High Efficiency in a Wide Range of Applications
- Suitable for a Wide Range of Switching Frequencies due to Low $V_{CE(ON)}$ and Low Switching Losses
- Rugged Transient Performance for Increased Reliability
- Excellent Current Sharing in Parallel Operation
- Low EMI

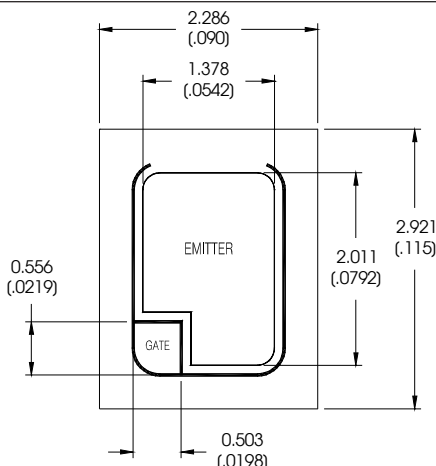
Key Electrical Characteristics (TO-220 Package) ②

Parameter	Description	Min	Typ.	Max	Test Conditions
$V_{CE(on)}$	Static Collector-to-Emitter On-Resistance	—	1.55V	1.85V	$V_{GE} = 15V, I_C = 8.0A, T_J = 25^\circ C$
$V_{(BR)CES}$	Collector-to-Emitter Saturation Voltage	600V	—	—	$V_{GE} = 0V, I_{CES} = 100\mu A, T_J = 25^\circ C$ ①
$V_{GE(th)}$	Gate Threshold Voltage	4.0V	—	6.5V	$V_{CE} = V_{GE}, I_C = 100\mu A, T_J = 25^\circ C$
I_{CES}	Collector-to-Emitter Leakage Current	—	—	25 μ A	$V_{CE} = 600V, V_{GE} = 0V, T_J = 25^\circ C$
I_{GES}	Gate-to-Emitter Leakage Current	—	—	$\pm 100nA$	$V_{GE} = \pm 20V$

Mechanical Data

Nominal Back Metal Composition, Thickness:	Al- Ti - Ni- Ag (1kA ^o -1kA ^o -4kA ^o -6kA ^o)
Nominal Front Metal Composition, Thickness:	Al, Si (4 μ m)
Dimensions:	0.090" x 0.115"
Wafer Diameter:	150 mm
Wafer Thickness:	70 μ m typ.
Relevant Die Mechanical Drawing Number	01-5754
Minimum Street Width	75 μ m
Reject Ink Dot Size	0.25 mm diameter minimum
Recommended Storage Environment:	Store in original container, in dessicated nitrogen, with no contamination
Recommended Die Attach Conditions:	For optimum electrical results, die attach temperature should not exceed 300 °C
Reference Packaged Part	IRGB4060D

Die Outline



NOTES:

1. ALL DIMENSIONS ARE SHOWN IN MILLIMETERS (INCHES).
2. CONTROLLING DIMENSION: (INCH).
3. LETTER DESIGNATION:
 S = SOURCE SK = SOURCE KELVIN E = EMITTER
 G = GATE IS = CURRENTSENSE
4. DIMENSIONAL TOLERANCES:
 BONDING PADS: < 0.635 TOLERANCE = + / - 0.013
 WIDTH < (0.250) TOLERANCE = + / - (.0005)
 & > 0.635 TOLERANCE = + / - 0.025
 LENGTH > (0.250) TOLERANCE = + / - (.0010)
 OVERALL DIE: < 1.270 TOLERANCE = + / - 0.102
 WIDTH < (0.050) TOLERANCE = + / - (.004)
 & > 1.270 TOLERANCE = + / - 0.203
 LENGTH > (0.050) TOLERANCE = + / - (.008)
5. DIE THICKNESS = 0.070 (.0028) TOL. = 0.007 (.0003)

Additional Testing and Screening

For Customers requiring product supplied as Known Good Die (KGD) or requiring specific die level testing, please contact your local IR Sales.

Shipping

Three shipping options are offered as standard.

- Un-sawn wafer
- Die in waffle pack
- Die on film

Tape and Reel is also available for some products. Please consult your local IR sales office or email <http://die.irf.com> for additional information. Please specify your required shipping option when requesting prices and ordering Die product. If not specified, Un-sawn wafer will be assumed.

Handling

- Product must be handled only at ESD safe workstations. Standard ESD precautions and safe work environments are as defined in MIL-HDBK-263.
- Product must be handled only in a class 10,000 or better-designated clean room environment.
- Singulated die are not to be handled with tweezers. A vacuum wand with a non-metallic ESD protected tip should be used.

Wafer/Die Storage

- Proper storage conditions are necessary to prevent product contamination and/or degradation after shipment.
- Un-sawn wafers and singulated die can be stored for up to 12 months when in the original sealed packaging at room temperature (45% +/- 15% RH controlled environment).
- Un-sawn wafers and singulated die that have been opened can be stored when returned to their containers and placed in a Nitrogen purged cabinet, at room temperature (45% +/- 15% RH controlled environment).
- Note: To reduce the risk of contamination or degradation, it is recommended that product not being used in the assembly process be returned to their original containers and resealed with a vacuum seal process.
- Sawn wafers on a film frame are intended for immediate use and have a limited shelf life.
- Die in Surf Tape type carrier tape are intended for immediate use and have a limited shelf life. This is primarily due to the nature of the adhesive tape used to hold the product in the carrier tape cavity. This product can be stored for up to 30 days. This applies whether or not the material has remained in its original sealed container.

Further Information

For further information please contact your local IR Sales office or email your enquiry to <http://die.irf.com>

Note:

① This IR product is 100% tested at wafer level and is manufactured using established, mature and well characterized processes. Due to restrictions in die level processing, die may not be equivalent to standard package products and are therefore offered with a conditional performance guarantee. The above data sheet is based on IR sample testing under certain predetermined and assumed conditions, and are provided for illustration purposes only. Customers are encouraged to perform testing in actual proposed packaged and use conditions. IR die products are tested using IR-based quality assurance procedures and are manufactured using IR's established processes. Programs for customer-specified testing are available upon request. IR has experienced assembly yields of generally 95% or greater for individual die; however, customer's results will vary. Estimates such as those described and set forth in this data sheet for semiconductor die will vary depending on a number of packaging, handling, use and other factors. Sold die may not perform on an equivalent basis to standard package products and are therefore offered with a limited warranty as described in IR's applicable standard terms and conditions of sale. All IR die sales are subject to IR's applicable standard terms and conditions of sale, which are available upon request.

For customers requiring a particular parameter to be guaranteed, special testing can be carried out or product can be purchased as known good die.

② Part number shown is for die in wafer. Contact factory for these other options.

Data and specifications subject to change without notice.
This product has been designed and qualified for Industrial market.
Qualification Standards can be found on IR's Web site.

International
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