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IRS26310DJ GATE DRIVER IC FEATURES

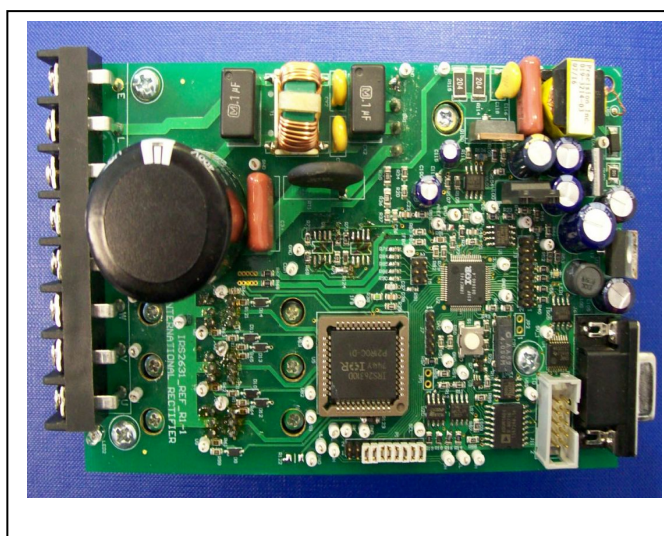
- Drives up to six IGBT/MOSFET power devices
- Gate drive supplies up to 20 V per channel
- Integrated bootstrap functionality
- DC bus sensing with Over Voltage protection
- Over-current protection
- Over-temperature shutdown input
- Advanced input filter
- Integrated deadtime protection
- Shoot-through (cross-conduction) protection
- Under voltage lockout for V_{CC} & V_{BS}
- Enable/disable input and fault reporting
- Adjustable fault clear timing
- Separate logic and power grounds
- 3.3 V input logic compatible
- Tolerant to negative transient voltage
- Designed for use with bootstrap power supplies
- Matched propagation delays for all channels

REFERENCE DESIGN FEATURES

- Complete 3-phase ac motor drive system to showcase IRS26310DJ gate driver IC operation
- No extra hardware needed for PWM signal generation; Option to incorporate external PWM signals to drive IRS26310DJ if desired
- GUI software for running Induction Motor in open loop Volts-per-Hertz mode of operation
- Discrete IGBT power stage for maximum flexibility
 - IGBT's rated for 10A @100°C
 - Short circuit rating 10 μ s
 - TO220 Full-pak package
- Conveniently located Test points for IRS26310DJ signal monitoring

Product Summary

AC input Voltage (typ.)	220 V rms
Continuous motor current	3 A rms
dV/dt	5 kV/ μ s
Bus over voltage protection (typ.)	386 V
Continuous output power	400 W



Overview

The IRMD26310DJ is a reference design kit for the IRS26310DJ three phase gate driver IC with integrated bootstrap functionality and DC bus overvoltage protection. The design kit includes a complete drive system board with an input rectifier, control power supplies, digital motor control IC and power inverter. Safety features include protections against motor line-to-line, line-to-DC Bus(-) short situations & DC bus overvoltage conditions. The power stage features the IRS26310DJ gate driver and six discrete IGBT power switches allowing for easy customization. The included PC based GUI software allows the user to drive an Induction Motor in open-loop Volts-per-Hertz mode of operation using the on-board digital control IC. The motor speed and system parameters such as PWM frequency and deadtime can be modified using the GUI. Alternately, the IRS26310DJ gate drive input PWM signals can be supplied externally by patching in signals through an on-board connector.

Table of Contents

Safety Precautions.....4

IRMD26310DJ Hardware Description.....6

 Top Level Description.....6

 IRS26310DJ Gate Driver IC.....7

 Bootstrap Supply.....8

 Gate Drive Resistors.....8

 IRMD26310DJ Protection Features.....9

 IRMD26310DJ Fault Reporting.....9

 Automatic Reset after ITRIP Fault.....9

 IRMD26310 Enable Function.....10

 PWM input to IRS26310DJ & Connection of an external system controller.....10

 Test Points for Investigation.....11

IRMD26310DJ Operating Instructions.....12

 Software Installation.....12

 Test Bench Connection.....12

 Powering the Board.....13

 Running the Software GUI.....14

 Software GUI Fault Handling Instructions.....17

 How to Trigger IRS26310DJ DC Bus Overvoltage Protection.....18

IRMD26310DJ Software GUI Reference Guide.....19

 Motor Parameters.....19

 Inverter Parameters.....19

 System Status.....19

 Control.....19

 Monitors.....20

 Speed Control.....20

IRMD26310DJ Circuit Schematics.....21

IRMD26310DJ Bill of Materials.....25

IRMD26310DJ Specifications.....28

List of Figures

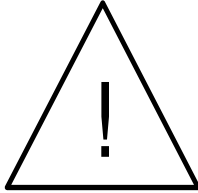
Figure 1: IRMD26310 system block diagram.....6
 Figure 2: Picture of IRMD26310 reference design.....7
 Figure 3: IRS26310 application diagram.....7
 Figure 4: Bootstrap circuit.....8
 Figure 5: Gate circuit.....8
 Figure 6: Protection circuits, fault reporting & automatic reset after ITRIP fault.....10
 Figure 7: 20 Pin connector, P1.....10
 Figure 8: Test bench connection.....12
 Figure 9: Board connections.....13
 Figure 10: 8 pin connector, J1.....13
 Figure 11: Location of diagnostic LEDs in IRMD26310DJ.....14
 Figure 12: Software GUI before establishing communication between IRMD26310DJ and PC.....14
 Figure 13: COM port selection.....15
 Figure 14: Software GUI after establishing communication between IRMD26310DJ and PC.....15
 Figure 15: Software GUI after successful configuration of IRMD26310DJ.....16
 Figure 16: Software GUI while running the motor using IRMD26310DJ.....17
 Figure 17: Software GUI during 2 different fault situations.....17
 Figure 18: IRS26310DJ DC bus overvoltage protection.....18
 Figure 19: Zero-vector insertion during IRS26310 DC bus overvoltage protection.....18
 Figure 20: IRMD26310DJ power stage circuit schematic.....21
 Figure 21: IRMD26310DJ Digital Control & Communications Circuit Schematic.....22
 Figure 22: IRMD26310DJ RS-232 Drivers & Receivers Circuit Schematic.....23
 Figure 23: IRMD26310DJ DC-DC Converter Low Voltage Power Supply Circuit Schematic.....24

List of Tables

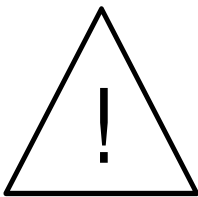
Table 1: Gate drive circuit components.....8
 Table 2: List of test points.....11
 Table 3: List of faults issued by GUI.....20
 Table 4: IRMD26310 electrical specifications.....28

Safety Precautions

In addition to the precautions listed throughout this manual, please read and understand the following statements regarding hazards associated with development system.



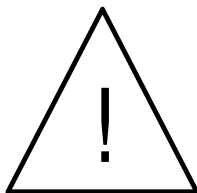
ATTENTION: The ground potential of the IRMD26310DJ system is biased to a negative DC bus voltage potential. When measuring voltage waveform by oscilloscope, the scope ground needs to be isolated. Failure to do so may result in personal injury or death.
Darkened display LEDs is not an indication that capacitors have discharged to safe voltage levels.



ATTENTION: The IRMD26310DJ system contains dc bus capacitors, which take time to discharge after removal of main supply. Before working on drive system, wait three minutes for capacitors to discharge to safe voltage levels. Failure to do so may result in personal injury or death.
Darkened display LEDs is not an indication that capacitors have discharged to safe voltage levels.



ATTENTION: Only personnel familiar with the drive and associated machinery should plan or implement the installation, start-up, and subsequent maintenance of the system. Failure to comply may result in personal injury and/or equipment damage.



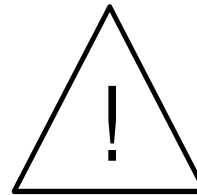
ATTENTION: The surface temperatures of the drive may become hot, which may cause injury.



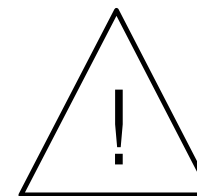
ATTENTION: The IRMD26310DJ system contains ESD (Electrostatic Discharge) sensitive parts and assemblies. Static control precautions are required when installing, testing, servicing or repairing this assembly. Component damage may result if ESD control procedures are not followed. If you are not familiar with static control procedures, reference applicable ESD protection handbook and guideline.



ATTENTION: An incorrectly applied or installed drive can result in component damage or reduction in product life. Wiring or application errors such as undersizing the motor, supplying an incorrect or inadequate AC supply, or excessive ambient temperatures may result in system malfunction.



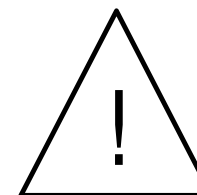
ATTENTION: Remove and lock out power from the drive before you disconnect or reconnect wires or perform service. Wait three minutes after removing power to discharge the bus voltage. Do not attempt to service the drive until bus voltage has discharged to zero. Failure to do so may result in bodily injury or death.



ATTENTION: Do not connect power factor correction capacitors to drive output terminals U, V, and W. Failure to do so may result in equipment damage or bodily injury.

Debris When Unpacking

IRMCS3041 system is shipped with packing materials that need to be removed prior to installation.



ATTENTION: Failure to remove all debris and packing materials which are unnecessary for system installation may result in overheating or abnormal operating condition.

IRMD26310DJ - Hardware Description

The IRMD26310DJ reference design kit supports the evaluation of the IRS26310DJ which is a high voltage power MOSFET and IGBT driver with integrated DC Bus overvoltage protection. The reference design is a full-function unit operating out of 220VAC input. An onboard digital controller IC enables driving an Induction Motor in open-loop Volts-per-Hertz mode. The included GUI software allows modification of system parameters in order to drive an induction motor in open-loop Volts-per-Hertz mode and study the IRS26310DJ high voltage gate driver IC by changing parameters such as PWM frequency and deadtime. In addition to DC Bus overvoltage protection offered by IRS26310DJ, the reference design is equipped with protection against motor line-to-line and line-to-DC Bus(-) short by way of IRS26310DJ ITRIP function and also a GUI software controlled motor current limit. The board includes multiple test points to facilitate monitoring IRS26310DJ input and output signal waveforms and examining its various features.

Top Level Description

The IRMD26310DJ reference design is composed of the following key blocks:

- The Input Stage includes an EMI filter, 8A bridge rectifier and dc bus capacitor rated for 400W continuous operation. It is noted that IRMD26310DJ does not feature a current limiting fuse.
- The Power Inverter uses 6 x IRGIB10K60D1 IGBT power switches mounted on a 1.9°C/W heatsink. The IGBTs can be replaced with user preferred power devices but user may need to replace components such as bridge rectifier & dc bus current shunt to match the device ratings.
- The IGBTs in the power inverter stage are driven by the IRS26310DJ Gate Driver and associated circuitry involving bootstrap capacitors, gate drive timing resistors, and fault diagnostic elements.
- The Control Circuitry for driving the motor is based on the IRMCF341 digital control IC. The IRMCF341 control IC has been configured to run an induction motor in the open-loop Volts-per-Hertz mode. The control IC generates the PWM signals for IRS26310DJ gate driver. The user also has the option to drive the IRS26310DJ externally by disconnecting the jumpers on connector P1 and bridging in external connections. The digital IC communicates with a PC over an isolated serial link and the included GUI software allows the user to set the motor speed and modify common inverter parameters such as switching frequency and deadtime.
- An on-board DC-DC Converter low voltage power supply derives the 15V, 3.3V & 1.8V rails from the main dc bus.

For more information regarding the other blocks of the reference design, please refer to the detailed schematics.

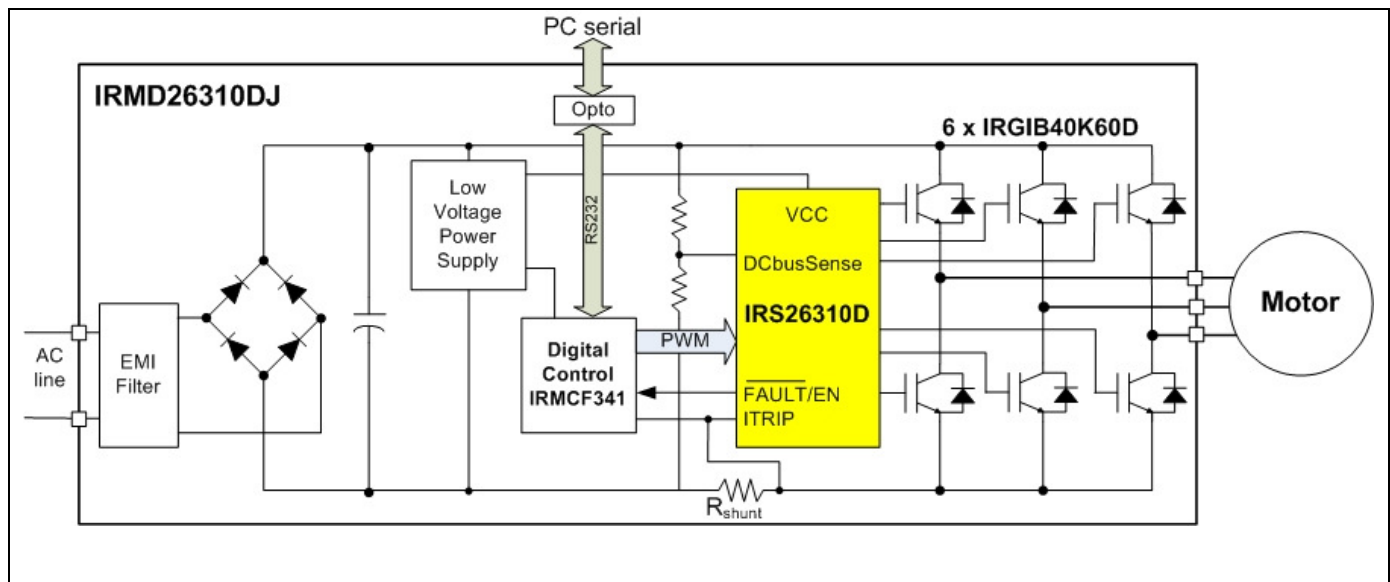


Figure 1: IRMD26310DJ System Block Diagram

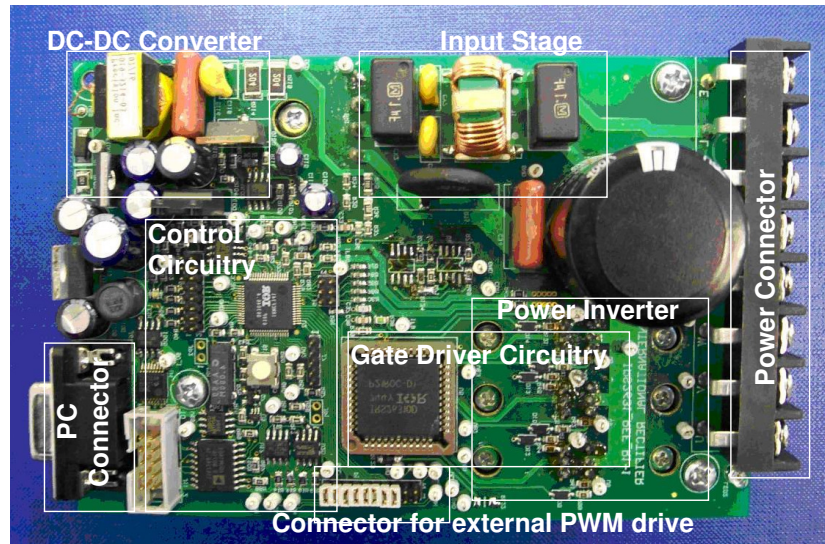


Figure 2: Picture of IRMD26310DJ Reference Design

IRS26310DJ Gate Driver IC

The IRS26310DJ IC integrates three independent half bridge drivers with shoot through protection and internal dead-time insertion. Proprietary HVIC technology enables ruggedized monolithic construction with logic inputs compatible with CMOS or LSTTL levels as low as 3.3 V. The output drivers feature a high pulse current buffer stage designed for minimum driver cross-conduction. Propagation delays are matched to simplify use in high frequency applications. The floating channel can be used to drive N-channel power MOSFETs or IGBTs in the high side configuration, which operates up to 600V. The IC is based on “Active High” input logic i.e. a logic HIGH input turns ON the corresponding output and vice versa. In addition to VSS pin, the IC features a COM pin to provide a dedicated, low impedance return path for the low side gate driver which also serves to shunt the high-frequency gate drive currents away from the current sense resistor thus preventing false trips. The IC provides DC bus capacitor protection by activating zero-vector motor braking whenever an overvoltage condition is sensed. This feature should only be used with motors that can withstand short-term short circuit of the motor windings. Other protection features offered by the IC include UVLO for VCC & VBS supplies and motor current trip along with integrated fault diagnostics are also offered in IRS26310DJ. An open-drain FAULT signal is provided to indicate that an over-current or a VCC under-voltage shutdown has occurred. An enable function integrated into the FAULT/ pin allowing the user to terminate all six outputs simultaneously.

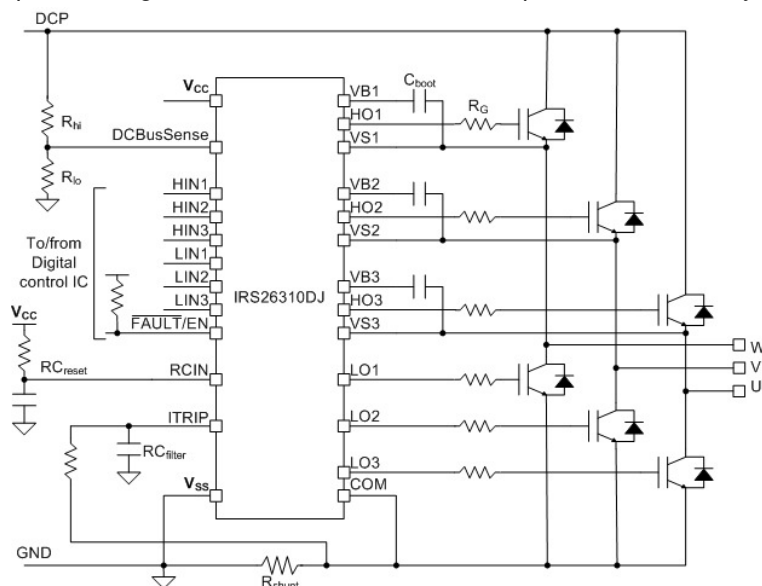


Figure 3: IRS26310DJ Application Diagram

Bootstrap Supply

The floating high side driver supplies are generated by a bootstrap circuit as shown in **Figure 4**. The IC turns on the internal 'bootFET' when the low side transistor is on to charge up the bootstrap capacitor (C_{boot}). The 'bootFET' eliminates the requirement for an external boot diode D_{boot} as is the case for previous generation gate drivers. An application note AN-1123 (<http://www.irf.com/technical-info/appnotes/an-1123.pdf>) describes the selection of the bootstrap capacitor and considerations when using the internal bootFET. In some cases, an external bootstrap circuit may be preferable. The IRMD26310DJ board also provides option for external bootstrap supply. This can be invoked by populating the bootstrap resistor, R_{boot} (R17) and bootstrap diodes (D6, D7 & D8).

The bootstrap capacitors must be fully charged to avoid missing high side pulses due to under voltage lockout on the floating high side supply. The digital controller on the board pre-charges the capacitors by turning on the low side transistors for a short period before starting to run the motor. The charging current and time depends on the resistance of the internal bootFET or the external boot resistance. Typically, the pre-charge sequence charges the capacitors one at a time to avoid spurious over current trips since the charging current flows through the current sensing shunt.

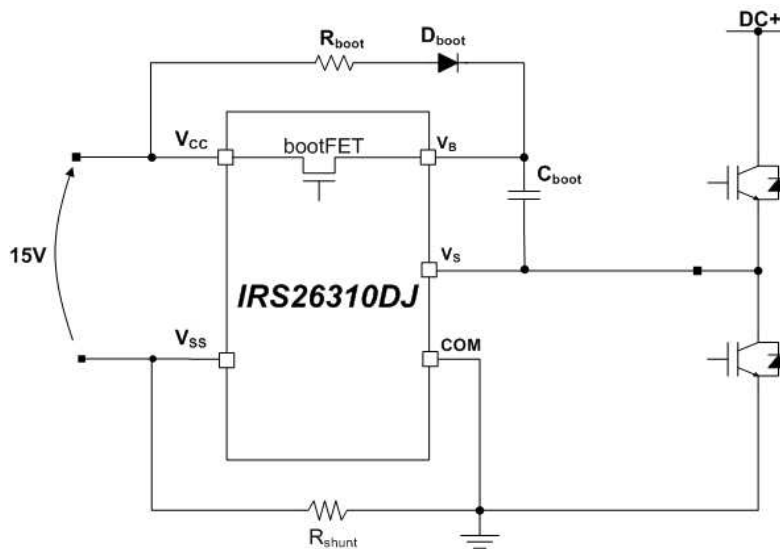


Figure 4: Bootstrap circuit

Gate drive resistors

The IRMD26310DJ board has gate resistor networks to allow fine tuning of the power switch turn on and turn off times. **Figure** shows the gate circuit schematic while Table 1 list the resistors used in the network for each power device.

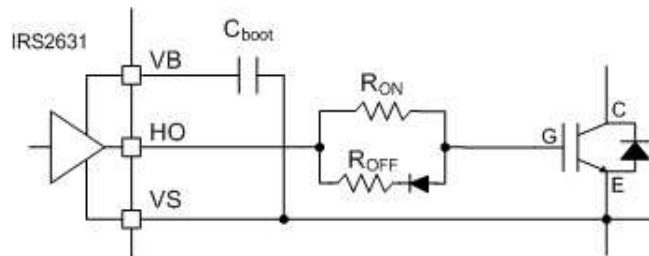


Figure 5: Gate circuit

	U	V	W
High side gate (HO turn-on/off)	R61/R99/D9	R62/R101/D10	R63/R118/D11
Low side gate (LO turn-on/off)	R64/R119/D12	R65/R120/D13	R66/R121/D14

Table 1: Gate drive circuit components

IRMD26310DJ Protection Features

Motor Overcurrent Protection

Motor over-current protection is implemented using a dc link shunt resistor (R60) placed between power ground (COM pin) and logic ground (VSS pin) as shown in Figure 6. The ITRIP signal from the shunt has an input RC filter (R38, C37) to reject power inverter noise. The IRS26310D has a comparator that generates a shutdown signal when the ITRIP pin voltage exceeds the ITRIP threshold ($V_{IT,TH+}$). An internal noise filter rejects pulses shorter than 400ns to avoid spurious trips due to diode reverse recovery current. The shutdown signal turns off all six outputs and pulls the \overline{FAULT}/EN pin low. When the over-current condition is cleared, the fault remains latched until reset by the voltage on the RCIN.

Under-voltage protection circuits

The IC monitors the supply voltage on the V_{CC} pin. It turns off all six outputs and pulls the \overline{FAULT}/EN pin low when V_{CC} falls below a minimum threshold (V_{CCUV-}). This fault is not latched but is cleared once V_{CC} goes above the positive going threshold (V_{CCUV+}). The IC also features UVLO protection for all 3 VBS floating supplies; However, a VBS UVLO condition does not trigger fault reporting i.e. \overline{FAULT}/EN pin is not pulled low. Under a VBS UVLO condition, only the corresponding gate driver output is terminated until the VBS UVLO condition is cleared.

DC Bus over-voltage protection circuits

The IRS26310D monitors the dc bus to prevent it charging to an unsafe voltage level during regenerative braking. The divider network R35, R36 and R37 drops the DC bus to a low voltage level and communicates to IRS26310D. A comparator and noise filter detects a bus over-voltage condition when the voltage on the DCBusSense pin goes above a maximum threshold ($V_{DCBUSOV+}$). The default resistor divider configuration in IRMD26310DJ will trigger IRS26310DJ DC bus overvoltage protection feature at approximately 386V.

When an over-voltage condition is sensed, it forces IRS26310DJ into zero vector mode, which turns on all low side transistors and turns off all high side transistors irrespective of the PWM input commands from digital control IC. IRS26310DJ then returns to normal operating mode when the voltage on the DCBusSense pin drops below a minimum threshold ($V_{DCBUSOV-}$). The dc bus over-voltage state is not reported on the \overline{FAULT}/EN pin. **For more information regarding how to trigger the DC bus overvoltage protection feature, please refer to “Operating Instructions” section.**

IRMD26310DJ Fault Reporting

The \overline{FAULT}/EN pin of the IC is used to communicate a fault situation to the digital control IC (DCIC). The \overline{FAULT}/EN pin, which is normally pulled up to 3.3V using pull up resistor, is connected to the GATEKILL pin on the IRMCF341. When the IC detects a fault situation (either due to VCC UVLO or ITRIP) all gate driver outputs are shut-down and the \overline{FAULT}/EN pin is pulled low and this triggers the GATEKILL function of IRMCF341 causing all PWM outputs of IRMCF341 to be shutdown. Fault reporting is performed only for the following:

- Motor overcurrent (ITRIP)
- VCC supply UVLO

It is again noted that VBS UVLO & dc bus overvoltage situations do not trigger a fault situation.

Automatic Reset after ITRIP Fault

Automatic fault reset provided by the HVIC is based on the time constant (RC_{reset}) of the passive network on the RCIN pin. An RC network (R39, C38) between the RCIN pin and VCC supply allows an automatic reset of the ITRIP fault. The capacitor on the RCIN pin is held low as long as the ITRIP comparator input exceeds the ITRIP voltage threshold. When the ITRIP fault condition clears, the capacitor is allowed to charge up. The ITRIP fault is reset when the capacitor voltage exceeds a minimum threshold ($V_{RCinTH+}$). The RCIN auto-reset time can be calculated from the following equation:

$$t = R_{39} \cdot C_{38} \ln \left(\frac{V_{cc}}{V_{cc} - V_{RCinTH+}} \right)$$

The reference design auto reset time is approximately 1.6ms for $V_{cc}=15V$, $V_{RCinTH+}=8V$ and $R_{39}=2M\Omega$, $C_{38}=1nF$. The on resistance of the reset FET on the RCIN pin cannot be neglected so a relatively small capacitor should be used so that it becomes fully discharged before the fault is released.

IRMD26310 Enable Function

The IC features an Enable function integrated into the \overline{FAULT}/EN pin to provide enable/disable functionality. The IC functionality is enabled when EN pin is biased high. In the IRMD26310DJ reference design, the \overline{FAULT}/EN pin is pulled up to 3.3V DC bus using resistor R28 potential to always enable operation.

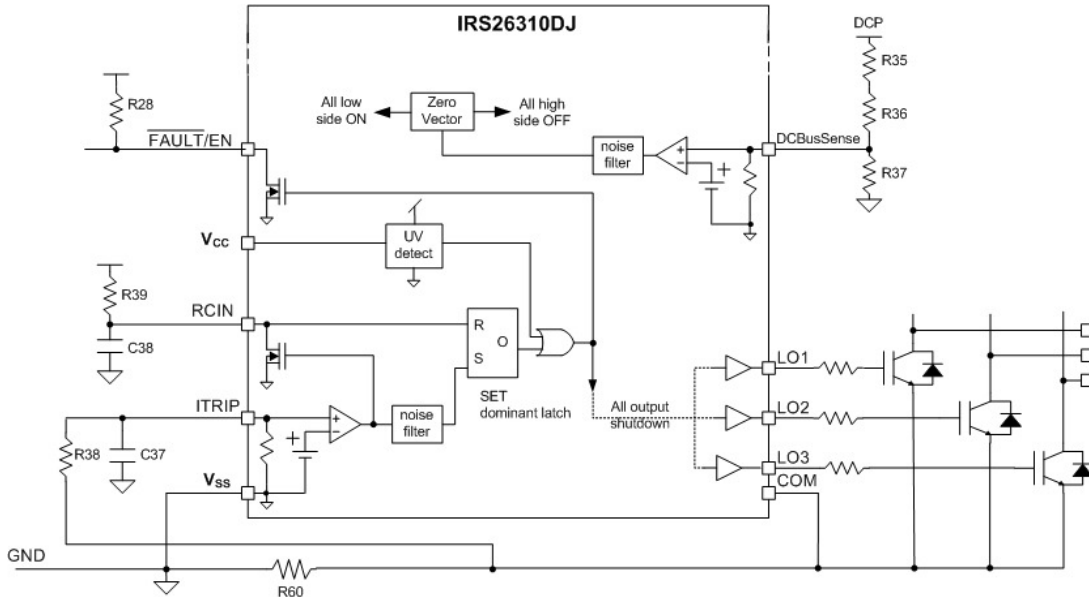


Figure 6: Protection circuits, Fault Reporting & Automatic Reset after ITRIP fault

PWM input to IRS26310DJ & Connection of an external system controller

IRMD26310DJ is configured by default to use the PWM signals from the on-board IRMCF341 controller for the IRS26310DJ HVIC. The default configuration connects the IRMCF341 digital control IC to the IRS26310DJ by shorting pins 5-6, 7-8, 9-10, 11-12, 13-14, 15-16, 17-18 on connector P1 shown in Figure 7. Since the IRS26310DJ is based on Active High logic, the PWM inputs to the HVIC in IRMD26310DJ are connected to GND using pull-down resistors (R26 to R32).

An external controller can easily be invoked for driving the IRS26310J by removing the shorting connections on the 20 pins DIL connector P1 and connecting the external PWM signals to the odd numbered pins on P1. It is reiterated that the correct PWM inputs that is appropriate for the “Active High” logic of IRS26310DJ must be maintained.

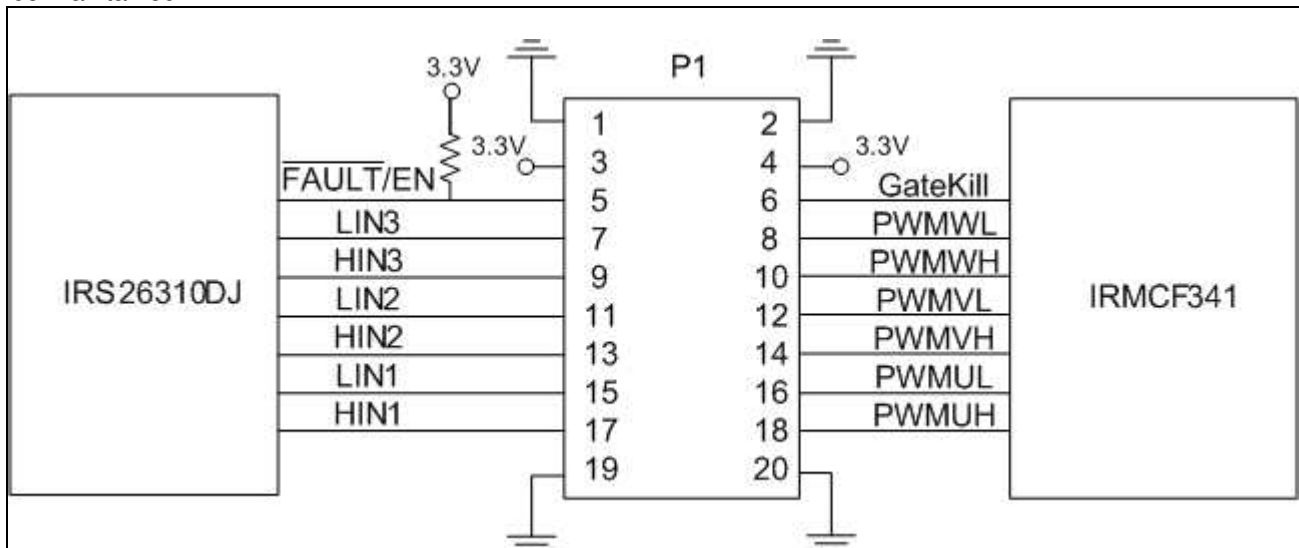


Figure 7: 20-pin connector P1

Test Points for Investigation

The reference design is provided with ample test points to probe the different pins of the gate driver IC and inverter circuit. The following table provides a summary of the most useful test-points around the IRS26310DJ HVIC and their associated functionality.

Test Point #	Test Point Name	Remark
TP28, 29, 30	U, V, W	Motor Phase nodes (also negative terminals of bootstrap supply voltage)
TP37, 38 & 39	VB3, VB2, VB1	Boot-strap supply voltage positive terminal
TP9, 10, 11	UH, VH, WH	PWM input to high-side channels of HVIC (HIN1, HIN2 & HIN3)
TP31, 32, 33	UG, VG & WG	Gate of high-side IGBTs
TP7, 8, 9	UL, VL, WL	PWM input to low-side channels of HVIC (LIN1, LIN2 & LIN3)
TP34, 35, 36	UG/, VG/, WG/	Gate of low-side IGBTs
TP24	COM	Common emitter of low-side IGBTs (also Positive terminal of current shunt resistor)
TP22	GK	\overline{FAULT} / EN pin of IRS26310D (also Input to IRMCF341 GATEKILL pin)
TP2	IFB	Motor current feedback
TP4	DCP	DC Bus
TP14, 15, 25, 26, 27	GND	Logic ground of IRS26310D (VSS pin) & DC Bus return
TP103	VCC	VCC pin of IRS26310D

Table 2: List of Test Points

IRMD26310DJ – Operating Instructions

The following hardware is supplied with the IRMD26310DJ reference design kit

- IRMD26310DJ board with heat sink
- GUI Software CD-ROM
- PC USB-serial cable & driver CD-ROM

Visually inspect IRMD26310DJ board to check for loose wiring, loose or damaged components or other abnormalities before proceeding.

STEP 1: Software Installation

The reference design kit is supplied with IRMD26310 GUI v1.0 software intended to support the evaluation of IRS26310DJ HVIC. The on board digital control IC (IRMC341) generates the inverter PWM signals and can run a three phase induction motor using open loop V/F or Volts-per-Hertz control. This software is the GUI that allows the specification of the motor, inverter and system parameters.

The software tool is distributed on a CD-ROM. Load the CD into the CD-ROM drive on a PC and if installation does not start double-click on the .exe installation file. The automated procedure installs all necessary software and documentation on the PC. The default location for the installation is “C:\Program Files\IR\IRMD26310DJ”.

STEP 2: Test Bench Connection

1. Connect the ac power cables to Earth (E), line (L) and neutral (N) terminals of J1 connector. Prepare a power contactor switch rated at 250V/10A in series with AC power cables if necessary.



ATTENTION: Turn off or unplug the power before making any connections to board. All circuits on board must be considered as ‘live’ with respect to the safety earth so please use extreme caution when making connections to circuit board. It is recommended to isolate the oscilloscope ground when making circuit waveform measurements.

2. Connect the motor windings to the U/V/W terminals of connector J1. If needed, connect the motor frame to the Safety Earth connection available on the E terminal of connector J1.
NOTE: GND terminal of connector J1 is NOT to be used for the purpose of safety grounding.
3. Connect the RS-232 cable between the DB-9 connector J6 on IRMD26310DJ board and an available COM port (USB port) on your PC. The RS-232 serial connector is galvanically isolated from the circuit board ground so the user can safely connect a PC to the board.

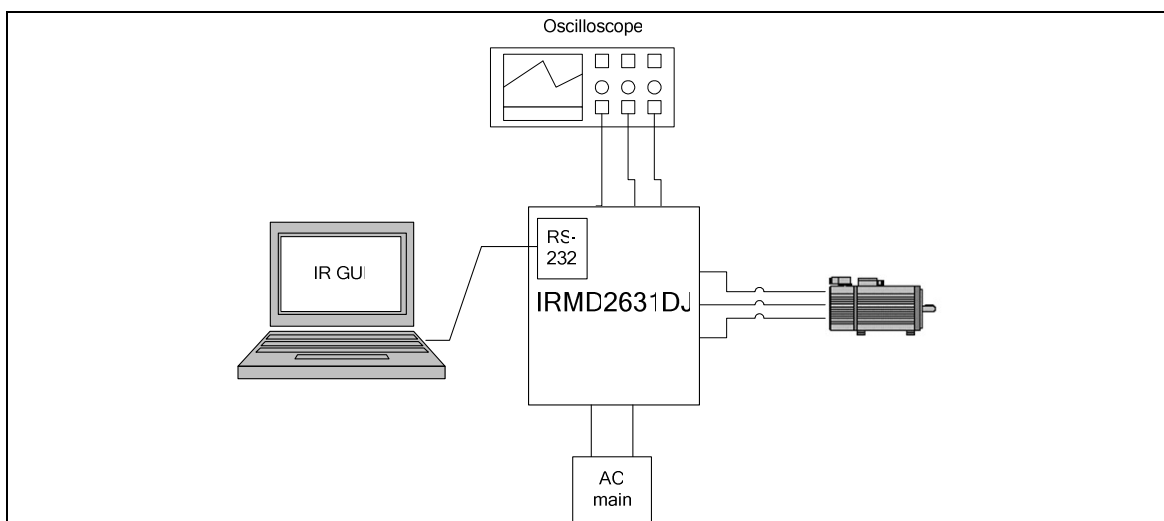


Figure 8: Test bench connection

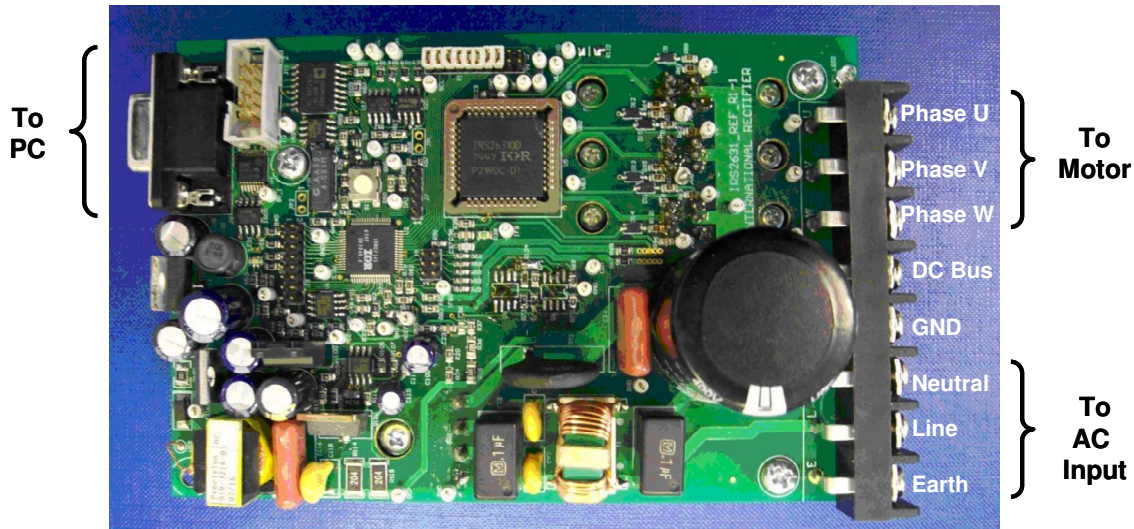


Figure 9: Board connections

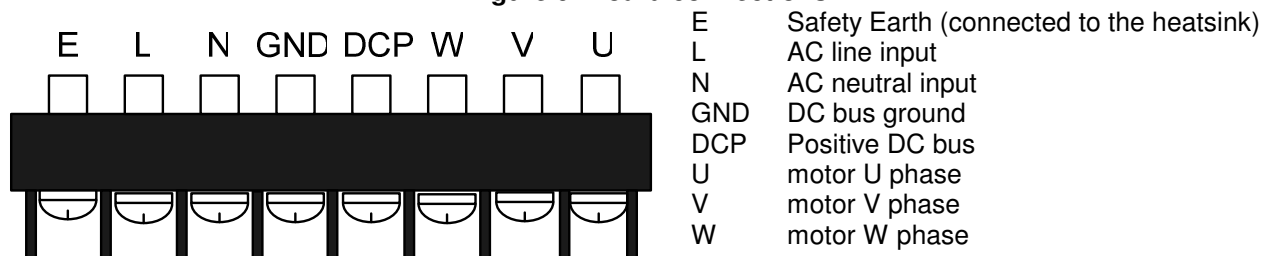


Figure 10: 8-pin connector J1



ATTENTION: The GND terminal of connector J1, called DC Bus Ground, is biased to the negative DC bus voltage potential of IRMD26310DJ, which connects to the ac power line input via the input rectifier. The GND terminal is NOT to be confused with safety ground of the system. Instead, the E terminal of connector J1, called Safety Earth, is to be used for purpose of safety grounding. Failure to follow these instructions can result in personal injury or death.

STEP 3: Powering the Board

Connect the power cables to 220VAC mains and turn-on the power switch. There are 2 on-board LEDs for diagnostic purposes as shown in Fig.11.

1. Check if LED2 is lit in red after you apply AC power. LED2 is connected to the dc bus and indicates that power is connected to the board and the on-board switching mode power supply is active.

NOTE: Never attempt to service the board or engage/disengage any connections when LED2 is lit since it indicates the presence of high-voltages on the board.

2. Check if LED1 is a slowly flashing green. LED1 is driven by the digital control IC IRMCF341 controller. It is lit red when a fault is detected and slowly flashes green when IRMCF341 is in proper operational status. If LED1 is lit red after powering the board then it indicates one of the following:
 - i. *Under-voltage fault:* This occurs if rectified DC bus voltage is less than 120V; Check DC Bus & AC input voltage levels and ensure DC bus is greater than 120V. Then proceed to Step 4. LED1 will be a flashing green once Step 4 is completed.
 - ii. *Over-voltage fault:* This occurs if rectified DC bus voltage is greater than 360V; Check DC Bus & AC input voltage levels and ensure that DC bus is less than 360V. Then proceed to Step 4. LED1 will be a flashing green once Step 4 is completed.
 - iii. *Reference Design Configuration Error:* Contact IR Support.

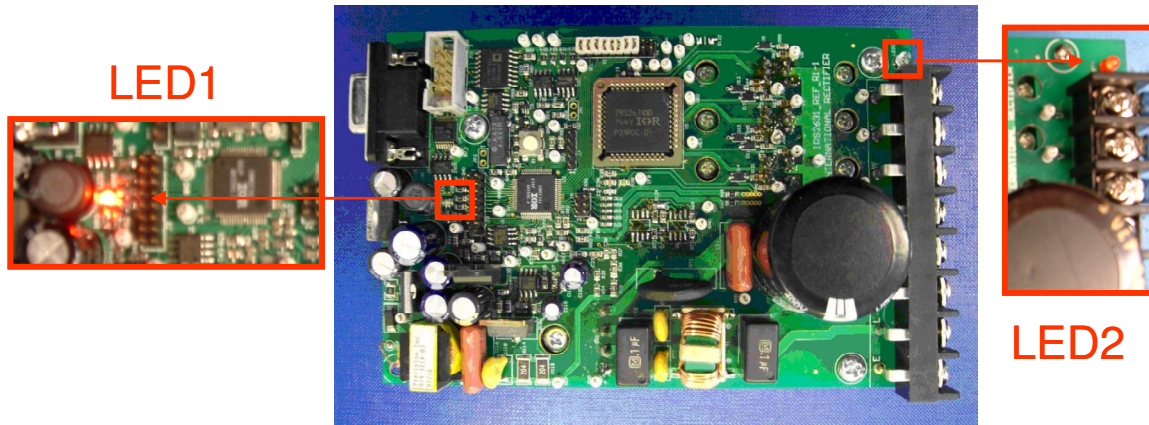


Figure 11: Location of diagnostic LEDs in IRMD26310DJ

STEP 4: Running the Software GUI

Step 4a: Start the GUI software program on PC

Start the GUI Software by clicking the desktop icon or double-clicking .exe file.



Figure 12: Software GUI before establishing communication between PC & IRMD26310DJ

Note the following:

- All 3 icons in System Status section of GUI in the top-right corner of the GUI (Connection Status, Fault Status & Configuration Status) are yellow in color.
- At the bottom of the GUI the messages “No COM Port Set” & “Disconnected” are displayed
- All buttons in the Controls section of GUI are disabled

Step 4b: Selecting the COM port (RS-232)

The software on the PC communicates with the digital control IC on-board using a COM port. It is necessary to select the COM port to which the USB cable is connected to run the GUI software. COM port can be selected by clicking on “COM Settings” at the bottom-left of the GUI window and selecting one of the options.

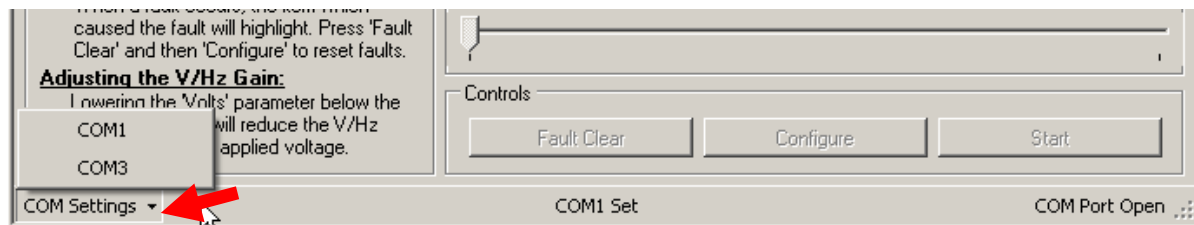


Figure 13: COM port selection

When the correct COM port has been selected and communication is successfully established,

- the “Connection status” & “Fault status” icons in the System Status section of the GUI should change colors from yellow to green
- the Monitor section icons should change color from yellow to green and provide real time information regarding the item being monitored i.e. DC Bus Voltage & Output current
- the “Fault Clear” & “Configure” buttons in Controls section are enabled and
- the messages “COM# Set” (where # is the appropriate COM port number) and “COM Port UP” are displayed at the bottom.

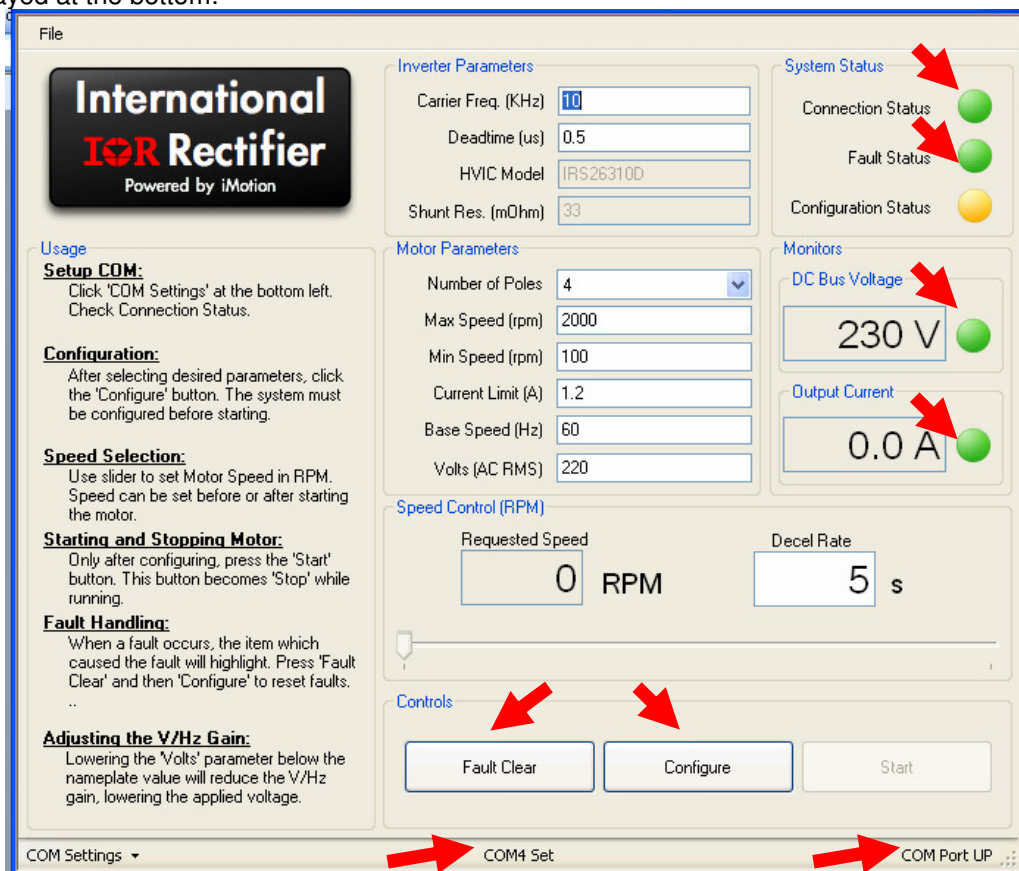


Figure 14: Software GUI after establishing communication between PC & IRMD26310DJ

Step 4c: Configuring the Hardware

After the selection of inverter and motor parameters on the GUI click on the 'Configure' button to load them to the digital control IC on the IRMD26310DJ board. This step is absolutely necessary before the motor can be started. **Please refer to "Software GUI Reference Guide" section for more information about GUI parameters and the associated system implications.**

Upon successful configuration, the

- the "configuration status" icon in the System Status section of the GUI should change colors from yellow to green
- the "Start" button in Controls section is enabled

NOTE: Remember to press the configure button every time after a change is made to any GUI parameter.

NOTE: The DC bus voltage must remain constant after pressing the configure button, otherwise the control algorithm will not operate as expected. Remember to press the configure button after every time the DC bus level is altered.

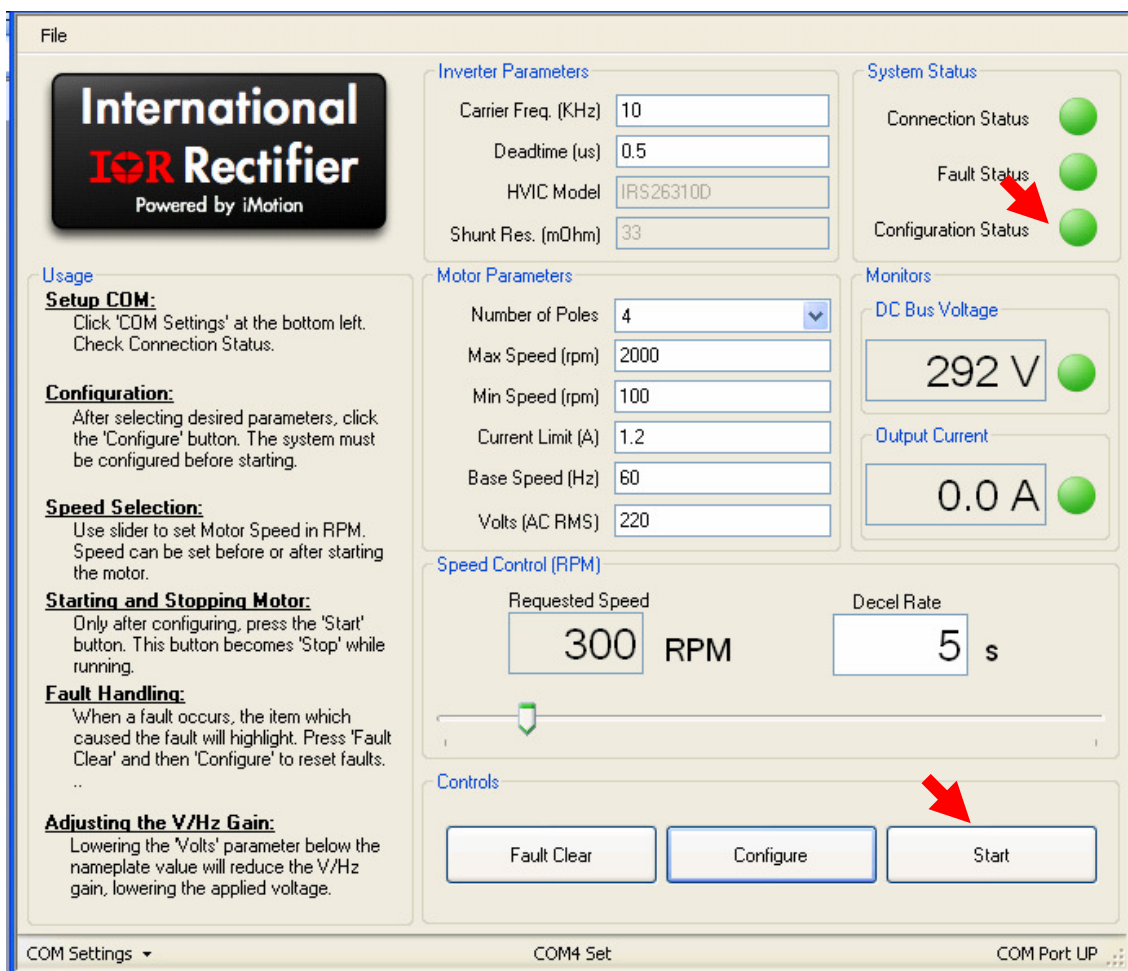


Figure 15: Software GUI after successful configuration of IRMD26310DJ

Step 4d: Starting, Running & Stopping the Motor

- To start the motor, press the “Start” button in the Controls section. The motor will start and accelerate to the specified rotation speed. Further, the “Start” button in the Controls section of the GUI will toggle into a “Stop” button

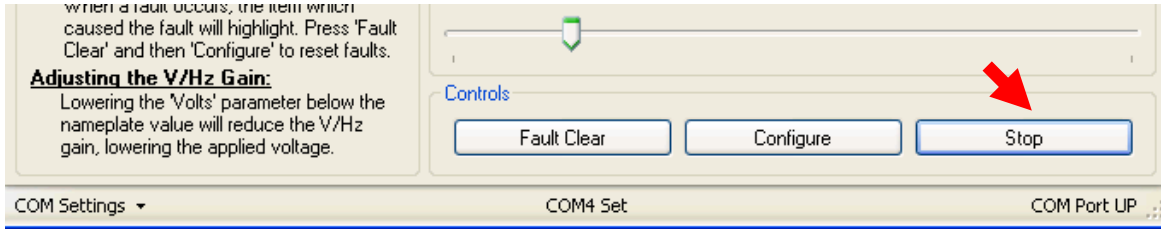


Figure 16: Software GUI while running the motor using IRMD26310DJ

- While running, the motor speed can be changed real-time by using the slide bar to set the new speed.
- To stop the motor, simply press the “Stop” button. The motor will coast to a full stop. Further, the “Stop” button will toggle back to “Start”.

Software GUI Fault Handling Instructions

A fault situation may be triggered either by the software GUI or hardware (digital control IC). **Please refer to the “Software GUI Reference Guide” section for more information about the fault conditions that can be encountered during operation.** In both cases, when a fault is encountered during motor operation, the GUI immediately stops the motor. Further,

- the “Fault Status” icon in System Status section changes to red; In addition, if the fault condition is related to any of the Monitors, then the appropriate Monitor icon also changes color to red
- the “Start” button in Controls Section is disabled
- the fault condition is displayed at the bottom of the GUI

Please refer to “Software GUI Reference Guide” section for a complete list of faults that may be encountered during operation.

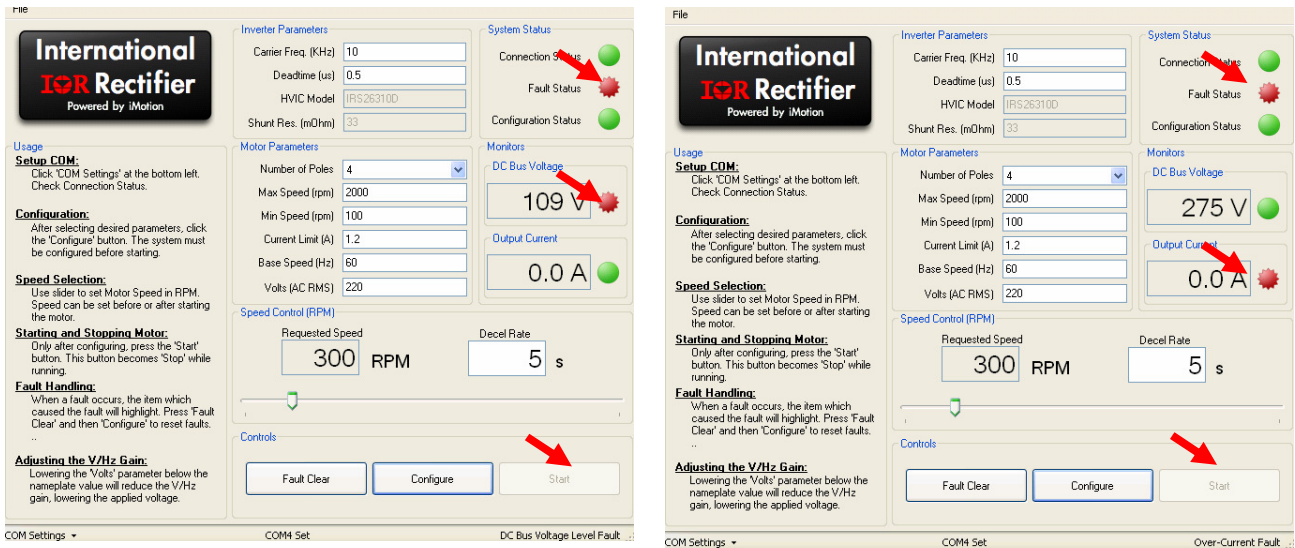


Figure 17: Software GUI during 2 different fault situations

To clear the fault and restart motor, the following actions are performed in the specified sequence

- press the “Fault Clear” button in Controls Section; the “Fault Status” icon & the respective system monitor icon in System Status section then change from red to green
- Reconfigure the motor as outlined in Step 4b
- Proceed to running the motor as outlined in Step 4c

How to Trigger IRS26310DJ DC Bus Overvoltage Protection

The IRS26310D gate driver IC monitors the DC bus voltage to prevent it from charging to an unsafe voltage level during regenerative braking when used to drive permanent magnet motors. The DC bus voltage is scaled down using a resistor divider and communicated to the DCBusSense pin. When the voltage on the DCBusSense pin goes above a maximum threshold ($V_{DCBUSOV+}$) an over-voltage condition is sensed and it forces IRS26310DJ into zero vector mode. In this mode the driver turns-on all low side transistors and turns-off all high side transistors irrespective of the PWM input commands from digital control IC. The divider network R35, R36 and R37 in IRMD26310DJ sets DC bus overvoltage protection level to 386V.

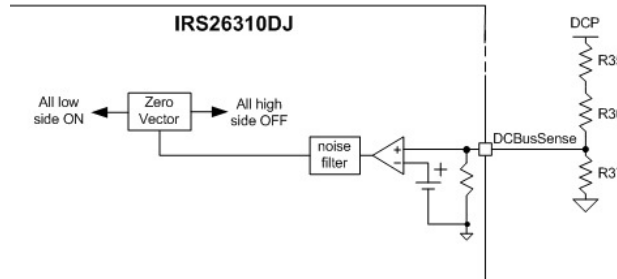


Figure 18: IRS26310 DC Bus Overvoltage Protection Feature

Since IRMD26310DJ will be used to drive an Induction motor, which does not engage regenerative braking like a permanent magnet motor, it is not straightforward to trigger the DC bus overvoltage protection of the driver IC. However, the following suggestions are provided

- i. The Decel Rate parameter in software GUI is used to define the ramp-down characteristic of the induction motor during halting. A smaller value halts the motor more abruptly. By engaging an abrupt stop of the induction motor when running at a high speed under loaded condition, it is possible to create a DC bus voltage overshoot during motor stop. Zero vector insertion is triggered when the overshoot exceeds 386V. **For more information regarding the Decel Rate parameter please refer to “Software GUI Reference Guide” section.** Further, by changing resistor R37, it is also possible to decrease the DC bus overvoltage level at which the protection is triggered to suit other test conditions such as AC input voltage level, motor speed, load-under-test etc. For example, in Fig.19 where zero-vector insertion is activated during motor stop, the reference design was modified to trigger DC Bus overvoltage protection feature at 275V.

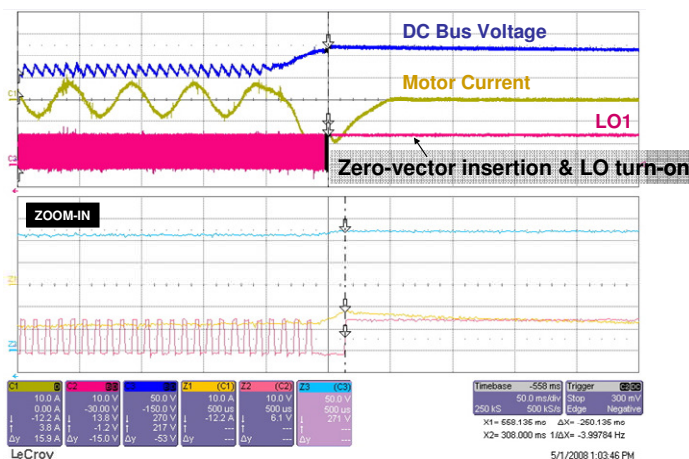


Figure 19: Zero vector insertion during IRS26310DJ DC Bus overvoltage protection

- ii. The DC bus overvoltage feature can also be engaged in a “static” test (motor not running) in order to verify the IC functionality. To do this the AC input voltage level can simply be increased high enough to result in a rectified DC bus voltage greater than 386V; However, in this method, running the motor will not be possible since software GUI will trigger a “DC Bus Voltage Level fault”.

IRMD26310DJ – Software GUI Reference Guide

Motor parameters

- **Number of poles** – The number of poles defines the ratio of the electrical to mechanical frequency in the induction motor (# of poles = Electrical Frequency/Mechanical Frequency). This is selected from the drop down menu in the GUI. Only even number values are listed in the drop down menu since the motor poles come in pairs.
- **Max speed** – This is the maximum speed that the motor can run. The motor speed cannot be set faster than this value. This is entered in revolutions per minute (RPM). This parameter can be obtained from the motor nameplate.
- **Min speed** – This is the minimum speed that the motor can run. The speed cannot be set lower than this value. This is entered in revolutions per minute (RPM).
- **Current Limit** – This is the maximum RMS current (phase current in the motor) that the system will allow in the motor before the GUI triggers a fault and stops the motor. This is entered in amperes (A). This parameter can be obtained from the motor nameplate.
- **Volts** – This is the rated RMS voltage (L-L) of the induction motor. This parameter is the voltage used in the V/Hz control algorithm. This is entered in Volts (V). This parameter can be obtained from the motor nameplate.
NOTE: When the configure button is pressed this value is sampled along with the current DC bus. The DC bus must remain constant after pressing the configure button, otherwise the control algorithm will not operate as expected.
- **Base Speed** – This is the rated electrical frequency for running the induction motor. This parameter is the Hertz value used in the V/Hz control algorithm. This is entered in Hz. This parameter can be obtained from the motor nameplate.

Inverter parameters

- **Carrier frequency** - User selectable input of PWM frequency of inverter. This is entered in KHz in the range from 5kHz to 35kHz.
- **Dead time** – The dead time that will be present between switching high and low side IGBTs. This is entered in μ s. Typically, deadtimes are less than 1usec.

System Status

- **Connection status** – Indicates if the connection between software and IRMCF341 is working and active. Green indicates successful connection, while yellow indicates lack of connection. Other System status icons also remain yellow when there is no connection.
- **Fault Status** – Indicates if any faults are present in the system. Green indicates fault-free status while red indicates occurrence of a fault. The fault condition is displayed at the bottom of the GUI and the associated Monitor icon is also lit red, if applicable. Faults can be cleared by pressing the Fault Clear button. If the fault remains even after pressing the Fault Clear button then it indicates that the underlying fault condition is continuing to persist. Table 3 shows a list of all the faults that can be encountered.
- **Configure Status** – Indicates if any parameters have been modified since the last configure. If this indicator is yellow, the configuration button should be pressed again prior running the motor. It is recommended to press the configuration button every time before starting and running the motor.

Control

- **Fault clear** – This button will clear all of the faults present in the system. If the fault remains even after pressing this button it indicates that the fault situation is continuing to persist. This button will be disabled when a PC-to-hardware connection is not present and when the motor is running. Shut-down power and restart GUI to resume operation.

- **Configure** - This button will configure the hardware with the parameters that have been selected in the GUI. This step is not done automatically. Remember to configure the hardware every time a parameter is altered or DC bus voltage level is changed. This button will be disabled when a PC-to-hardware connection is not present and when the motor is running. Shut-down power and restart GUI to resume operation.
- **Start/Stop** – This toggle-type button starts and stops the motor. During motor-start, an acceleration control gradually ramps the motor speed to the value in “Requested Speed” field.

Fault Name	Fault Type (source)	Explanation of Fault	Fault Trip Level
Over-voltage Fault	Hardware Generated (by Digital IC)	Indicates DC bus over-voltage condition at system power-up (NOTE: this fault occurs only during power up, when GUI is <u>still unconfigured</u>)	DC Bus Voltage > 360V
Under-voltage Fault	Hardware Generated (by Digital IC)	Indicates DC bus voltage under-voltage condition at system power-up (NOTE: this fault occurs only during power up, when GUI is <u>still unconfigured</u>)	DC Bus Voltage < 120V
DC Bus Voltage Level Fault	Hardware Generated (by Digital IC)	Indicates either a DC bus over-voltage or under-voltage condition (NOTE: this fault occurs <u>after GUI has been configured</u>)	DC Bus Voltage > 360V OR DC Bus Voltage < 120V
Gatekill Fault	Hardware generated (by HVIC)	Indicates ITRIP fault or VCC UVLO fault occurring in IRS26310DJ; Gatekill fault is triggered by \overline{FAULT} / EN pin of IRS26310D	VCC < 8.2V (Typ) AND/OR V(R60) > 14A (Typ)
Overcurrent Fault	Software Generated	Indicates that motor current fault	RMS Motor Current calculated by GUI > “Current Limit”
System Error	Software Generated	Indicates out-of-bounds errors, contact IR support	-

Table 3: List of faults issued by software GUI

Monitors

- **DC bus voltage** – The instantaneous voltage of the DC bus. The over-voltage fault is encountered if the DC bus increases beyond 360V. The under-voltage fault is encountered if DC bus is less than 120V.
- **Output current** – The rms motor current calculated by software. It is dependent on the shunt resistor value defined on the system parameter. The over-current fault is encountered if the calculated value exceeds the “Current Limit” parameter.

Speed Control

- The START button in the GUI starts the motor and gradually ramps the motor speed to the specified value in “Requested Speed” field. In addition, the GUI has a slider to adjust the motor if it is already running. An acceleration control is included to gently accelerate to the requested speed and is not user definable.
- **Decel Rate** - DecelRate parameter is used to define the ramp-down characteristic of the motor during halting. This value is entered in s (seconds) between 0.1 and 10. This parameter is manipulated along with the motor speed, load and rectified AC line voltage to simulate DC bus overvoltage condition during motor stop. A smaller Decel Rate value brings the motor to halt faster and will cause a higher voltage overshoot. In the IRMD26310DJ default configuration, when DC bus voltage exceeds 386V, the zero-vector mode of IRS26310DJ is activated.

NOTE: The zero-vector insertion based DC bus overvoltage protection feature is triggered exclusively by IRS26310DJ and not by the digital control IC or software GUI. Hence no specific fault flags are displayed by GUI. However, since DC bus voltage has to exceed 360V before hitting the 386V limit when zero-vector insertion can occur, “DC bus Voltage Level Fault” message is displayed in GUI.

IRMD26310DJ – Circuit Schematics

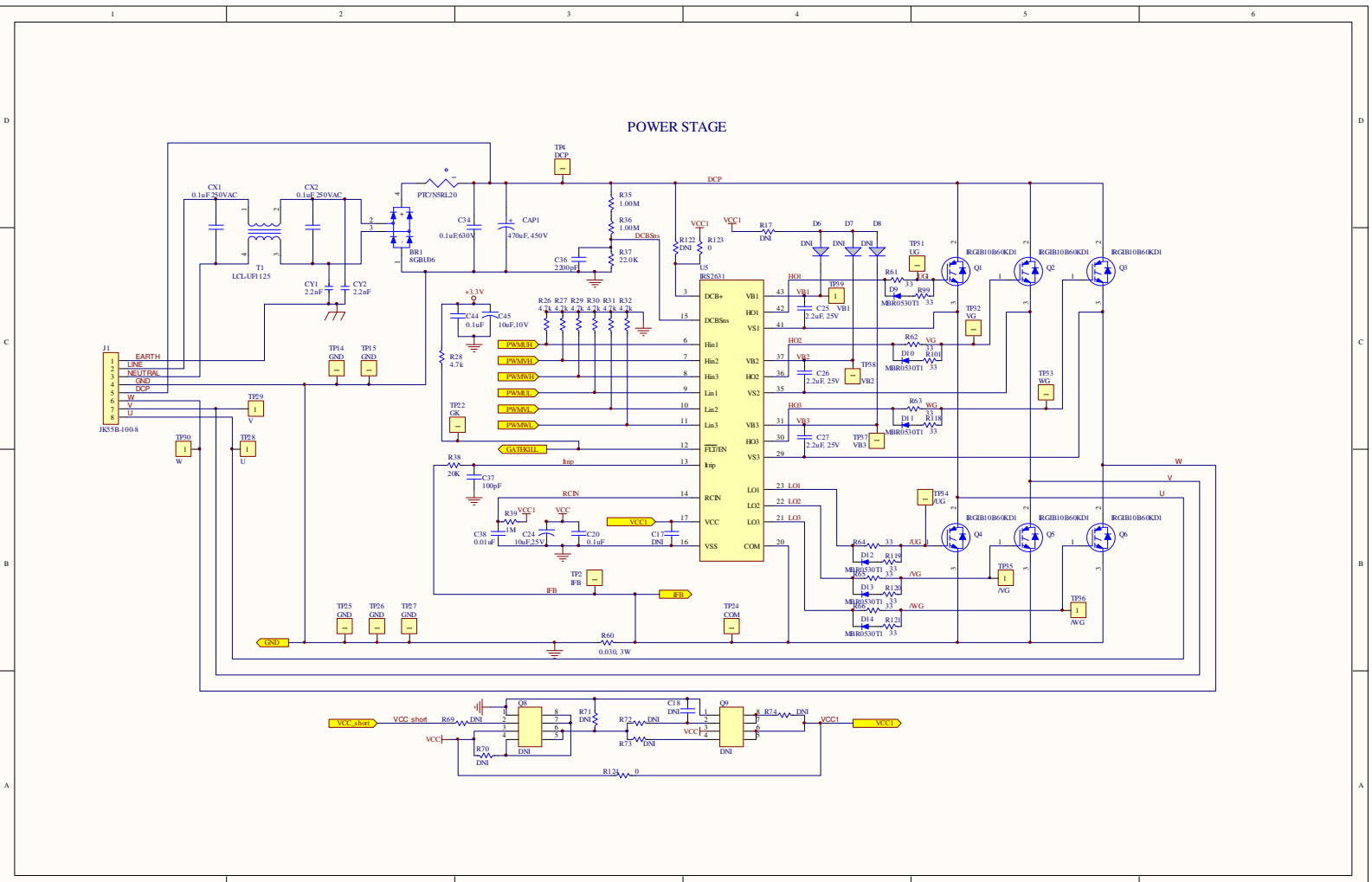


Figure 20- IRMD26310DJ power stage circuit schematic

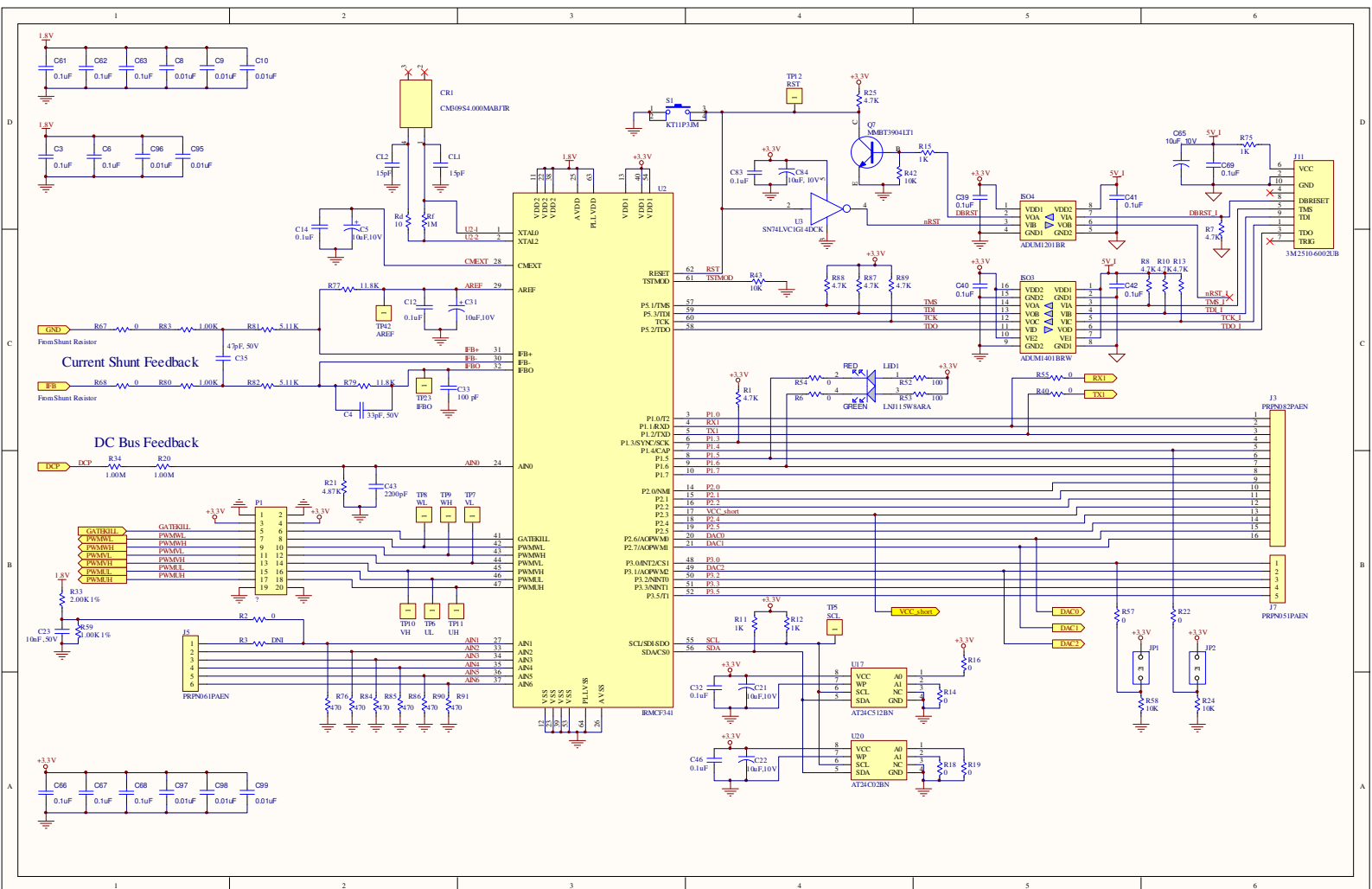


Figure 21 : IRMD26310DJ Digital Control & Communications Circuit Schematic

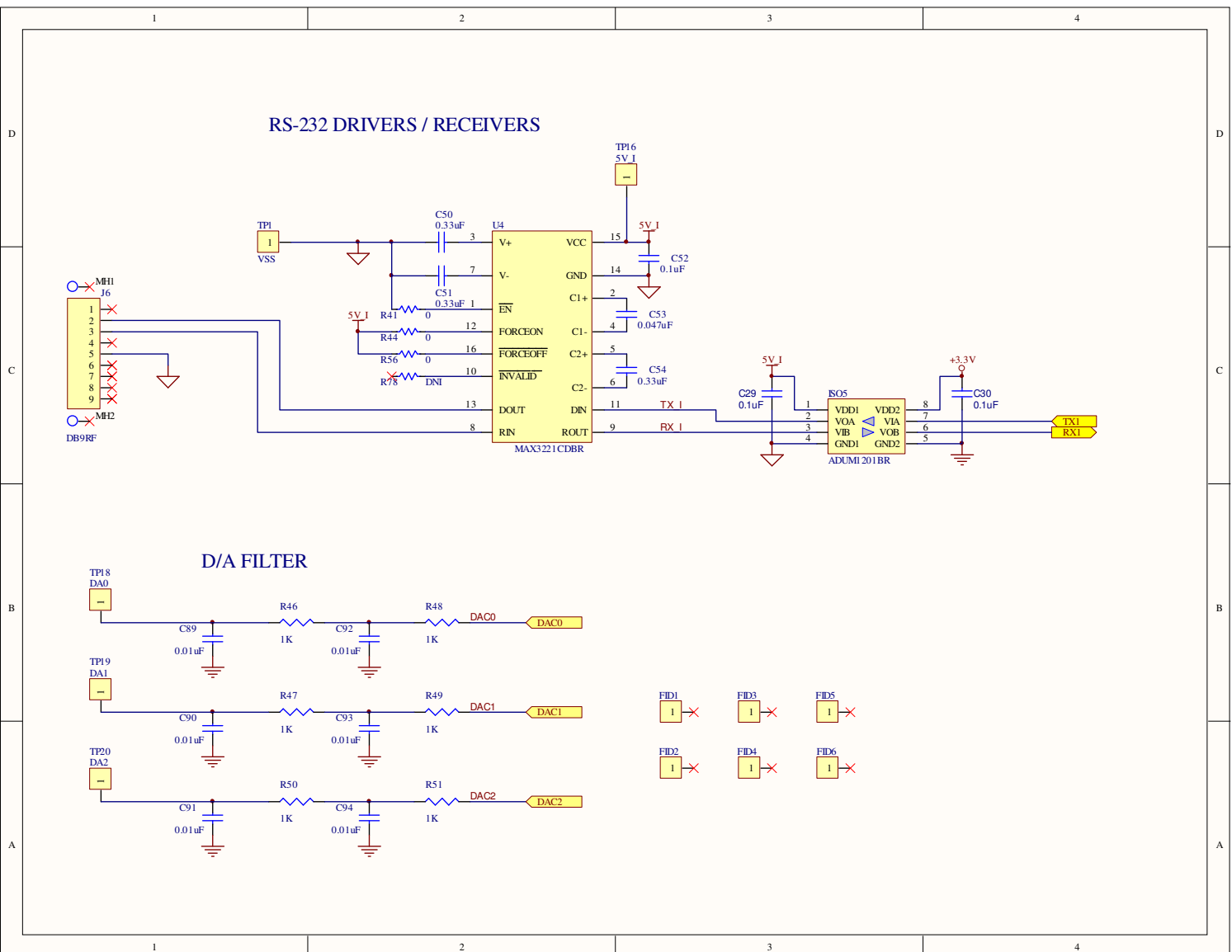


Figure 22: IRMD26310DJ RS-232 Drivers & Receivers Circuit Schematic

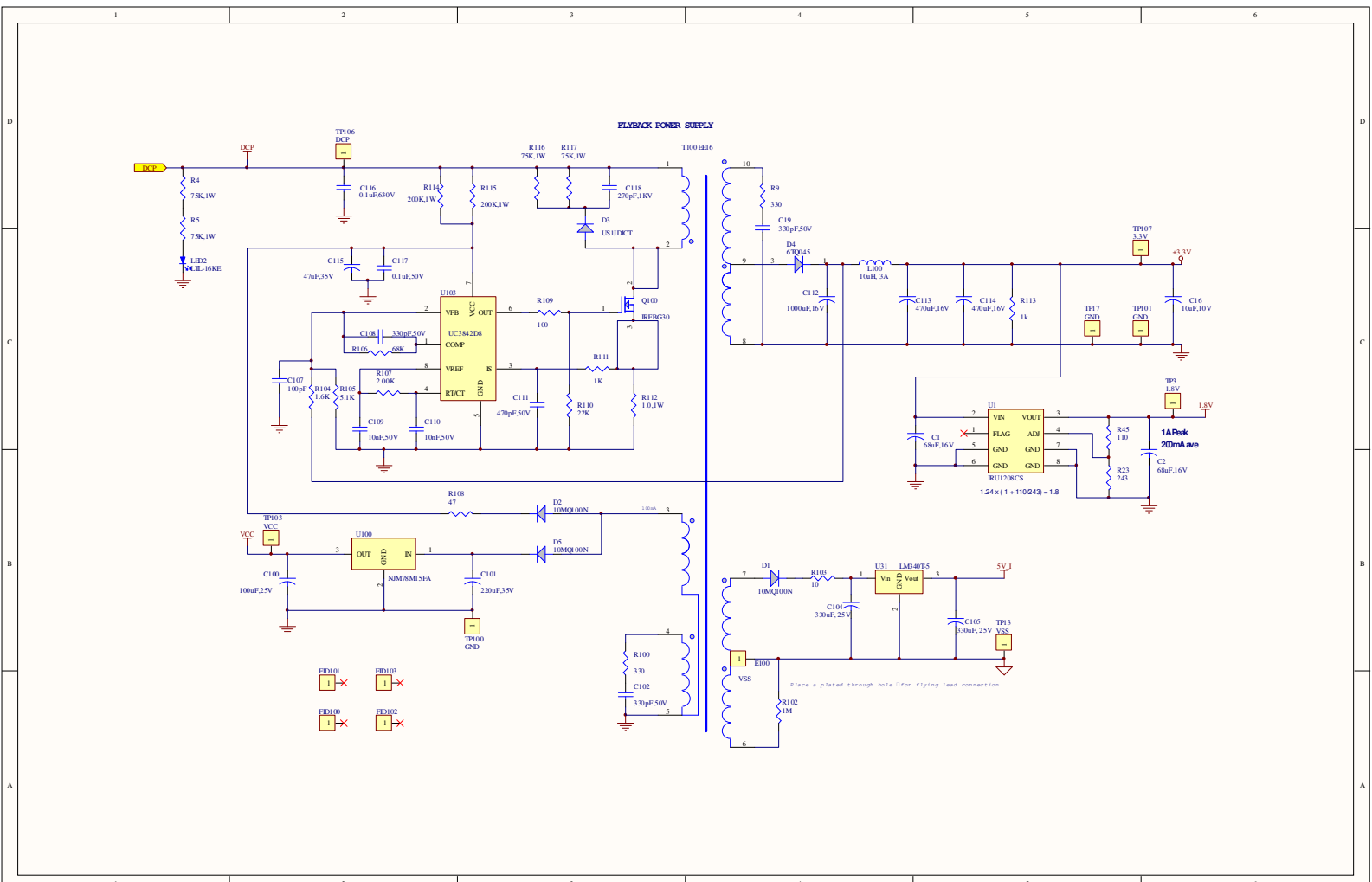


Figure 23: IRMD26310DJ DC-DC Converter Low Voltage Power Supply Circuit Schematic

IRMD26310DJ – Bill of Materials

#	COMPONENT DESCRIPTION	MANUFACTURER & PART#	QTY	DESIGNATOR
1	PCB	PCB, IRMD2631-R1.3	1	
2	CAP THR, 0.1uF 300VAC 20% X1	Panasonic ECQ-U3A104MG DIGIKEY P11116-ND	2	CX1,CX2
3	CAP SMT, 0.1uF 0805 CER 50V 10% X7R		1	C117
4	CAP THR, 0.1uF CER,630V	Panasonic ECQ-E6104KF Digikey EF6104-ND	2	C34,C116
5	CAP SMT, 0.01uF 0603 CER 50V 10% NPO/COG		14	C8-C10,C89-C99
6	CAP SMT, 0.1uF 0603 CER 50V 10% X7R		20	C3,C6,C12,C14,C29,C30,C32,C39-C42,C44,C46,C61-C63,C66-C69
7	CAP SMT, 0.1uF 0805 CER 50V 10% X7R		3	C20,C52,C83
8	CAP SMT, 10000pF 0805 CER 50V 10% X7R		1	C38
9	CAP SMT, 0.33uF 0805 CER 50V 10% X7R		3	C50,C51,C54
10	CAP SMT, 47nF 0805 CER 50V 10% X7R		1	C53
11	CAP THR, 2200PF 250VAC 20% Y2/X1	Panasonic ECK-NVS222ME DIGIKEY P11420CT-ND	2	CY1,CY2
12	CAP SMT, 2.2uF 0805 CER 25V 10% X7R		3	C25-C27
13	CAP SMT, 10000pF 0805 CER 50V 10% X7R		3	C109,C110,C23
14	CAP SMT, 10uF 3216 (A) TANT 16V 10%	KEMET T491A106K016AT T491A106K016AG	8	C5,C16,C21,C22,C31,C45,C65,C84
15	CAP SMT, 10uF 6032 TANT 25V 10%		1	C24
16	CAP SMT, 15pF 0603 CER 50V 5% NPO/COG		2	CL1,CL2
17	CAP SMT, 33pF 0603 CER 50V 5% NPO/COG		1	C4
18	CAP SMT, 47pF 0603 CER 50V 5% NPO/COG		1	C35
19	CAP THR, 47uF, AL ELEC, 35V 2mmLS	DIGIKEY P5164	1	C115
20	CAP SMT, 68uF 7343 TANT 16V 20%		2	C1,C2
21	CAP SMT, 100pF 0603 CER 50V 5% NPO/COG		1	C33
22	CAP SMT, 100pF 0805 CER 50V 5% NPO/COG		2	C37,C107
23	CAP THR, 100uF, AL ELEC, 25V 2mmLS		1	C100
24	CAP THR, CAP 270PF 1KV CERAMIC DISC GP 10%		1	C118
25	CAP THR, 220uF AL ELEC, 35V POL, (0.140"ls/3.5mm)	PANASONIC EEU-FC1V221L DIGI-KEY P10296-ND	1	C101
26	CAP SMT, 330pF 0805 CER 50V 10% X7R		3	C19,C102,C108
27	CAP THA, 330uF, AL ELEC 25V		2	C104,C105
28	CAP SMT, 470pF 0805 CER 50V 10% X7R		1	C111
29	CAP THR, 470uF, AL ELEC 16V 20%		2	C113,C114
30	CAP THR, 1000uF, AL ELEC 16V 20%		1	C112
31	CAP SMT, 2200pF 0805 CER 50V 10% X7R		2	C36,C43
32	CONN, HDR, 2x10 PIN 0.025" SQ POST GOLD (0.100"/0.230")	SAMTEC TSW-110-07-S-D	1	P1
33	CONN, SHUNT, 2-PIN SHORTING SHUNT		10	@P1
34	CAP THR, 470uF,ELEC 450V 20%	Panasonic EET-UQ2W471DA DIGIKEY P11951-ND	1	CAP1
35	RES SMT, 0-OHM 1/8W 5% 0805		18	R2,R6,R14,R16,R18,R19,R22,R40,R41,R44,R54,R55,R56,R57,R67,R68,R123,R124
36	RES SMT, 0.03-OHM 3W 1% 1225		1	R60
37	RES SMT, 1-OHM 1W 5% 2512		1	R112
38	RES SMT, 1.0K-OHM 1/8W 1% 0805		15	R59,R80,R83,R11,R12,R15,R46-R51,R75,R111,R113
39	RES SMT, 1.0M-OHM 1/8W 1% 0805		4	R20,R34,R39,Rf
40	RES SMT, 1M-OHM 1/4W 1% 1206		3	R35,R36,R102