imall

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



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ISO1BFRGB

Version (1)

DISTINCTIVE CHARACTERISTICS

Enhanced LED Illumination with:

- Broad and even light distribution
- Consistent bright backlighting
- Lower energy consumption

Programmable to display graphics, alphanumeric characters and animated sequences.

SMARTDISPLAY can be used alone or in conjunction with electromechanical switches.

Integrated liquid crystal display provides wide viewing angle with high contrast and clarity.

Viewing area 13.9mm x 10.6mm (horizontal x vertical) at 36 x 24 pixels.

Epoxy sealed terminals prevent entry of solder flux and other contaminants.

Optional accessories available to enhance panel design and simplify production process.

Built-in RGB LED backlighting enhances display and provides numerous color variations.



PRECAUTIONS FOR HANDLING & STORAGE

Handling

- 1. The VLC voltage should not be applied before logic voltage. If VLC voltage is present before logic voltage, it may cause the driver logic to freeze and damage the LCD, and the driver logic itself may become damaged.
- 2. The IS Series devices are electrostatic sensitive.
- 3. Recommended soldering time and temperature limits are 5 seconds maximum @ 270°C maximum.
- 4. Do not exceed 60°C at the LCD level.
- 5. The IS series devices are not process sealed.
- 6. If the LCD is accidentally broken, avoid contact with the liquid and wash off any liquid spills to the skin or clothing.
- 7. Clean cap surface with dry cloth. If further cleaning is needed, wipe with dampened cloth using neutral cleanser and dry with clean cloth. Do not use organic solvent.

Storage

- 1. Store away from direct sunlight.
- 2. Keep away from static electricity.
- 3. Avoid extreme temperatures, high humidity, gaseous substances, and all forms of chemical contamination.



SMARTDISPLAY

LCD SPECIFICATIONS

Characteristics of Display

Display Operation Mode	FSTN positive
Display Condition	Transflective with built-in LED backlight
Viewing Angle	Adjustable
Driving Method	1/24 duty. 1/5 bias (built-in driving circuit)
Viewing Area	13.9mm x 10.6mm (horizontal x vertical)
Pixel Format	36 x 24 dots (horizonal x vertical)
Pixel Size	0.32mm x 0.32mm (horizontal x vertical)
Operating Temperature Range	0°C through 40°C (32°F through 104°F)
Storage Temperature Range	–10°C through 60°C (14°F through 140°F)
Backlight LED	Red/Green/Blue



RGB LED with Black & White LCD Mode

Absolute Maximum Ratings (Temperature at 25°C) Items Symbols Ratings

 Supply Voltage for Logics
 V_{DD}
 -0.3V to
 +7.0V

 Supply Voltage for LCD
 V_{LC}
 -0.3V to
 +12.0V

 Input Voltage
 V₁
 -0.3V to
 V_{DD}+0.3V

 Output Voltage
 V₀
 -0.3V to
 V_{DD}+0.3V

Recommended Operating Conditions (Temperature at 25°C)

· ·			•
Symbols	Minimum	Typical	Maximum
V _{DD}	4.5V	5.0V	5.5V
V_{LC}		7.3V	
V	0V		V _{DD}
f _{flm}		150Hz	
	V _{DD} V _{LC} V _I	$ \begin{array}{c} V_{DD} & 4.5V \\ V_{LC} & \\ V_{I} & 0V \\ f $	$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$

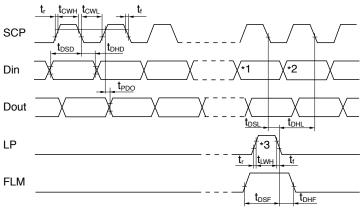
DC Characteristics of LCD Drive IC (Temperature at 0°C to 40°C and $V_{DD} = 5.0V \pm 10\%$)

ltems	Symbols	Test Conditions	Minimum	Typical	Maximum	Unit
High Level Input Voltage	V _{IH}		0.7V _{DD}		V _{DD}	V
Low Level Input Voltage	V _{IL}		0		0.3 V _{DD}	٧
High Level Input Leakage Current	I _{UH}	$V_1 = V_{DD}$			10	μA
Low Level Input Leakage Current	I _{LIL}	$V_1 = 0V$			-10	μA
High Level Output Voltage	V _{OH}	I _{он} = -500µА	V _{DD} -0.5			V
Low Level Output Voltage	V _{OL}	I _{OL} = 500μA			0.5	V
High Level Output Leakage Current	I _{LOH}	$V_{O} = V_{DD}$			10	μA
Low Level Output Leakage Current	ILOL	$V_{o} = 0V$			-10	μA
Supply Current	I _{DD}	$f_{SCP} = 1.0MHz$			500	μA
LCD Drive Current	I _{LC}	$f_{LP} = 2.4 \text{kHz} \text{ V}_{LC} = 7.3$	8V	500	2,000	μA

Timing Characteristics of LCD Drive IC

(Temperature at 0°C to 40°C and $V_{DD} = 5.0V \pm 10\%$)					
Symbols	Minimum	Maximum			
f_{scp}		6.0MHz			
f _{LP}		50kHz			
t _{CWH}	70ns				
t _{CWL}	70ns				
t _{DSD}	45ns				
t _{DHD}	50ns				
t _{PDO}		25ns			
t _{DSL}	50ns				
t _{DHL}	50ns				
t _{LWH}	200ns				
t _{DSF}	50ns				
t _{DHF}	50ns				
t _r /t _f		15ns			
	Symbols fscP fLP tCWH tCWL tDDD tDHD tPDO tDSL tDHL tLWH tDSF tDHL	Symbols Minimum f _{SCP} f _{LP} 70ns t _{CWH} 70ns t _{CWL} 70ns t _{DSD} 45ns t _{DHD} 50ns t _{DSL} 50ns t _{DHL} 50ns t _{DHL} 50ns t _{DSL} 50ns t _{DHL} 50ns t _{DHL} 50ns t _{DHL} 50ns t _{DHL} 50ns			

Timing Diagram

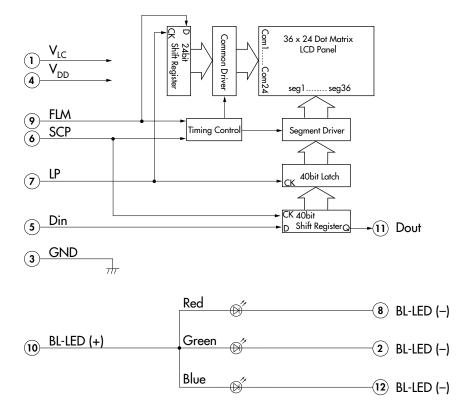


- *1 Last data on first line
- *2 Beginning data on second line
- *3 Location of LP signal on first line

SMARTDISPLAY



BLOCK DIAGRAM & PIN CONFIGURATIONS



<u>Pin No</u>	<u>. Symbol</u>	Name	Function
\bigcirc	VLC	Power	Power source for LCD drive
2	BL-LED ()	Terminal of Backlight LED	Cathode for green
3	GND	Ground	
(ď	V_{dd}	Power	Power source for logic circuit
5	Din	Data Input	Display serial data bit. Note: to map the display data, because of the difference between the number of internal shift register data (40) and the single line of LCD pixels (36), the first four bits of data shifted will be dummy bits.
6	SCP	Serial Clock Pulse	Clock used by 40-bit internal shift register of the switch, shifting the display data bit presented at Din at falling edge.
7	LP	Latch Pulse	Line data latch pulse will latch content of internal 40-bit shift register at falling edge for one line of display. LP will also increment the display line by one.
8	BL-LED ()	Terminal of Backlight LED	Cathode for red
9	FLM	First Line Marker	The marking signal for the first line data of LCD display. The first line of LCD will be selected by the falling edge of LP signal during the high level (FLM).
10	BL-LED (+)	Terminal of Backlight LED	Anode for common
1	Dout	Data Output	Display serial output. Can be used to connect to Din of the next SMARTDISPLAY. As a result, many SMARTDISPLAYS can be controlled with one clock and data signal.
12	BL-LED ()	Terminal of Backlight LED	Cathode for blue



SUPER BRIGHT RGB LED SPECIFICATIONS

Typical Electrical Characteristics (Temperature at 25°C)

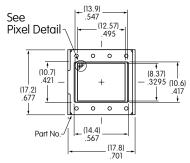
Backlight Color	Symbols	Red	Green	Blue	Unit
Forward Current	I _F	10	10	10	mA
Forward Voltage	V _F	2.1	3.3	3.3	V

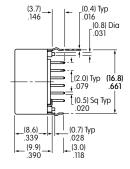
ABSOLUTE MAXIMUM RGB LED

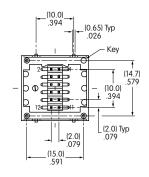
Electrical Characteristics (Temperature at 25°C)

Backlight Color	Symbols	Red/Green/Blue	Unit			
Forward Current	I _F	20	mA			
Reverse Voltage	V _R	4.0	V			
Current Reduction Rate Above 25°C $\Delta I_F(DC)$ -0.27 $mA/°C$						
Power Dissipation*	P _D	130 maximum	mW			
*For uniform light emission, Power Dissipation should not exceed the Absolute Maximum Rating.						

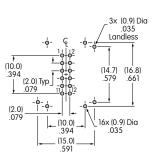
TYPICAL DISPLAY DIMENSIONS







Terminal numbers are not on the device.



Footprint



