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ISL9K18120G3

18A, 1200V Stealth™ Dual Diode

General Description

The ISL9K18120G3 is a StealthTM dual diode optimized for low loss performance in high frequency hard switched applications. The StealthTM family exhibits low reverse recovery current ($I_{RM(REC)}$) and exceptionally soft recovery under typical operating conditions.

This device is intended for use as a free wheeling or boost diode in power supplies and other power switching applications. The low $I_{RM(REC)}$ and short t_a phase reduce loss in switching transistors. The soft recovery minimizes ringing, expanding the range of conditions under which the diode may be operated without the use of additional snubber circuitry. Consider using the Stealth diode with a 1200V NPT IGBT to provide the most efficient and highest power density design at lower cost

Formerly developmental type TA49414.

Features

•	Soft Recovery $t_b / t_a > 5.0$
•	Fast Recovery t_{rr} < 45ns
•	Operating Temperature
•	Reverse Voltage

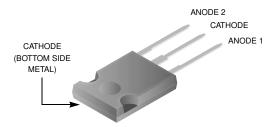
· Avalanche Energy Rated

Applications

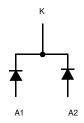
- · Switch Mode Power Supplies
- · Hard Switched PFC Boost Diode
- · UPS Free Wheeling Diode
- · Motor Drive FWD
- SMPS FWD
- · Snubber Diode

Package

JEDEC STYLE TO-247



Symbol



Device Maximum Ratings (per leg) T_C = 25°C unless otherwise noted

Symbol	Parameter	Ratings	Units
V_{RRM}	Repetitive Peak Reverse Voltage	1200	V
V _{RWM}	Working Peak Reverse Voltage	1200	V
V _R	DC Blocking Voltage	1200	V
I _{F(AV)}	Average Rectified Forward Current (T _C = 92°C) Total Device Current (Both Legs)	18 36	A A
I _{FRM}	Repetitive Peak Surge Current (20kHz Square Wave)	36	Α
I _{FSM}	Nonrepetitive Peak Surge Current (Halfwave 1 Phase 60Hz)	200	Α
P_{D}	Power Dissipation	125	W
E _{AVL}	Avalanche Energy (1A, 40mH)	20	mJ
T _J , T _{STG}	Operating and Storage Temperature Range	-55 to 150	°C
TL	Maximum Temperature for Soldering		
T_{PKG}	Leads at 0.063in (1.6mm) from Case for 10s	300 260	°C
	Package Body for 10s, See Application Note AN-7528	200	

CAUTION: Stresses above those listed in "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress only rating and operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied.

	,	ng and Ordering I						
	Marking	Device	Package	Tape Width			Quar	
K181	20G3	ISL9K18120G3	TO-247	N/A			30	
Electric	cal Char	acteristics (per leg)	T _C = 25°C unles	s otherwise noted				
Symbol Parameter		Test Conditions		Min	Тур	Max	Units	
Off State	Charact	eristics						
I _R	Instantane	ous Reverse Current	V _R = 1200V	T _C = 25°C	-	-	100	μΑ
				T _C = 125°C	-	-	1.0	mA
On State	Charact	eristics						
V _F	Instantaneous Forward Voltage	I _F = 18A	T _C = 25°C	-	2.7	3.3	V	
•		· ·		T _C = 125°C	-	2.5	3.1	V
Dynamic	Charact	eristics						
CJ	Junction C	apacitance	$V_R = 10V, I_F = 0A$		-	69	-	pF
Switchin	g Charac	eteristics						
t _{rr}	t _{rr} Reverse Recovery Time		$I_F = 1A$, $dI_F/dt = 100A/\mu s$, $V_R = 30V$		-	38	45	ns
			$I_F = 18A$, $dI_F/dt = 100A/\mu s$, $V_R = 30V$		-	60	70	ns
t _{rr}	Reverse R	ecovery Time	I _F = 18A,		-	300	-	ns
I _{RM(REC)} Maximum Reverse Recovery Current		$dI_F/dt = 200A/\mu s$,		-	6.5	-	Α	
Q _{RR} Reverse Recovered Charge		$V_R = 780V, T_C = 25^{\circ}C$		-	950	-	nC	
t _{rr}	Reverse R	ecovery Time	I _F = 18A,		-	400	-	ns
S	7001/		-	7.0	-	-		
I _{RM(REC)} Maximum		Reverse Recovery Current	$V_R = 780V,$ $T_C = 125^{\circ}C$		-	8.0	-	Α
Q_{RR}	Reverse R	ecovered Charge	71 _C = 123 C		-	2.0	-	μC
t _{rr}	Reverse R	ecovery Time	$\begin{aligned} &I_{F} = 18A, \\ &dI_{F}/dt = 1000A/\mu s, \\ &V_{R} = 780V, \\ &T_{C} = 125^{\circ}C \end{aligned}$		-	235	-	ns
S	Softness F	actor (t _b /t _a)			-	5.2	-	-
I _{RM(REC)}	Maximum	Reverse Recovery Current			-	22	-	Α
Q _{RR}	Reverse R	ecovered Charge			-	2.1	-	μС
dl _M /dt	Maximum	di/dt during t _b			-	370	-	A/μs
Thermal	Characte	eristics						
$R_{\theta JC}$	Thermal R	esistance Junction to Case	TO-247		-	-	1.0	°C/W
R _{θ,JA}	Thormal D	esistance Junction to Ambien	ient TO-247			1	30	°C/W

Typical Performance Curves (per leg)

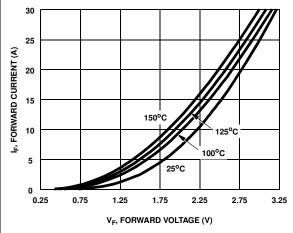


Figure 1. Forward Current vs Forward Voltage

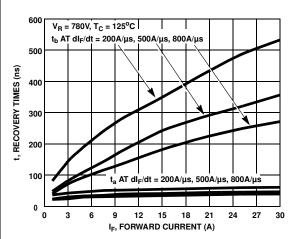


Figure 3. t_a and t_b Curves vs Forward Current

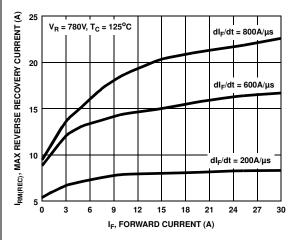


Figure 5. Maximum Reverse Recovery Current vs Forward Current

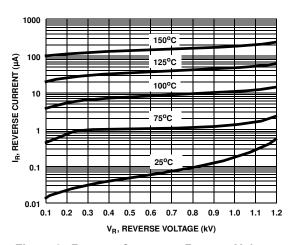


Figure 2. Reverse Current vs Reverse Voltage

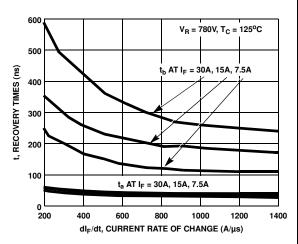


Figure 4. t_a and t_b Curves vs dl_F/dt

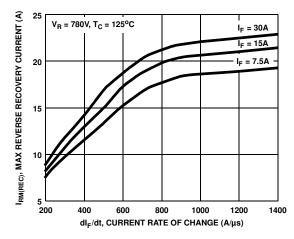
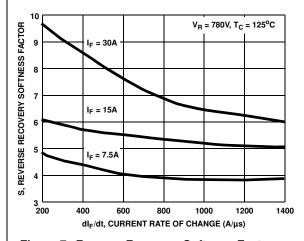


Figure 6. Maximum Reverse Recovery Current vs dI_F/dt

Typical Performance Curves (per leg) (Continued)



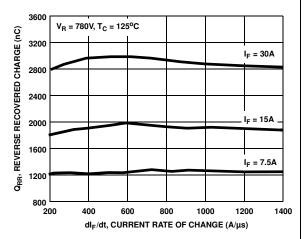
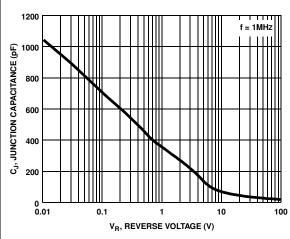


Figure 7. Reverse Recovery Softness Factor vs $\mathrm{dI_F/dt}$

Figure 8. Reverse Recovered Charge vs dI_F/dt



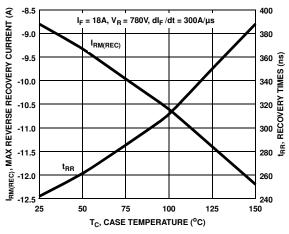


Figure 9. Junction Capacitance vs Reverse Voltage

Figure 10. Reverse Recovery Current and Times vs Case Temperature

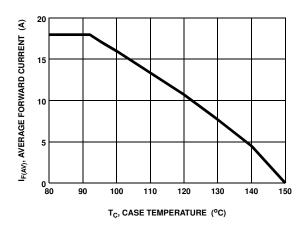


Figure 11. DC Current Derating Curve

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Typical Performance Curves (per leg) (Continued)

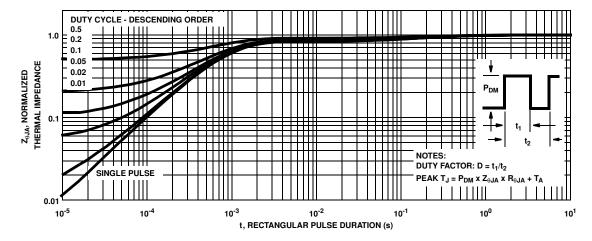
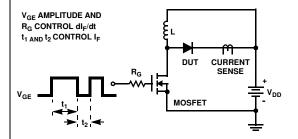


Figure 12. Normalized Maximum Transient Thermal Impedance

Test Circuit and Waveforms



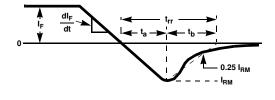
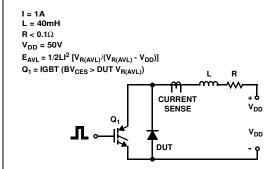


Figure 13. t_{rr} Test Circuit

Figure 14. t_{rr} Waveforms and Definitions



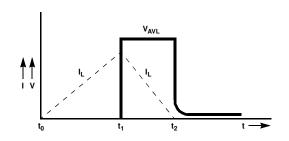


Figure 15. Avalanche Energy Test Circuit

Figure 16. Avalanche Current and Voltage Waveforms

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