imall

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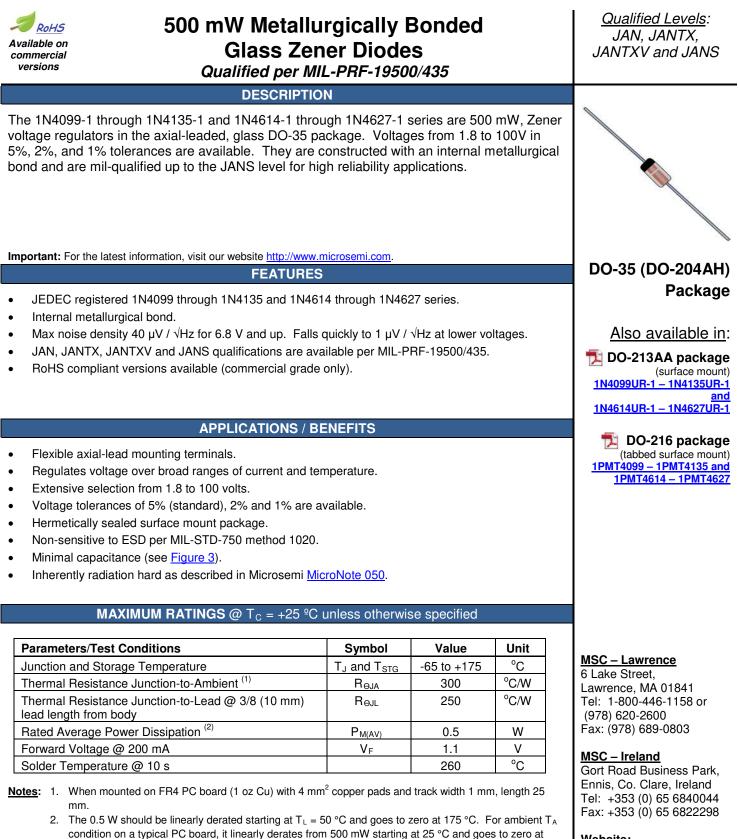


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Website: www.microsemi.com

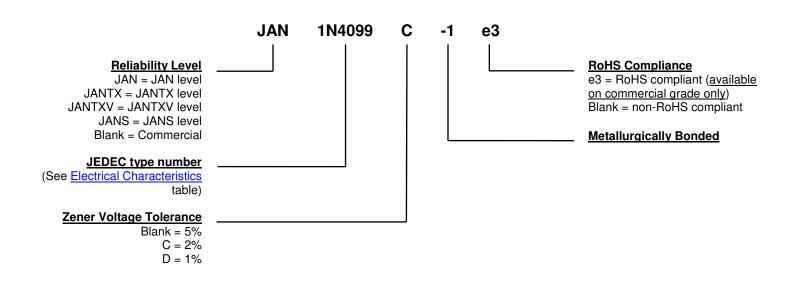
175 °C (see Figure 2).



MECHANICAL and PACKAGING

- CASE: Hermetically sealed axial-lead glass DO-35 (DO-204AH) style package.
- TERMINALS: Tin-lead or RoHS compliant annealed matte-tin (on commercial grade only) plating. Solderable per MIL-STD-750, method 2026.
- POLARITY: Cathode indicated by band. The diode is to be operated with the banded end positive with respect to the opposite end for Zener regulation.
- MARKING: Part number.
- TAPE & REEL option: Standard per EIA-296 (add "TR" suffix to part number). Consult factory for quantities.
- WEIGHT: Approximately 0.2 grams.
- See <u>Package Dimensions</u> on last page.

PART NOMENCLATURE



SYMBOLS & DEFINITIONS						
Symbol	Definition					
ανΖ	Temperature Coefficient of Regulator Voltage: The change in regulator voltage divided by the change in temperature that caused it expressed in %/C or mV/°C.					
I _R	Reverse Current: The maximum reverse (leakage) current that will flow at the specified voltage and temperature.					
I_Z, I_{ZT}, I_{ZK}	Regulator Current: The dc regulator current (I_Z), at a specified test point (I_{ZT}), near breakdown knee (I_{ZK}).					
I _{ZM}	Maximum Regulator (Zener) Current: The maximum rated dc current for the specified power rating.					
ND	Noise Density: The noise generated over a specified frequency bandwidth usually specified in terms of mV/ \sqrt{Hz} .					
V _R	Reverse Voltage: The reverse voltage dc value, no alternating component.					
Vz	Zener Voltage: The Zener voltage the device will exhibit at a specified current (Iz) in its breakdown region.					
Z_{ZT} or Z_{ZK}	Dynamic Impedance: The small signal impedance of the diode when biased to operate in its breakdown region at a specified rms current modulation (typically 10% of I_{ZT} or I_{ZK}) and superimposed on I_{ZT} or I_{ZK} respectively.					



		ELECI		ARACTERIS	11 6 3 @	25 °C u	mess otherw	nse stated	
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1N4128-1 60.0 250 400 0.01 45.6 40 6.4 +0.100 1N4129-1 62.0 250 500 0.01 47.1 40 6.1 +0.100 1N4130-1 68.0 250 700 0.01 51.7 40 5.6 +0.100 1N4131-1 75.0 250 700 0.01 57.0 40 5.1 +0.100 1N4132-1 82.0 250 800 0.01 62.4 40 4.6 +0.100 1N4133-1 87.0 250 1000 0.01 66.2 40 4.4 +0.100 1N4134-1 91.0 250 1200 0.01 69.2 40 4.2 +0.100									
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ELECTRICAL CHARACTERISTICS @ 25 °C unless otherwise stated

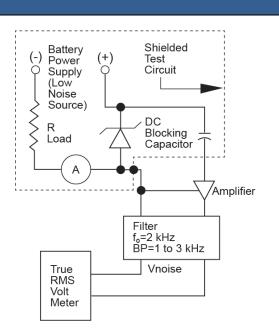
*JEDEC Registered Data.

SEE NOTES ON NEXT PAGE.



- **NOTE 1:** The JEDEC type numbers shown in the prior table have a standard tolerance of +/-5% on the nominal Zener voltage. V_Z is measured with the diode in thermal equilibrium (still air) at 25 °C.
- NOTE 2: Zener impedance is derived by superimposing on I_{ZT} a 60 Hz rms ac current at 10% of I_{ZT} (25 μA). See MicroNote 202 for Zener impedance variation with different operating currents.
- **NOTE 3:** Based upon 400 mW maximum power dissipation at 25 °C lead temperature, allowance has been made for the higher voltage associated with operation at higher currents.

GRAPHS



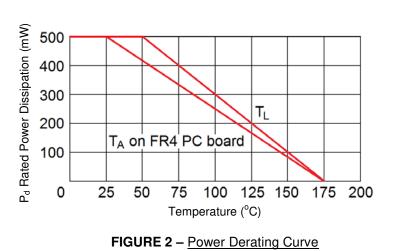


FIGURE 1 - Noise Density Measurement Circuit

Noise density, (N_D) is specified in microvolt-rms per square-root-hertz. Actual measurement is performed using a 1 KHz to 3 KHz frequency bandpass filter at a constant Zener test current (I_{ZT}) at 25 °C ambient temperature. N_D is calculated from the formula.

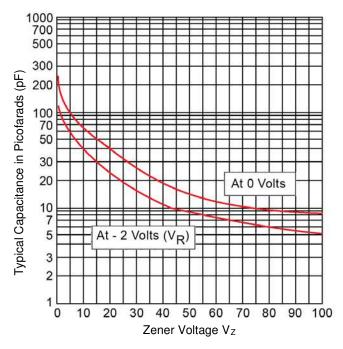
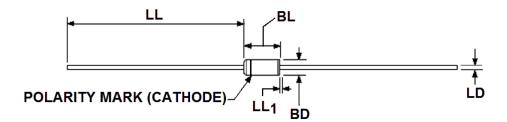


FIGURE 3 – Capacitance vs. Zener Voltage (Typical)



PACKAGE DIMENSIONS



Ltr	Inc	hes	Millin	Notes	
	Min	Max	Min	Max	
BD	0.056	0.090	1.42	2.29	3
BL	0.140	0.200	3.56	5.08	3
LD	0.018	0.022	0.46	0.56	
LL	1.000	1.500	25.40	38.10	
LL ₁	-	0.050	-	1.27	4

NOTES:

- 1. Dimensions are in inches.
- 2. Millimeters are given for information only.
- Package contour optional within BD and length BL. Heat slugs, if any, shall be included within this cylinder but shall not be subject to minimum limit of BD. The BL dimension shall include the entire body including slugs.
- 4. Within this zone lead, diameter may vary to allow for lead finishes and irregularities other than heat slugs.
- 5. In accordance with ASME Y14.5M, diameters are equivalent to Φx symbology.