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Silicon Zener Diode Series

1N957BUR-1 thru 1N986BUR-1

Features

- Available in JAN, JANTX and JANTXV per MIL-PRF-19500/117
- Standard voltage tolerences are plus/minus 5% with B suffix, 10% with A suffix identification.
- Tight tolerences available in plus or minus 2% or 1% with C or D suffix respectively.
- 500 mW power handling
- Hermetically sealed axial-leaded glass DO-213AA package.

Maximum Ratings

Operating & Storage Temperature: -65°C to +175°C

Thermal Resistance: 250°C/W Steady-State Power: 0.5 watts

Foward Voltage @200 MA: 1.1 V for 1N957BUR-1 - 1N986BUR-1

NEW

A passion for performance.





Electrical Specifications @ +25 °C (Unless Otherwise Specified)

JEDEC TYPE	Normal Zener Voltage	Zener Test Current	Maximum Zener Impedance			Maximum DC Zener Current	Maxmum Surge Current	Maximum Reverse Leakage		Maximum Temperature Coefficient
Number	V_{z}	l _{ZT}	Z _{ZT @} I _{ZT}	Z _{ZK} @ I _{ZK}		^I ZM	I _{ZSM}	I _{R @} V _R		α_{VZ}
(Note 1)	Volts	mA	Ohms	Ohms	mA	mA	mA	μА	Volts	%/°C
1N957BUR-1 1N958BUR-1 1N959BUR-1 1N960BUR-1 1N961BUR-1	6.8 7.5 8.2 9.1 10	18.5 16.5 15.0 14.0 12.5	4.5 5.5 6.5 7.5 8.5	700 700 700 700 700	1.0 .5 .5 .5	55 50 45 41 38	300 275 250 225 200	150 75 50 25 10	5.2 5.7 6.2 6.9 7.6	+0.05 +0.058 +0.065 +0.068 +0.075
1N962BUR-1 1N963BUR-1 1N964BUR-1 1N965BUR-1 1N966BUR-1	11 12 13 15	11.5 10.5 9.5 8.5 7.8	9.5 11.5 13 16	700 700 700 700 700 700	.25 .25 .25 .25 .25	32 31 28 25 24	175 160 150 130 120	5 5 5 5 5	8.4 9.1 9.9 11.4 12.2	+0.076 +0.077 +0.079 +0.082 +0.083
1N967BUR-1 1N968BUR-1 1N969BUR-1 1N970BUR-1 1N971BUR-1	18 20 22 24 27	7.0 6.2 5.6 5.2 4.6	21 25 29 33 41	750 750 750 750 750	.25 .25 .25 .25 .25	20 18 16 15 13	110 100 90 80 70	5 5 5 5 5	13.7 15.2 16.7 18.2 20.6	+0.085 +0.086 +0.087 +0.088 +0.090
1N972BUR-1 1N973BUR-1 1N974BUR-1 1N975BUR-1 1N976BUR-1	30 33 36 39 43	4.2 3.8 3.4 3.2 3.0	49 58 70 80 93	1000 1000 1000 1000 1000	.25 .25 .25 .25 .25	12 11 10 9.5 8.8	65 60 55 46 44	5 5 5 5 5	22.8 25.1 27.4 29.7 32.7	+0.091 +0.092 +0.093 +0.094 +0.095
1N977BUR-1 1N978BUR-1 1N979BUR-1 1N980BUR-1 1N981BUR-1	47 51 56 62 68	2.7 2.5 2.2 2.0 1.8	105 125 150 185 230	1500 1500 2000 2000 2000	.25 .25 .25 .25 .25	7.9 7.4 6.8 6.0 5.5	40 37 35 30 28	5 5 5 5 5	35.8 38.8 42.6 47.1 51.7	+0.095 +0.096 +0.096 +0.097 +0.097
1N982BUR-1 1N983BUR-1 1N984BUR-1 1N985BUR-1 1N986BUR-1	75 82 91 100 110	1.7 1.5 1.4 1.3 1.1	270 330 400 500 750	2000 3000 3000 3000 4000	.25 .25 .25 .25 .25	5.0 4.6 4.1 3.7 3.3	26 23 21 18 16	5 5 5 5 5	56.0 62.2 69.2 76.0 83.6	+0.098 +0.098 +0.099 +0.11 +0.11

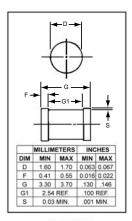
NOTE 1: The JEDEC type numbers shown (B Suffix) have a $\pm 5\%$ tolerence on nominal Zener Votlage. The suffix A is used to identify $\pm 10\%$ tolerance; suffix C is used to identify $\pm 2\%$: and suffix D is used identify $\pm 1\%$; no suffix indicates $\pm 20\%$.







Outline Drawing



LEADED DESIGN DATA

CASE: DO-213AA, Hermetically sealed glass case. (MELF, SOD-80, LL34)

LEAD FINISH: Tin / Lead

THERMAL RESISTANCE: $(R_{\Theta,JFC})$: 100 °C/W maximum at L = 0 inch

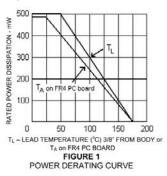
THERMAL IMPEDANCE: (Z_{O.JX}): 25 °C/W maximum

POLARITY: Diode to be operated with the banded (cathode) end positive.

MOUNTING POSITION: Any.

MOUNTING SURFACE SELECTION: The Axial Coefficient of Expansion (COE) Of this Device is Approximately +6 PPM/°C. The COE of the Mounting Surface System Should Be Selected To Provide A Suitable Match With This Device.

Graphs



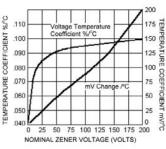
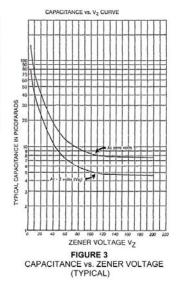


FIGURE 2
ZENER VOLTAGE TEMPERATURE
COEFFICIENT vs. ZENER VOLTAGE



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