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JTAGjet[™]- Trace for Cortex-M series with Real-Time ETM Trace

JTAGjet-Trace-CM is a small, palm-sized In-Circuit Debugger (commonly called emulator) used for debugging, testing and verification of embedded systems based on the Cortex-M devices. It is equipped with ETM trace memory that captures the compressed data on the ETM port in real-time so it can be decompressed, decoded and displayed on the PC showing the program execution path and variable changes.

Complete ARM Core Support

JTAGjet-Trace supports all **Cortex-M** based devices, however, the trace capability can be only used on devices with the ETM port.

Chameleon Debugger™

Each emulator is bundled with a Chameleon Debugger™, a high-end, full-featured, **multi-core debugger** that handles single and multi-CPU debugging. Chameleon Debugger features macros for automated board initialization and testing, fly-over variable pop-ups in source window, drag-and-drop between windows, Graphical Event Triggering and hundreds of other time saving debug features.

Keil uVision™ Support

JTAGjet-Trace-CM is compatible with Keil uVision debugger making it an ideal complementing tool to their IDE, simulator and code generation tools.

Multi-Core Debugging

One of the unique JTAGjet features is that it may be used concurrently with other debuggers (like TI Code Composer Studio or eSOL eBinder), offering a complete multi-core debug environment.

Smart Flash Programmer

JTAGjet comes with a Flash Programmer that recognizes the type and geometry of the device and automatically configures the flashing algorithm. Both on-chip and external NOR flash devices are supported. NAND flash programming is available as an external utility option.

Adaptive JTAG Clock & Voltage

JTAGjet-Trace supports fixed and adaptive JTAG clocks. Adaptive clock is synchronized with the CPU and is recommended on systems that vary the CPU clock to conserve power.

The emulator automatically adjusts the JTAG output signals to the VTREF voltage on the target board and supports 1.8V to 3.3V targets.



JTAGjet-Trace-CM Features

- Trace capture of up to the **maximum CPU speed**
- Supports Cortex-M0, M1,M3, M4 cores equipped with Embedded Trace Macrocell (ETM) from Atmel, Freescale, NXP, ST Micro, TI, Toshiba and others.
- □ Keil uVision compatible
- □ **Upgradeable** to support other ARM cores: ARM7, ARM9, ARM11, Cortex-R, Cortex-A, OMAP, DaVinci, Sitara, etc.
- □ Auto adjusting timing eliminates problems with data skew
- 4.5 MBytes of internal ETM trace memory, upgradeable to 18 MBytes to capture longer execution times
- □ 56-bit time stamp with CPU cycle accuracy for timing analysis
- Easy access to ETM settings, triggers and trace filtering
- Quiet operation no fans, no external heat sinks
- Only one connection to target both JTAG and trace are taken from a high-density, 20-pin Cortex header.
- Optional Mictor-38 ETM cable and plain JTAG adapter

Chameleon Debugger™ Features:

- □ Non-intrusive ETM & ETB trace display and debugging
- □ Synchronized Source and Trace windows for easy debugging
- D Pre- and post-capture filtering of trace data
- Trace clock (CPU speed) display in real-time
- □ Instrumentation Trace (ITM) display
- □ Support for all on-chip breakpoints, triggers and filtering
- Super fast code downloads
- □ JTAG and Serial Wire Debug (SWD) support
- Support for all three CPU resets: Core, System and Hardware Reset
- Automatic processor initialization on power-up or reset (memory mapping, peripheral setting, MMU, WD disable etc.)
- Flash Programming from GUI or from macros (DOS batch mode utility is also available)
- Multi-Core support for debugging of two or more devices on the same JTAG or separate JTAG connection
- □ Available **Concurrent Debugging** to allow two different debuggers to access two different cores at the same time
- □ Windows XP & Vista 32 & 64-bit compatible



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Specifications

Specifications	JTAGjet-Trace-CM
USB type / Speed	USB 2.0 type A / 480 Mbps
ETM Trace Memory	4.5 MBytes standard, 18Mbytes optional
Cores Supported	Cortex-M0, M1, M3, M4, upgradeable to other ARM cores
Max. ETM Trace Clock	200 MHz
Max. JTAG Clock	30 MHz
Probe Length	4 in
Probe Type	20-pin Cortex/SWD standard, Mictor-38 optional
JTAG I/O Voltage	1.8V – 3.3V
Current Draw (typ.)	0.8A typ., AC adapter included
Dimensions (in.)	5.6L-2.6W-1.2H



optional blue Mictor-38 cable and JTAG splitter (green)

TRC Cortexa	n3:trace												
Control	Enable Sta	art Resume	Clear	< Query V	Query Filter	. Fields S	ave						
#	PC		Disas		Source			TStamp (abs) TS	tamp (rel)	RdWr	ITMPar	MemData 🔷
#61		sing+0x2		R1,[R0,#0]	if (*tex	t != ' ')	{	0.0		-12794			
#61/1		sing+0x4		R1,#0x20				0.0		-12793			
#61/2		sing+0x6			221			0.0		-12792			
#67 #67/1		sing+0x8 sing+0xC		R1,[R0,#+0]	*text	^= 0x20;		0.0 0.0		-12788 -12787			
#67/1		ing+0xC		R1,R1,#0x20 R1,[R0,#0]				0.0		-12786			
#67/2		ING+OXIO	DIRD	RI, [RU, #U]				0.0		-12786	Rd	Comp1	0x67 (
#73								0.0		-12782	Rd	Comp1	0x67
#77								0.0		-12778	Wr	Comp1	0x47
#132		ocessing	В	0x20000078	while (*te	xt != '\0')	0.1		0			
#136	process	ing+0x14	LDRB	R1,[R0,#0]	while (*te	xt != '\0')	0.1		4			
#136/1		ing+0x16		R1,#0				0.1		5			
#136/2		ing+0x18		0x20000066				0.1		6			
#140		sing+0x2		R1,[R0,#0]	if (*tex	t != ' ')	{	0.1		8			
#140/1		sing+0x4		R1,#0x20				0.1		9			
#140/2		sing+0x6	[-]BEC	0x20000076				0.1	179	10	D .1	C 0	0.50.
#144 #150		Cortexa	n3:setur							12 18	Rd Rd	Comp0 Comp0	0x53 1 0x53 1
#150										22	Rd	CompO	0x53 . 0x53 .
#154		Comparator								26	Wr	Comp0	0x33 .
#162		Outpu	t	Function		Access			re Bits	30	Rd	Comp1	0x47
#166		ITM Trace	▼ En	nit Data on Data Address Match	-	Read or Write 🔻	&Buf1	[0]		34	Rd	Comp1	0x47
#170					,					38	Rd	Comp1	0x47 1
#176	proces	Comparator Outou		Function		Access			2.57	44		20	
#176/1							and here the second sec		re Bits	45			
#176/2		ITM Trace	En	nit Data on Data Address Match	-	Read or Write 💌	&Buf1	[2]		46			
#176/3					Add	ress Match 0 🖌	Address M	latch 1 Data Val	ue Size	47			
#176/4				Data	Address Range None	- V	one	- Word		48			
#176/5					Addiess Hange None	۱ <u>۲</u>	one			49 50			
#176/6 #182		Comparator	2						1	50			
#182/1		Outpu	t .	Function		Access		Value Igno	re Bits	51			
#182/2		ETM Event	▼ PC	Match	-	Fetch 👻	proces	sing		52			
#184		Januara				22276) R. 44	1.20			52			1000
Actives.		Comparator										- Fi-	×
<		Outpu		Function		Access			re Bits				>
Status: NotA	ctive	ETM Event	Da	ta Address Match	<u> </u>	Write 🔄	&Buf1	[2]				Trace	Clock: 36.00MHz 🏒

Trace window showing real-time trace information captured during program execution. Full program path (PC) is shown with function names and the corresponding assembler and C/C++ source code. In addition, on-chip Comparators (shown in the setup window) were set to generate **ITM data trace** for **Buf1[0]** and **Buf1[2]** accesses. Comparator2 is used to generate **ETM Event** to start the trace on **processing** function and Comparator3 is used to stop the trace on **write to location** Buf1[2]. Red line shows trace discontinuity. Yellow line shows STRB instruction (which was a write of 0x47 to Buf1[2] that stopped the PC trace). Timestamp is being shown in two columns (in ms and CPU cycles). The gap in trace capture was for 12778 CPU cycles (~178 ms). Current Trace Clock frequency is shown as **36MHz**. Cortex-M CPU always runs twice as fast as the Trace Clock, so the CPU speed is **72 MHz**.

Ordering Information

Part Number	Description	Price
JTAGjet-Trace-CM	JTAGjet for Cortex-M with 4.5 MBytes ETM trace memory, Chameleon Debugger, Keil uV driver and Cortex 20-pin cable	\$ 1,995
JTAGjet-Trace-CM2	JTAGjet for Cortex-M with 4.5 MBytes ETM trace memory, Chameleon Debugger, Keil uV driver and Cortex 20-pin cable	\$ 2,500
JTAGjet-Trace-CM4	JTAGjet for Cortex-M with 4.5 MBytes ETM trace memory, Chameleon Debugger, Keil uV driver and Cortex 20-pin cable	\$ 3,000
Cable-ETM-6	Mictor 38-pin ETM cable, 6 inch (optional)	\$ 250
ADA-ETM-JTAG	Mictor 38-pin ETM to ARM-20 and TI-14 splitter probe. Comes with 20-pin and 14-pin flat cables (optional)	\$ 250
MDK-ARM-B	Keil IDE with RealView compile tools (256K limited) for ARM7, ARM9 and Cortex-M, uVision debugger and simulator	\$ 2,895
MDK-ARM	Keil IDE with RealView compile tools (unlimited) for ARM7, ARM9 and Cortex-M, uVision debugger and simulator	\$ 4,895



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