

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



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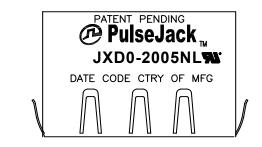


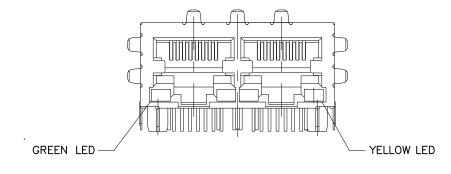


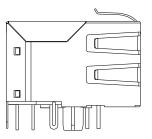


NOTES:

- 1. CONNECTOR MATERIAL:
 HOUISING:THERMOPLASTIC UL94 V-0
 INSERTER:PBT BLACK UL94 V-0
 SHIELD:Brass
 SHIELD PLATING:NICKEL
 CONTACT:COOPER ALLOY
 CONTACT PLATING: SELECTIVE GOLD, 30 MICRO-INCHS MIN
 IN CONTACT AREA
- 2. PIN NOT ELECTRICALLY CONNECTED MAYBE OMITTED SEE ELECTRICAL DRAWING FOR OMITTED PINS
- 3. RJ45 CAVITIES CONFORM TO FCC RULES AND REGULATION PART 68.
- 4. THE PART IS RECOMMENDDED FOR WAVE SOLDERING PROCESS PEAK SOLDERING TEMPERATURE IS 260°C MAX, 10 SECS MAX
- 5. OPERATING TEMPERATURE $T=-40^{\circ}C$ TO $+85^{\circ}C$.
- 6. STORAGE TEMPERATURE $T=-40^{\circ}C$ TO $+85^{\circ}C$.
- 7. All CRITICAL DIMENSIONS WITH "*"
- 8. PACKAGING INFORMATION: 50 PCS/TRAY, 1200 PCS/BOX







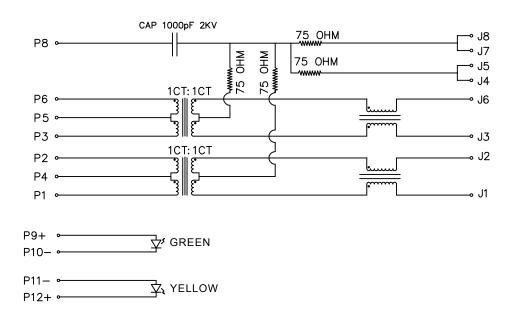
FINAL OUTLINE

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PRODUCT DESCRIPTION	TLA DRAWING	PS DRAWING	SHEET	PART NO.	DATASHEET REV.
CONN,RJ45,1X2,100D,1:1,TY	JXD0-2005NL - X1	_	1 OF 3	JXD0-2005NL	P1







SCHEMATIC

Electronical Specifications:

1.0 Turn Ratio @100KHz: $(P1\sim P2):(J1\sim J2) = 1:1\pm 5\%$ $(P7\sim P8):(J3\sim J6) = 1:1\pm 5\%$

2.0 Primary Inductance: 350uH MIN @100KHZ,0.1V 8mA DC BIAS

3.0 DC Resistance:(J1 \sim J2):(J3 \sim J6) 1.2 OHMS MAX 4.0 Insertion Loss: 1-100MHz -1.2dB MAX

5.0 Return Loss: $1-30 \mathrm{MHz}$ $-16 \mathrm{dB}$ MIN $30-60 \mathrm{MHz}$ $-12 \mathrm{dB}$ MIN

60-80MHz -10dB MIN

6.0 CROSS TALK: 1-100MHz -30dB MIN

7.0 COMMON TO COMMON MODE ATTENUATION: 1-100MHz -30dB MIN

8.0 Isolation: PHY Side to Line Side: 1500VAC or 2250VDC

LED SPECIFICATION:

STANDARD LED	WAVELENGTH	Forward V(max)	TYP
GREEN	565nm	2.4V	2.2V
YELLOW	590nm	2.5V	2.1V

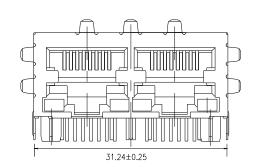
*WITH A FORWARD CURRENT OF 20mA

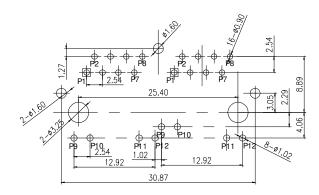
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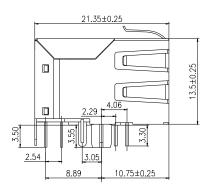


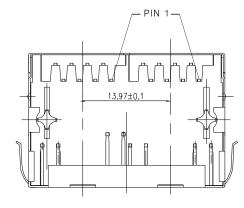


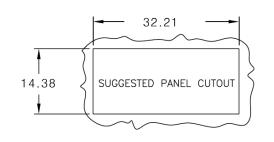
P. C. B RECOMMENDED HOLE LAYOUT SEEEN FROM COMPONENT SLIDE.

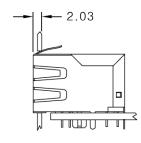
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DIMENSIONS ARE IN MILLIMETERS [INCHES] WITH THE FOLLOWING TOLERANCES: [INCHES] ARE FOR REFERENCE ONLY. $X.XX = \pm 0.25$ [$\pm .010$]









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