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July 2013

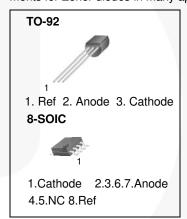
KA431 / KA431A / KA431L Programmable Shunt Regulator

Features

- · Programmable Output Voltage to 36 V
- Low Dynamic Output Impedance: 0.2 Ω (Typical)
- · Sink Current Capability: 1.0 to 100 mA
- Equivalent Full-Range Temperature Coefficient of 50 ppm/°C (Typical)
- Temperature Compensated for Operation Over Full Rated Operating Temperature Range
- · Low Output Noise Voltage
- · Fast Turn-on Response

Description

The KA431 / KA431A / KA431L are three-terminal adjustable regulators with a guaranteed thermal stability over the operating temperature range. The output voltage can be set to any value between V_{REF} (approximately 2.5 V) and 36 V with two external resistors. These devices have a typical dynamic output impedance of 0.2 Ω . Active output circuitry provides a sharp turn-on characteristic, making these devices excellent replacements for Zener diodes in many applications.



Ordering Information

Part Number	Operating Temperature Range	Output Voltage Tolerance	Top Mark	Package	Packing Method	
KA431DTF	\ \	2%	431	8-SOIC	Tape and Reel	
KA431ADTF			431A	8-SOIC	Tape and Reel	
KA431AZBU	-25 ~ +85°C	1%	KA431AZ	TO-92	Bulk	
KA431AZTA			KA431AZ	TO-92	Ammo	
KA431LZTA		0.5%	KA431LZ	TO-92	Ammo	

1

Block Diagram

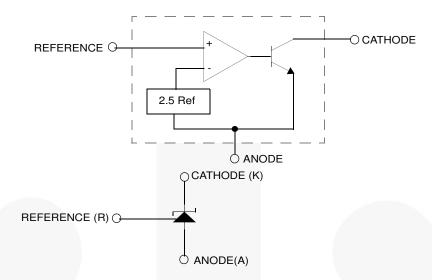


Figure 1. Block Diagram

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_A = 25^{\circ}$ C unless otherwise noted.

Symbol	Parameter	Value	Unit	
V _{KA}	Cathode Voltage	37	V	
I _{KA}	Cathode Current Range (Continuous)	-100 to +150	mA	
I _{REF}	Reference Input Current Range	-0.05 to +10	mA	
P_{D}	Power Dissipation TO-92, 8-SOIC Packages	770	mW	
R_{\thetajA}	Thermal Resistance, Junction to Ambient TO-92, 8-SOIC Packages	160	°C/W	
T _{OPR}	Operating Temperature Range	-25 to +85	°C	
T_{J}	Junction Temperature	150	°C	
T _{STG}	Storage Temperature Range	-65 to +150	°C	

Recommended Operating Conditions

The Recommended Operating Conditions table defines the conditions for actual device operation. Recommended operating conditions are specified to ensure optimal performance to the datasheet specifications. Fairchild does not recommend exceeding them or designing to Absolute Maximum Ratings.

Symbol	Parameter	Min.	Max.	Unit	
V_{KA}	Cathode Voltage	V_{REF}	36	V	
I_{KA}	Cathode Current	1	100	mA	

Electrical Characteristics

Values are at $T_A = 25$ °C unless otherwise noted.

0	Dawamatan	Conditions		KA431			KA431A			KA431L			11
Symbol	Parameter			Min.	Тур.	Max.	Min.	Тур.	Max.	Min.	Тур.	Max.	Unit
V _{REF}	Reference Input Voltage	$V_{KA} = V_{REF},$ $I_{KA} = 10 \text{ mA}$		2.450	2.500	2.550	2.470	2.495	2.520	2.482	2.495	2.508	V
ΔV _{REF} / ΔT	Deviation of Reference Input Voltage Over- Temperature	$V_{KA} = V_{REF},$ $I_{KA} = 10 \text{ mA}$ $T_{MIN} \le T_A \le T_{MAX}$ ⁽¹⁾			4.5	17.0		4.5	17.0		4.5	17.0	mV
	Ratio of Change in		$\Delta V_{KA} = 10V - V_{REF}$		-1.0	- 2.7		-1.0	- 2.7		-1.0	-2.7	
$\Delta V_{REF}/\Delta V_{KA}$	Reference Input Voltage to the Change in Cathode Voltage	I _{KA} = 10 mA	ΔV _{KA} = 36 V-10 V		-0.5	-2.0		-0.5	-2.0		-0.5	-2.0	mV / V
I _{REF}	Reference Input Current	I_{KA} = 10 mA, R1 =10 kΩ, R2 = ∞			1.5	4.0		1.5	4.0		1.5	4.0	μΑ
ΔΙ _{REF} / ΔΤ	Deviation of Reference Input Current Over Full Temperature Range	I_{KA} = 10 mA, R1 = 10 kΩ, R2 = ∞ T_A = Full Range			0.4	1.2		0.4	1.2		0.4	1.2	μΑ
I _{KA(MIN)}	Minimum Cathode Current for Regulation	V _{KA} = V _{REF}			0.45	1.00		0.45	1.00		0.45	1.00	mA
I _{KA(OFF)}	Off - Stage Cathode Current	$V_{KA} = 36 V,$ $V_{REF} = 0$			0.05	1.00		0.05	1.00		0.05	1.00	μА
Z _{KA}	Dynamic Impedance	$V_{KA} = V_{REF}$, $I_{KA} = 1$ to 100 mA $f \ge 1.0$ kHz			0.15	0.50		0.15	0.50		0.15	0.50	Ω

Note:

1. $T_{MIN} = -25^{\circ}C$, $T_{MAX} = +85^{\circ}C$.

Test Circuits

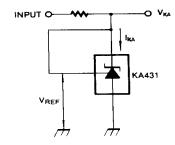


Figure 2. Test Circuit for $V_{KA} = V_{REF}$

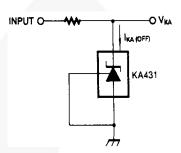


Figure 4. Test Circuit for I_{KA(OFF)}

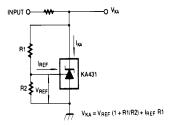


Figure 3. Test Circuit for $V_{KA} \ge V_{REF}$

Typical Performance Characteristics

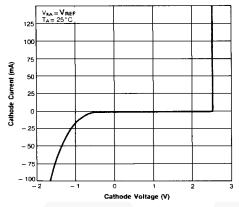
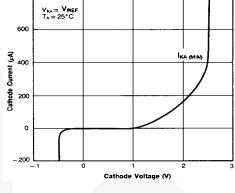


Figure 5. Cathode Current vs. Cathode Voltage



800

Figure 6. Cathode Current vs. Cathode Voltage

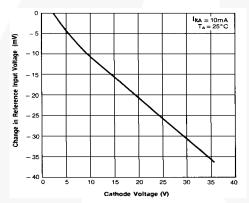


Figure 7. Change in Reference Input Voltage vs. Cathode Voltage

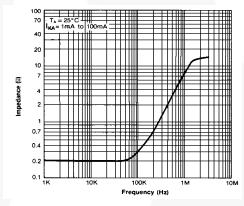


Figure 8. Dynamic Impedance Frequency

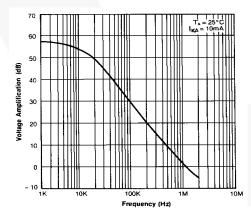


Figure 9. Small Signal Voltage Amplification vs. Frequency

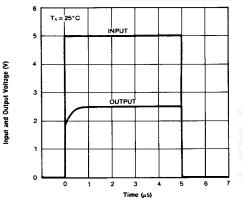


Figure 10. Pulse Response

Typical Performance Characteristics (Continued)

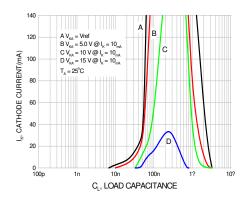
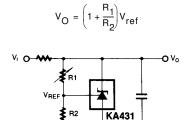
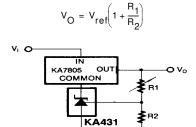


Figure 11. Stability Boundary Conditions

Typical Application





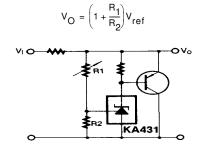
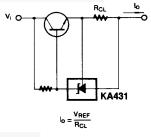


Figure 12. Shunt Regulator

Figure 13. Output Control for Three-Terminal Fixed Regulator

Figure 14. High-Current Shunt Regulator





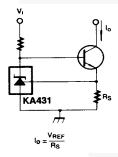


Figure 16. Constant-Current Sink

Physical Dimensions

TO-92 Bulk Type

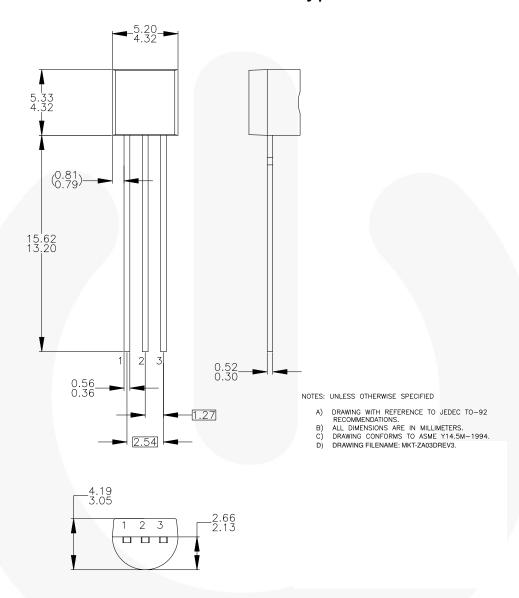


Figure 17. 3-Lead, TO-92, Molded, Standard Straight Lead

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Physical Dimensions (Continued)

TO-92 Ammo Type

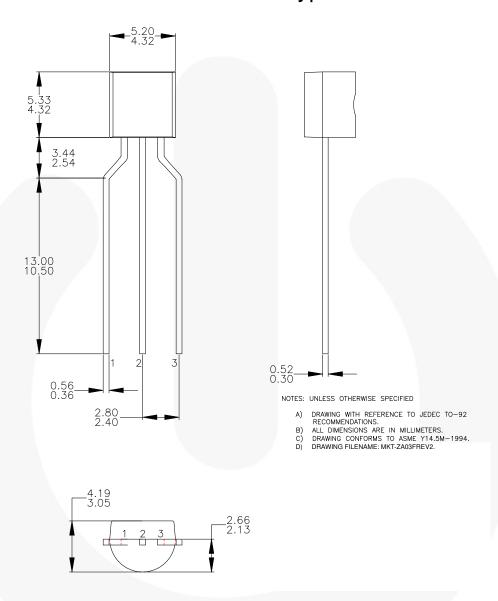


Figure 18. 3-Lead, TO-92, Molded, 0.200 in Line Spacing Lead Form

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Physical Dimensions (Continued)

8-SOIC

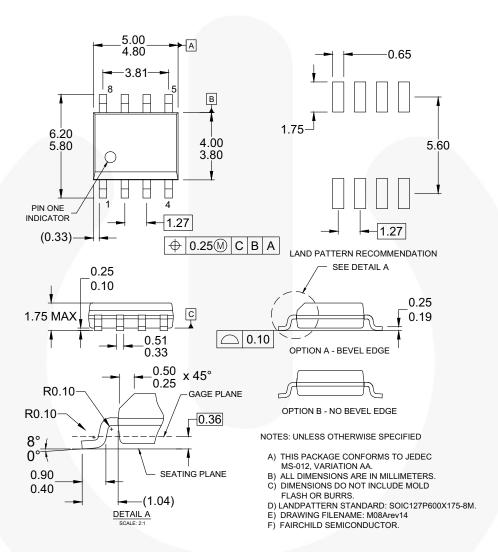


Figure 19. 8-Lead, SOIC, JEDEC MS 0-12, 0.150 inch Narrow Body

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