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KSB1116/1116A

Audio Frequency Power Amplifier & Medium Speed Switching

Complement to KSD1616/1616A



PNP Epitaxial Silicon Transistor

Absolute Maximum Ratings T_a=25°C unless otherwise noted

Symbol	Paramet	Ratings	Units	
V _{CBO}	Collector-Base Voltage	: KSB1116 : KSB1116A	-60 -80	V V
V _{CEO}	Collector-Emitter Voltage	: KSB1116 : KSB1116A	-50 -60	V V
V _{EBO}	Emitter-Base Voltage		-6	V
I _C	Collector Current (DC)		-1	Α
I _{CP}	* Collector Current (Pulse)		-2	Α
P _C	Collector Power Dissipation		0.75	W
TJ	Junction Temperature		150	°C
T _{STG}	Storage Temperature		-55 ~ 150	°C

^{*} PW≤10ms, Duty Cycle≤50%

Electrical Characteristics T_a=25°C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
I _{CBO}	Collector Cut-off Current	$V_{CB} = -60V, I_{E} = 0$			-100	nA
I _{EBO}	Emitter Cut-off Current	$V_{EB} = -6V, I_{C} = 0$			-100	nA
h _{FE1}	* DC Current Gain : KSB1116 : KSB1116A	V _{CE} = -2V, I _C = -100mA	135 135		600 400	
h_{FE2}		$V_{CE} = -2V, I_{C} = -1A$	81			
V _{BE} (on)	* Base-Emitter On Voltage	V_{CE} = -2V, I_{C} = -50mA	-600	-650	-700	mV
V _{CE} (sat)	* Collector-Emitter Saturation Voltage	I _C = -1A, I _B = -50mA		-0.2	-0.3	V
V _{BE} (sat)	* Base-Emitter Saturation Voltage	I _C = -1A, I _B = -50mA		-0.9	-1.2	V
C _{ob}	Output Capacitance	V _{CB} = -10V, I _E =0, f=1MHz		25		pF
f _T	Current Gain Bandwidth Product	V _{CE} = -2V, I _C = -100mA	70	120		MHz
t _{ON}	Turn On Time	V _{CC} = -10V, I _C = -100mA		0.07		μs
t _{STG}	Storage Time	I _{B1} = -I _{B2} = -10mA		0.7		μs
t _F	Fall Time	V _{BE} (off)= 2~3V		0.07		μs

^{*} Pulse Test: PW ≤350μs, Duty Cycle≤2%

h_{FE} Classification

Classification	Y	G	L
h _{FE1}	135 ~ 270	200 ~ 400	300 ~ 600

Typical Characteristics

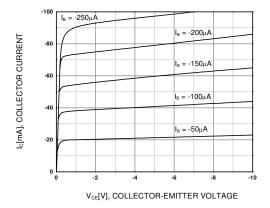
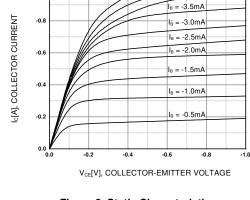


Figure 1. Static Characteristic



-4.5mA

 $I_B = -5.0 \text{mA}$

Figure 2. Static Characteristic

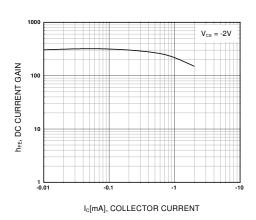


Figure 3. DC current Gain

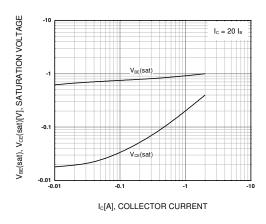


Figure 4. Base-Emitter Saturation Voltage Collector-Emitter Saturation Voltage

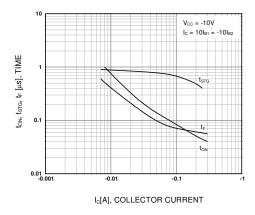


Figure 5. Switching Time

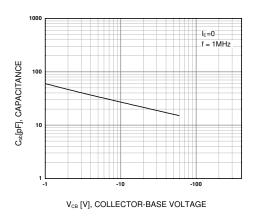


Figure 6. Collector Output Capacitance

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Typical Characteristics (Continued)

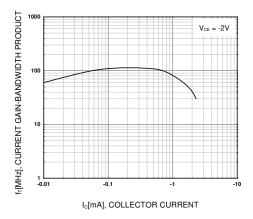


Figure 7. Current Gain Bandwidth Product

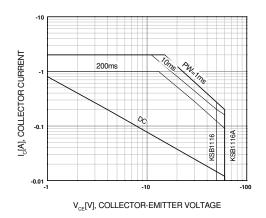


Figure 8. Safe Operating Area

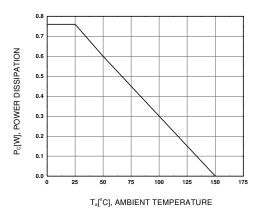
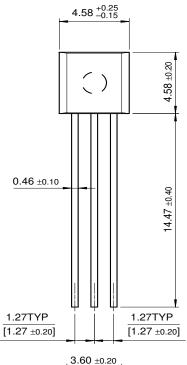
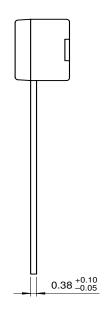


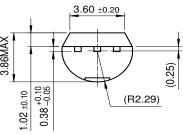
Figure 9. Power Derating

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Package Demensions





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No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
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