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September 2009

FAIRCHILD

## KSC5305DF NPN Silicon Transistor

## Features

High Voltage High Speed Power Switch

## Application

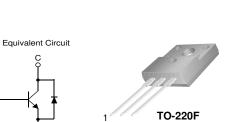
- Built-in Free-wheeling Diode makes efficient anti saturation operation
- Suitable for half bridge light ballast Applications
- No need to interest an h<sub>FE</sub> value because of low variable storage-time spread even though corner spirit product
- Low base drive requirement

## Absolute Maximum Ratings $T_A = 25^{\circ} C$ unless otherwise noted

Symbol	Parameter	Value	Units	
V <sub>CBO</sub>	Collector Base Voltage	800	V	
V <sub>CEO</sub>	Collector Emitter Voltage	400	V	
V <sub>EBO</sub>	Emitter Base Voltage	12	V	
Ι <sub>C</sub>	Collector Current (DC)	5	Α	
I <sub>CP</sub>	*Collector Current (Pulse)	10	А	
Ι <sub>Β</sub>	Base Current (DC)	2	A	
I <sub>BP</sub>	*Base Current (Pulse)	4	А	
P <sub>C</sub>	Power Dissipation (T <sub>C</sub> =25°C)	40	W	
ТJ	Junction Temperature	150	°C	
T <sub>STG</sub> Storage Temperature range		-65 to +150	°C	

### **Thermal Characteristics**

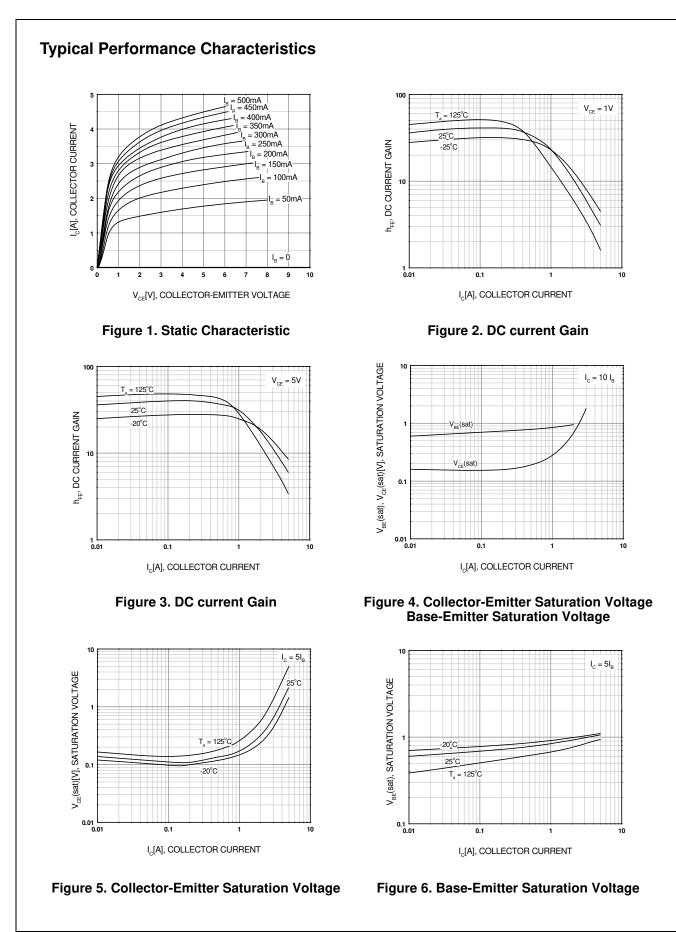
Symbol	Parameter	Rating	Units
$R_{ extsf{ heta}JC}$	Thermal Resistance, Junction to Case	3.125	°C/W
$R_{ extsf{ heta}JA}$	Thermal Resistance, Junction to Ambient	69.5	°C/W



1.Base 2.Collector 3.Emitter

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units V
BV <sub>CBO</sub>	Collector-Base Breakdown Voltage	I <sub>C</sub> =1mA, I <sub>E</sub> =0	800			
BV <sub>CEO</sub>	Collector-Emitter Breakdown Voltage	I <sub>C</sub> =5mA, I <sub>B</sub> =0	400	-	-	V
$BV_{EBO}$	Emitter Cut-off Current	I <sub>E</sub> =1mA, I <sub>C</sub> =0	12	-	-	V
I <sub>CBO</sub>	Collector Cut-off Current	V <sub>CB</sub> =500V, I <sub>E</sub> =0	-	-	10	μA
I <sub>EBO</sub>	Emitter Cut-off Current	$V_{EB} = 9V, I_{C} = 0$	-	-	10	μA
h <sub>FE1</sub> h <sub>FE2</sub>	DC Current Gain	$V_{CE}=1V, I_{C}=0.8A$ $V_{CE}=1V, I_{C}=2A$	22 8	-	-	
V <sub>CE</sub> (sat)	Collector-Emitter Saturation Voltage	I <sub>C</sub> =0.8A, I <sub>B</sub> =0.08A I <sub>C</sub> =2A, I <sub>B</sub> =0.4A	- -	-	0.4 0.5	V V
V <sub>BE</sub> (sat)	Base-Emitter Saturation Voltage	I <sub>C</sub> =0.8A, I <sub>B</sub> =0.08A I <sub>C</sub> =2A, I <sub>B</sub> =0.4A	-	-	1.0 1.0	V V
C <sub>ob</sub>	Output Capacitance	V <sub>CB</sub> = 10V, f=1MHz	-	-	75	pF
t <sub>ON</sub>	Turn On Time	V <sub>CC</sub> =300V, I <sub>C</sub> =2A	-	-	150	ns
t <sub>STG</sub>	Storage Time	I <sub>B1</sub> = 0.4A, I <sub>B2</sub> =-1A	-	-	2	μS
t <sub>F</sub>	Fall Time	R <sub>L</sub> = 150Ω	-	-	0.2	μS
t <sub>STG</sub>	Storage Time	V <sub>CC</sub> =15V,V <sub>Z</sub> =300V	-	-	2.25	μS
t <sub>F</sub>	Fall Time	$I_{C} = 2A, I_{B1} = 0.4A$ $I_{B2} = -0.4A, L_{C} = 200 \mu H$	-	-	150	ns
V <sub>F</sub>	Diode Forward Voltage	I <sub>F</sub> = 1A I <sub>F</sub> = 2A	- -	-	1.5 1.6	V V
t <sub>rr</sub>	* Reverse recovery time (di/dt = 10A/µs)	$I_F = 0.4A$ $I_F = 1A$ $I_F = 2A$	- -	800 1.4 1.9		ns μs μs

\* Pulse Test : Pulse Width=5mS, Duty cycles  $\leq$  10%



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KSC5305DF — NPN Silicon Transistor



**Typical Performance Characteristics** 

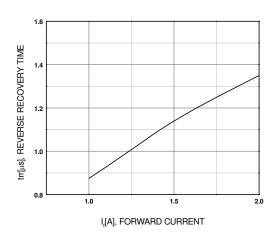
V<sub>CC</sub> = 300V I<sub>C</sub> = 5I<sub>B1</sub> = -2.5I<sub>B</sub>

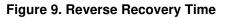
10

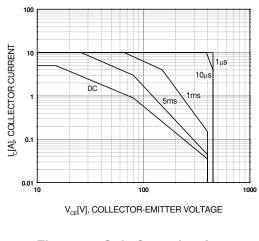
t<sub>sre</sub>, t<sub>F</sub> [µs], TIME

0.1

0.01 -0.1









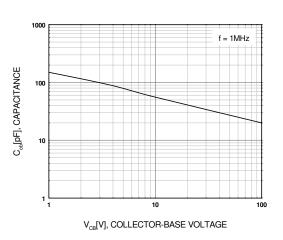


Figure 8. Collector Output Capacitance

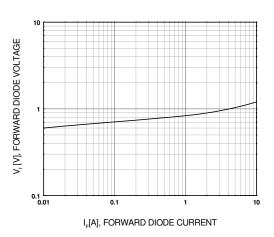


Figure 10. Forward Diode Voltage

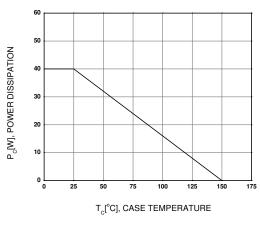
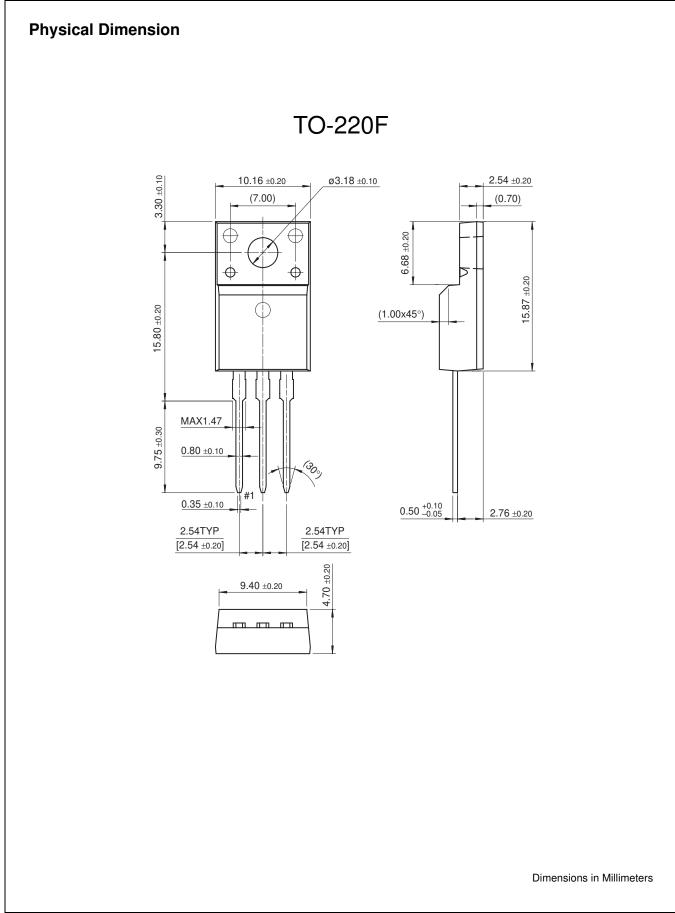
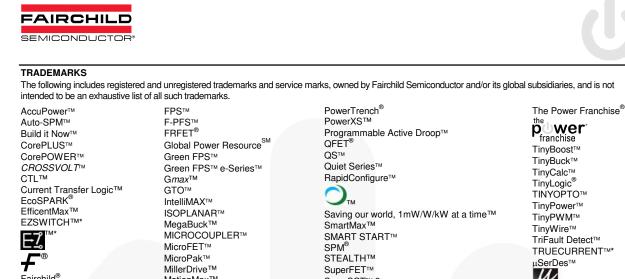


Figure 12. Power Derating

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