



Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from,Europe,America and south Asia,supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of “Quality Parts,Customers Priority,Honest Operation,and Considerate Service”,our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip,ALPS,ROHM,Xilinx,Pulse,ON,Everlight and Freescale. Main products comprise IC,Modules,Potentiometer,IC Socket,Relay,Connector.Our parts cover such applications as commercial,industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



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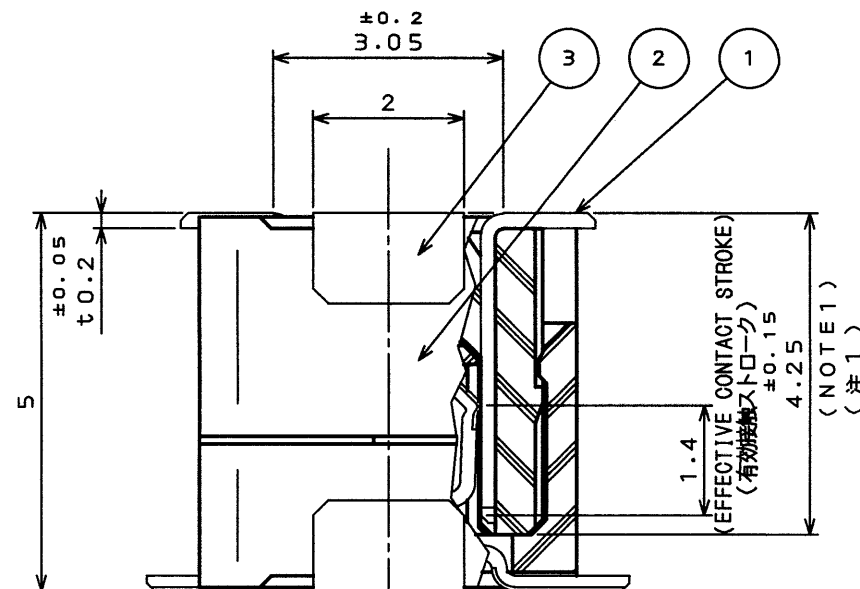
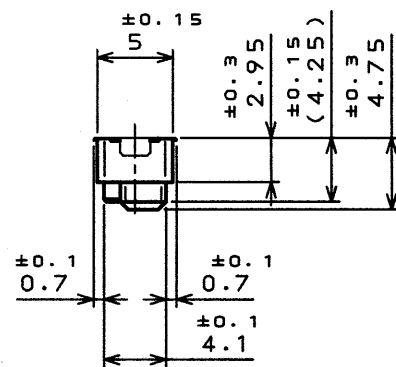
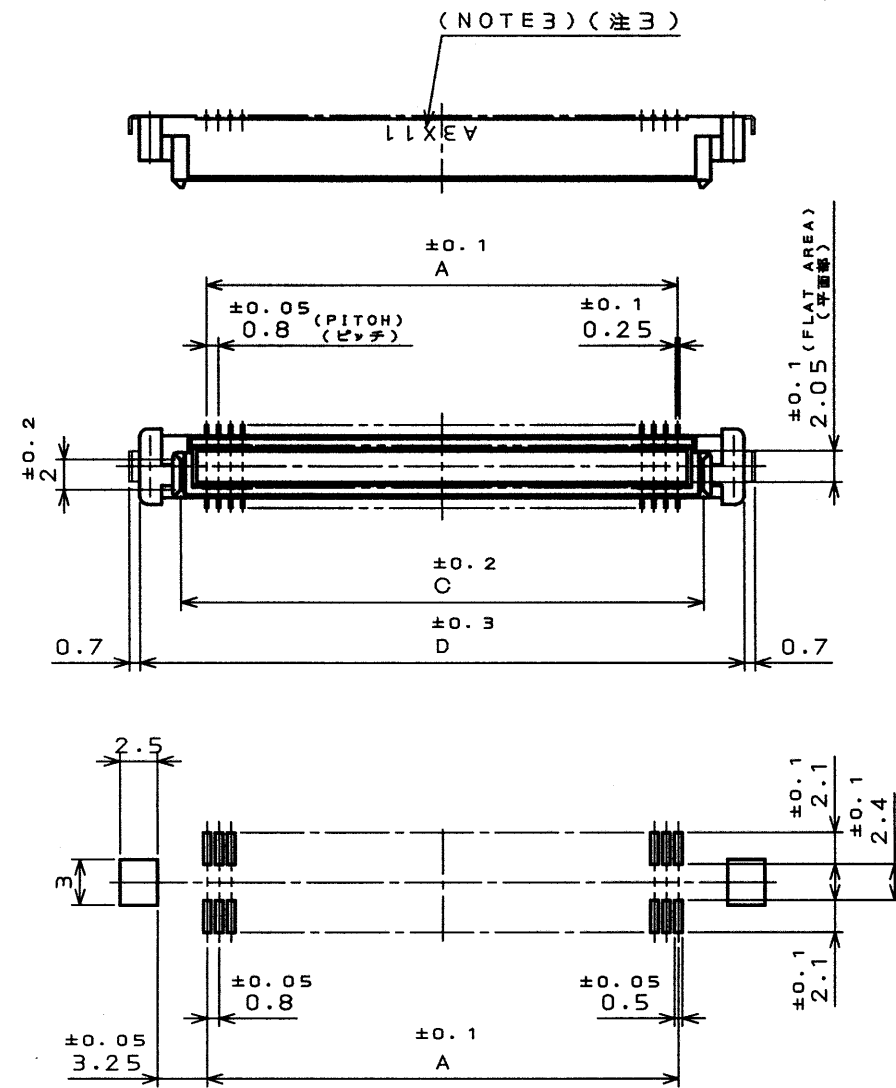
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7ZE800S
('ON 0NIMVHO)台景図

版数 REV.	年月日 DATE	図号 DGN NO.	変更内容 DESCRIPTION	製図 DR.	担当 CHK.	査閲 APPD.	承認 APPD.



APPLICABLE P.C.B. DIMENSION (REF.)
適合基板寸法 (参考)

MATED CONDITION (REF.) (SCALE 10:1)
嵌合状態図 (参考) (尺度 10:1)

DESIGNATION
命名法

KX15-**-K3F*E
(LEAD FREE)TIN/TIN ALLOY
錫又は銅合金(無鉛)
CONTACT FINISH (TABLE2)
コンタクト仕上(表2参照)
MOUNT FORM D: WITH POSITIONING BOSS, WITH HOLD-DOWN
E: WITH POSITIONING BOSS, WITHOUT HOLD-DOWN
F: WITHOUT POSITIONING BOSS, WITH HOLD-DOWN
マウント形状 D: 位置決めボス有、ホルルドダウン有
E: 位置決めボス有、ホルルドダウン無
F: 位置決めボス無、ホルルドダウン有
STACKING HEIGHT PER ONE SIDE (NOTE2)
平行基板間高さ(注2)
N: WITHOUT MISMATING PREVENTION KEY
K: WITH MISMATING PREVENTION KEY
N: 逆挿入防止キー無
K: 逆挿入防止キー有
NO. OF CONTACT
芯数
14: RECEPTACLE
15: PLUG
14: レセプタクル
15: プラグ
SERIES
シリーズ

NOTE1. COPLANARITY: 0.1mm MAX.
2. STACKING HEIGHT = HEIGHT OF KX14 (RECEPTACLE) + HEIGHT OF KX15 (PLUG)
3. PRODUCTION LOT NUMBER AS INDICATED.
注1. 端子の平坦度は、0.1以下。
2. KX14 (レセプタクル) の数と、KX15 (プラグ) の数とを加えた値が平行基板間高さになります。
3. 図示の面に、ロット番号を表示する。

(EX.) A3X11
(例) LOT NUMBER OF CURRENT MONTH 月の生産ロット番号
MONTH: 1~9 (OCT: O, NOV: X, DEC: Y) 月表示: 1~9 (10月: O, 11月: X, 12月: Y)
YEAR (LAST DIGIT ONLY) 年表示 (西暦末尾)
MANUFACTURE CODE 製造コード

TABLE1 DIMENSION
表1 寸法

DEVELOPMENT CONDITION 開発状態	NO. OF CONTACT 芯数	A	B	C	D
○	20	7.2	8.7	10.6	13
○	30	11.2	12.7	14.6	17
○	40	15.2	16.7	18.6	21
×	50	19.2	20.7	22.6	25
×	60	23.2	24.7	26.6	29
×	70	27.2	28.7	30.6	33
×	80	31.2	32.7	34.6	37
×	100	39.2	40.7	42.6	45
×	120	47.2	48.7	50.6	53
×	140	55.2	56.7	58.6	61

○: DEVELOPED X: DEVELOPMENT PROGRAM
○: 開発済み X: 開発予定

TABLE2 FINISH
表2 仕上

FINISH 仕上記号	CONTACT AREA 接触部仕上	TERMINAL AREA 端子部仕上
NONE 無印	GOLD PLATING OVER NICKEL (GOLD: 0.1μm MIN) Ni上Au0.1μm以上	GOLD FLASH OVER NICKEL Ni上Auフラッシュ
1	GOLD PLATING OVER NICKEL (GOLD: 0.1μm MIN) Ni上Au0.1μm以上	(LEAD FREE) TIN/TIN ALLOY 錫又は銅合金(無鉛)

3	HOLD-DOWN	2	COPPER ALLOY	(LEAD FREE) TIN/TIN ALLOY 錫又は銅合金(無鉛)	
2	INSULATOR	1	GLASS FILLED PPS		COLOR: BLACK
1	CONTACT	TABLE1	COPPER ALLOY	TABLE2	
符号 NO.	名称 DESCRIPTION	個数 QTY.	材料 MATERIAL	仕上 FINISH	備考 REMARKS
仕様書 (SPECIFICATION) JACS-1401-*		第1版 (ORIGINAL DATE) 28.Feb.2003		尺 (SCALE) 2:1	シリーズ (SERIES) KX
公差 (GENERAL TOLERANCE)		製図 DR.	E. MURANO	名称 (TITLE) JAE CONNECTOR 日本航空電子工業株式会社	
寸法 (DIMENSION)		担当 CHK.	H. TAKAHASHI	KX15-**-K3F*E PLUG	
角度 (ANGLES)		査閲 APPD.	—		
・ ±0.8	X° ±	承認 APPD.	S. Kashiwagi	重量 (WEIGHT)	図面番号 (DRAWING NO.) SJ038327
・ X ±0.4	X° X° ±				版数 (REV.) 1
・ XX ±0.1					
・ XXX ±					