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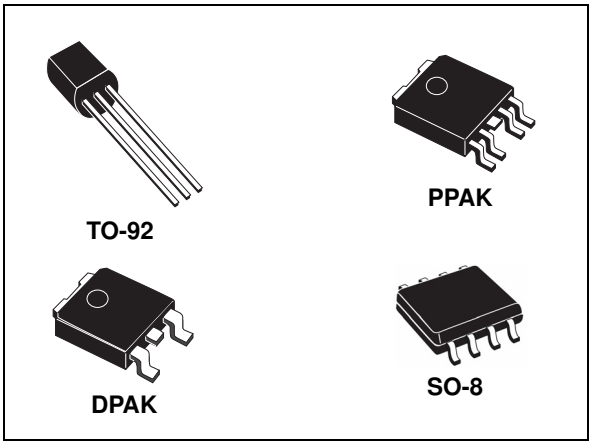
L4931

Very low drop voltage regulators with inhibit

Datasheet - production data



Features

- Very low dropout voltage (0.4 V)
- Very low quiescent current
- Typ. 50 µA in OFF mode, 600 µA in ON mode
- Output current up to 250 mA
- Logic controlled electronic shutdown
- Output voltages: 2.7; 3.3; 3.5; 5; 8; 12 V
- Automotive-grade product: 2.7 V, 3.3 V V_{OUT} in SO-8 package only
- Internal current and thermal limit
- Only 2.2 µF for stability
- Available in ± 1% (AB) or 2% (C) selection at 25 °C
- Supply voltage rejection: 70 dB typ. for 5 V version
- Temperature range: from -40 to 125 °C

Description

The L4931 is a very low drop regulator available in SO-8, DPAK, PPAK and TO-92 packages and in a wide range of output voltages.

The very low drop voltage (0.4 V) and the very low quiescent current make it particularly suitable for low noise, low power applications and especially in battery-powered systems.

A TTL compatible shutdown logic control function is available in PPAK and SO-8 packages. This means that when the device is used as a local regulator, a part of the board can be put in standby mode, decreasing the total power consumption. It requires only a 2.2 µF capacitor for stability allowing space and cost saving.

The L4931 is available as automotive-grade in SO-8 package only. This device is qualified according to the specification AEC-Q100 of the automotive market, in the temperature range from 40 °C to 125 °C, and the statistical tests: PAT, SYL, SBL are performed.

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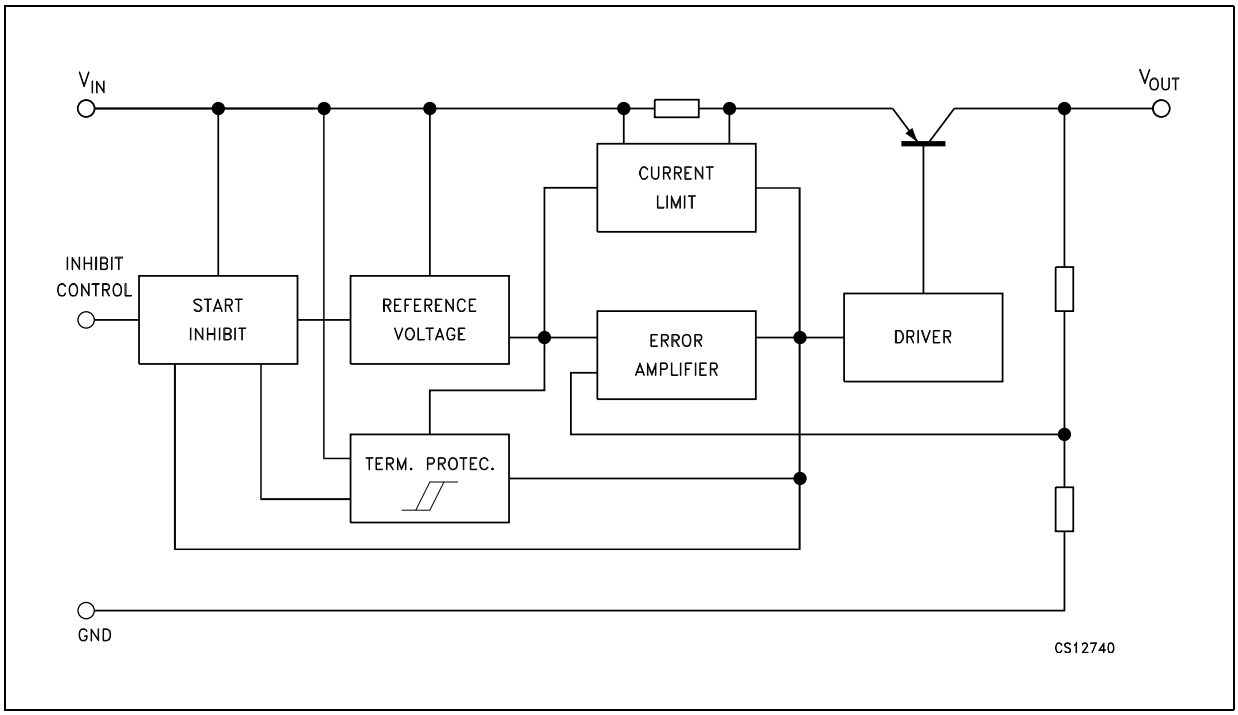
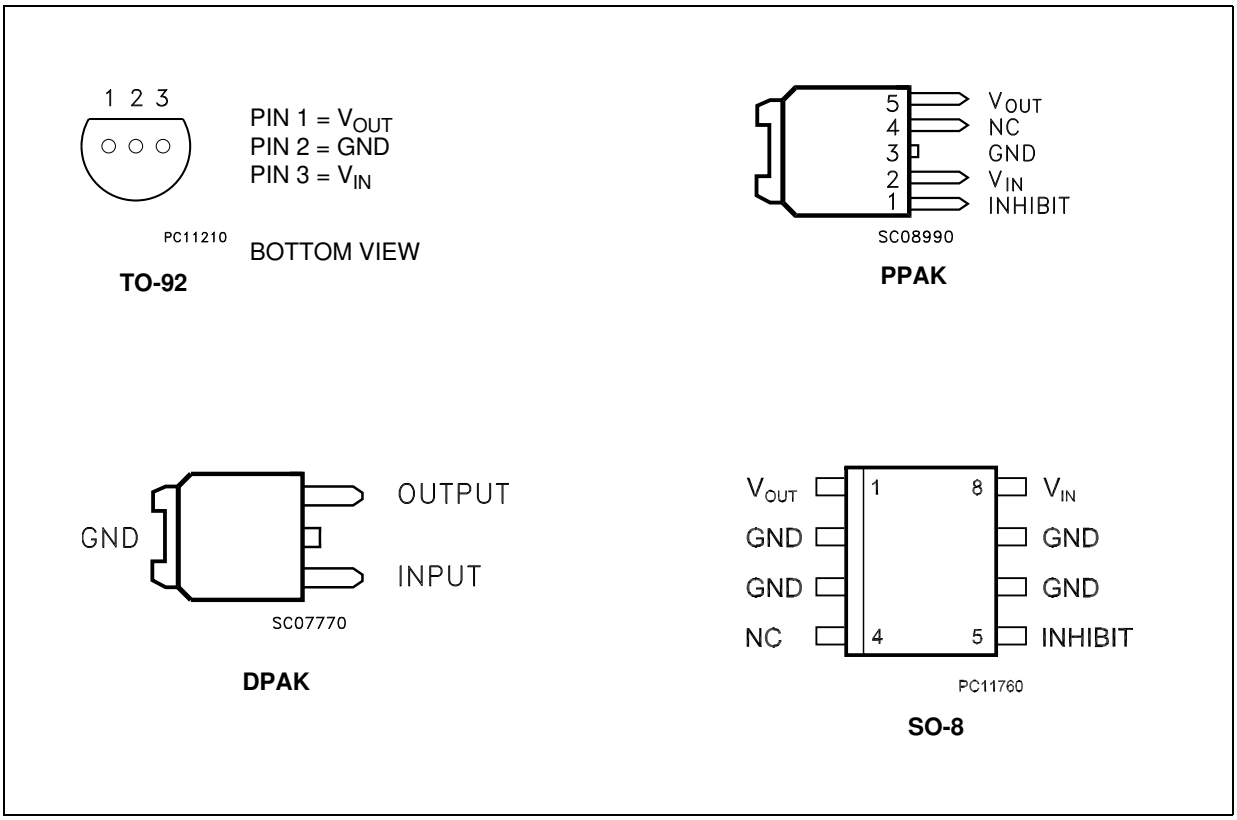
1 Diagram

Figure 1. Schematic diagram



2 Pin configuration

Figure 2. Pin connections (top view)



3 Maximum ratings

Table 1. Absolute maximum ratings

Symbol	Parameter	Value	Unit
V_I	DC Input voltage	20	V
I_O	Output current	Internally limited	mA
P_D	Power dissipation	Internally limited	mW
T_{STG}	Storage temperature range	-40 to 150	°C
T_{OP}	Operating junction temperature range	-40 to 125	°C

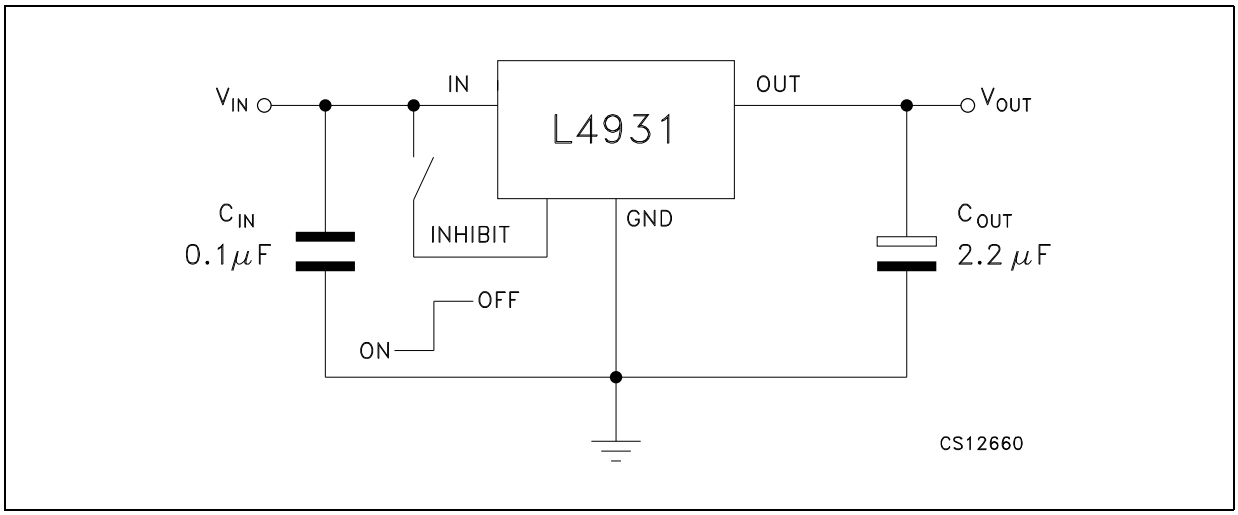
Note: Absolute maximum ratings are those values beyond which damage to the device may occur. Functional operation under these condition is not implied.

Table 2. Thermal data

Symbol	Parameter	TO-92	DPAK	SO-8	Unit
R_{thJC}	Thermal resistance junction-case		8	20	°C/W
R_{thJA}	Thermal resistance junction-ambient	200	100	55	°C/W

4 Application circuit

Figure 3. Test circuit



5 Electrical characteristics

(Refer to the test circuits, T_A = 25 °C, C_I = 0.1 µF, C_O = 2.2 µF unless otherwise specified).

Table 3. L4931Cxx27 electrical characteristics

Symbol	Parameter	Test conditions		Min.	Typ.	Max.	Unit
V_O	Output voltage	I_O = 5 mA, V_I = 4.7 V		2.646	2.7	2.754	V
		I_O = 5 mA, V_I = 4.7 V, T_A = -25 to 85 °C		2.592		2.808	
V_I	Operating input voltage	I_O = 250 mA				20	V
I_{out}	Output current limit				300		mA
DV_O	Line regulation	V_I = 3.4 to 20 V, I_O = 0.5 mA			3	18	mV
DV_O	Load regulation (1)	V_I = 3.6 V, I_O = 0.5 to 250 mA			3	18	mV
I_d	Quiescent current ON mode	V_I = 3.6 to 20 V, I_O = 0 mA			0.6	1	mA
		V_I = 3.6 to 20 V, I_O = 250 mA			4	6	
	OFF mode	V_I = 6 V			50	100	µA
SVR	Supply voltage rejection	I_O = 5 mA V_I = 4.6 ± 1 V	f = 120 Hz		74		dB
			f = 1 kHz		71		
			f = 10 kHz		55		
eN	Output noise voltage	B = 10 Hz to 100 kHz			50		µV
V_d	Dropout voltage (1)	I_O = 250 mA			0.4	0.6	V
		I_O = 250 mA, T_A = -40 to 125 °C				0.8	V
V_{IL}	Control input logic low	T_A = -40 to 125 °C				0.8	V
V_{IH}	Control input logic high	T_A = -40 to 125 °C		2			V
I_I	Control input current	V_I = 6 V, V_C = 6 V			10		µA
C_O	Output bypass capacitance	ESR = 0.1 to 10 W, I_O = 0 to 250 mA		2	10		µF

1. For SO-8 package the maximum limit of load regulation and dropout should be increased by 20 mV.

(Refer to the test circuits, T_A = -40 to 125 °C, C_I = 0.1 µF, C_O = 2.2 µF unless otherwise specified).

Table 4. L4931Cxx27-TRY (automotive-grade) electrical characteristics

Symbol	Parameter	Test conditions		Min.	Typ.	Max.	Unit
V_O	Output voltage	I_O = 5 mA, V_I = 4.7 V, T_A = 25 °C		2.646	2.7	2.754	V
		I_O = 5 mA, V_I = 4.7 V		2.592		2.808	
V_I	Operating input voltage	I_O = 250 mA				20	V
I_{out}	Output current limit	T_A = 25 °C			300		mA
ΔV_O	Line regulation	V_I = 3.4 to 20 V, I_O = 0.5 mA				20	mV
ΔV_O	Load regulation	V_I = 3.6 V, I_O = 0.5 to 250 mA				38	mV
I_d	Quiescent current ON mode	V_I = 3.6 to 20 V, I_O = 0 mA				1	mA
		V_I = 3.6 to 20 V, I_O = 250 mA				6	
	OFF mode	V_I = 6 V				100	µA
SVR	Supply voltage rejection	I_O = 5 mA V_I = 4.6 ± 1 V T_A = 25 °C	f = 120 Hz		74		dB
			f = 1 kHz		71		
			f = 10 kHz		55		
eN	Output noise voltage	B = 10 Hz to 100 kHz, T_A = 25 °C			50		µV
V_d	Dropout voltage	I_O = 250 mA, T_A = 25 °C			0.4	0.6	V
		I_O = 250 mA				0.82	V
V_{IL}	Control input logic low					0.82	V
V_{IH}	Control input logic high			2			V
I_I	Control input current	V_I = 6 V, V_C = 6 V, T_A = 25 °C			10		µA
C_O	Output bypass capacitance	ESR = 0.1 to 10 Ω, I_O = 0 to 250 mA, T_A = 25 °C		2	10		µF

(Refer to the test circuits, T_A = 25 °C, C_I = 0.1 µF, C_O = 2.2 µF unless otherwise specified).

Table 5. L4931ABxx33 electrical characteristics

Symbol	Parameter	Test conditions		Min.	Typ.	Max.	Unit
V_O	Output voltage	I_O = 5 mA, V_I = 5.3 V		3.267	3.3	3.333	V
		I_O = 5 mA, V_I = 5.3 V, T_A=-25 to 85 °C		3.234		3.366	
V_I	Operating input voltage	I_O = 250 mA				20	V
I_{out}	Output current limit				300		mA
ΔV_O	Line regulation	V_I = 4 to 20 V, I_O = 0.5 mA			3	15	mV
ΔV_O	Load regulation (1)	V_I = 4.2 V, I_O = 0.5 to 250 mA			3	15	mV
I_d	Quiescent current ON mode	V_I = 4.2 to 20 V, I_O = 0 mA			0.6	1	mA
		V_I = 4.2 to 20 V, I_O = 250 mA			4	6	
	OFF mode	V_I = 6 V			50	100	µA
SVR	Supply voltage rejection	I_O = 5 mA V_I = 5.2 ± 1 V	f = 120 Hz		73		dB
			f = 1 kHz		70		
			f = 10 kHz		55		
eN	Output noise voltage	B = 10 Hz to 100 kHz			50		µV
V_d	Dropout voltage (1)	I_O = 250 mA			0.4	0.6	V
		I_O = 250 mA, T_A= -40 to 125 °C				0.8	V
V_{IL}	Control input logic low	T_A = -40 to 125 °C				0.8	V
V_{IH}	Control input logic high	T_A = -40 to 125 °C		2			V
I_I	Control input current	V_I = 6 V, V_C = 6 V			10		µA
C_O	Output bypass capacitance	ESR = 0.1 to 10 Ω, I_O = 0 to 250 mA		2	10		µF

1. For SO-8 package the maximum limit of load regulation and dropout should be increased by 20 mV.

(Refer to the test circuits, T_A = 25 °C, C_I = 0.1 µF, C_O = 2.2 µF unless otherwise specified).

Table 6. L4931Cxx33 electrical characteristics

Symbol	Parameter	Test conditions		Min.	Typ.	Max.	Unit
V_O	Output voltage	I_O = 5 mA, V_I = 5.3 V		3.234	3.3	3.366	V
		I_O = 5 mA, V_I = 5.3 V, T_A=-25 to 85 °C		3.168		3.432	
V_I	Operating input voltage	I_O = 250 mA				20	V
I_{out}	Output current limit				300		mA
ΔV_O	Line regulation	V_I = 4.1 to 20 V, I_O = 0.5 mA			3	18	mV
ΔV_O	Load regulation (1)	V_I = 4.3 V, I_O = 0.5 to 250 mA			3	18	mV
I_d	Quiescent current ON mode	V_I = 4.3 to 20 V, I_O = 0 mA			0.6	1	mA
		V_I = 4.3 to 20 V, I_O = 250 mA			4	6	
	OFF mode	V_I = 6 V			50	100	µA
SVR	Supply voltage rejection	I_O = 5 mA V_I = 5.3 ± 1 V	f = 120 Hz		73		dB
			f = 1 kHz		70		
			f = 10 kHz		55		
eN	Output noise voltage	B = 10 Hz to 100 kHz			50		µV
V_d	Dropout voltage (1)	I_O = 250 mA			0.4	0.6	V
		I_O = 250 mA, T_A= -40 to 125°C				0.8	V
V_{IL}	Control input logic low	T_A = -40 to 125 °C				0.8	V
V_{IH}	Control input logic high	T_A = -40 to 125 °C		2			V
I_I	Control input current	V_I = 6 V, V_C = 6 V			10		µA
C_O	Output bypass capacitance	ESR = 0.1 to 10 Ω, I_O = 0 to 250 mA		2	10		µF

1. For SO-8 package the maximum limit of load regulation and dropout should be increased by 20 mV.

(Refer to the test circuits, T_A = -40 to 125 °C, C_I = 0.1 µF, C_O = 2.2 µF unless otherwise specified).

Table 7. L4931Cxx33-TRY (automotive-grade) electrical characteristics

Symbol	Parameter	Test conditions		Min.	Typ.	Max.	Unit
V_O	Output voltage	I_O = 5 mA, V_I = 5.3 V, T_A = 25 °C		3.234	3.3	3.366	V
		I_O = 5 mA, V_I = 5.3 V		3.168		3.432	
V_I	Operating input voltage	I_O = 250 mA				20	V
I_{out}	Output current limit	T_A = 25 °C			300		mA
ΔV_O	Line regulation	V_I = 4.1 to 20 V, I_O = 0.5 mA				20	mV
ΔV_O	Load regulation	V_I = 4.3 V, I_O = 0.5 to 250 mA				38	mV
I_d	Quiescent current ON mode	V_I = 4.3 to 20 V, I_O = 0 mA				1	mA
		V_I = 4.3 to 20 V, I_O = 250 mA				6	
	OFF mode	V_I = 6 V				100	µA
SVR	Supply voltage rejection	I_O = 5 mA V_I = 5.3 ± 1 V T_A = 25 °C	f = 120 Hz		73		dB
			f = 1 kHz		70		
			f = 10 kHz		55		
eN	Output noise voltage	B = 10 Hz to 100 kHz, T_A = 25 °C			50		µV
V_d	Dropout voltage	I_O = 250 mA, T_A = 25 °C			0.4	0.6	V
		I_O = 250 mA				0.82	V
V_{IL}	Control input logic low					0.82	V
V_{IH}	Control input logic high			2			V
I_I	Control input current	V_I = 6 V, V_C = 6 V, T_A = 25 °C			10		µA
C_O	Output bypass capacitance	ESR = 0.1 to 10 Ω, I_O = 0 to 250 mA, T_A = 25 °C		2	10		µF

(Refer to the test circuits, T_A = 25 °C, C_I = 0.1 µF, C_O = 2.2 µF unless otherwise specified).

Table 8. L4931ABxx35 electrical characteristics

Symbol	Parameter	Test conditions		Min.	Typ.	Max.	Unit
V_O	Output voltage	I_O = 5 mA, V_I = 5.5 V		3.465	3.5	3.535	V
		I_O = 5 mA, V_I = 5.5 V, T_A=-25 to 85 °C		3.43		3.57	
V_I	Operating input voltage	I_O = 250 mA				20	V
I_{out}	Output current limit				300		mA
ΔV_O	Line regulation	V_I = 4.2 to 20 V, I_O = 0.5 mA			3	15	mV
ΔV_O	Load regulation (1)	V_I = 4.4 V, I_O = 0.5 to 250 mA			3	15	mV
I_d	Quiescent current ON mode	V_I = 4.4 to 20 V, I_O = 0 mA			0.6	1	mA
		V_I = 4.4 to 20 V, I_O = 250 mA			4	6	
	OFF mode	V_I = 6 V			50	100	µA
SVR	Supply voltage rejection	I_O = 5 mA V_I = 5.4 ± 1 V	f = 120 Hz		73		dB
			f = 1 kHz		70		
			f = 10 kHz		55		
eN	Output noise voltage	B = 10 Hz to 100 kHz			50		µV
V_d	Dropout voltage (1)	I_O = 250 mA			0.4	0.6	V
		I_O = 250 mA, T_A= -40 to 125 °C				0.8	V
V_{IL}	Control input logic low	T_A = -40 to 125 °C				0.8	V
V_{IH}	Control input logic high	T_A = -40 to 125 °C		2			V
I_I	Control input current	V_I = 6 V, V_C = 6 V			10		µA
C_O	Output bypass capacitance	ESR = 0.1 to 10 Ω, I_O = 0 to 250 mA		2	10		µF

1. For SO-8 package the maximum limit of load regulation and dropout should be increased by 20 mV.

(Refer to the test circuits, T_A = 25 °C, C_I = 0.1 µF, C_O = 2.2 µF unless otherwise specified).

Table 9. L4931Cxx35 electrical characteristics

Symbol	Parameter	Test conditions		Min.	Typ.	Max.	Unit
V_O	Output voltage	I_O = 5 mA, V_I = 5.5 V		3.43	3.5	3.57	V
		I_O = 5 mA, V_I = 5.5 V, T_A=-25 to 85 °C		3.36		3.64	
V_I	Operating input voltage	I_O = 250 mA				20	V
I_{out}	Output current limit				300		mA
ΔV_O	Line regulation	V_I = 4.3 to 20 V, I_O = 0.5 mA			3	18	mV
ΔV_O	Load regulation (1)	V_I = 4.5 V, I_O = 0.5 to 250 mA			3	18	mV
I_d	Quiescent current ON mode	V_I = 4.5 to 20 V, I_O = 0 mA			0.6	1	mA
		V_I = 4.5 to 20 V, I_O = 250 mA			4	6	
	OFF mode	V_I = 6 V			50	100	µA
SVR	Supply voltage rejection	I_O = 5 mA V_I = 5.5 ± 1 V	f = 120 Hz		73		dB
			f = 1 kHz		70		
			f = 10 kHz		55		
eN	Output noise voltage	B = 10 Hz to 100 kHz			50		µV
V_d	Dropout voltage (1)	I_O = 250 mA			0.4	0.6	V
		I_O = 250 mA, T_A= -40 to 125 °C				0.8	V
V_{IL}	Control input logic low	T_A= -40 to 125 °C				0.8	V
V_{IH}	Control input logic high	T_A= -40 to 125 °C		2			V
I_I	Control input current	V_I = 6 V, V_C = 6 V			10		µA
C_O	Output bypass capacitance	ESR = 0.1 to 10 Ω, I_O = 0 to 250 mA		2	10		µF

1. For SO-8 package the maximum limit of load regulation and dropout should be increased by 20 mV.

(Refer to the test circuits, T_A = 25 °C, C_I = 0.1 µF, C_O = 2.2 µF unless otherwise specified).

Table 10. L4931ABxx50 electrical characteristics

Symbol	Parameter	Test conditions		Min.	Typ.	Max.	Unit
V_O	Output voltage	I_O = 5 mA, V_I = 7 V		4.95	5	5.05	V
		I_O = 5 mA, V_I = 7 V, T_A=-25 to 85 °C		4.9		5.1	
V_I	Operating input voltage	I_O = 250 mA				20	V
I_{out}	Output current limit				300		mA
ΔV_O	Line regulation	V_I = 5.8 to 20 V, I_O = 0.5 mA			3.5	17.5	mV
ΔV_O	Load regulation (1)	V_I = 6 V, I_O = 0.5 to 250 mA			3	15	mV
I_d	Quiescent current ON mode	V_I = 6 to 20 V, I_O = 0 mA			0.6	1	mA
		V_I = 6 to 20 V, I_O = 250 mA			4	6	
	OFF mode	V_I = 6 V			50	100	µA
SVR	Supply voltage rejection	I_O = 5 mA V_I = 7 ± 1 V	f = 120 Hz		70		dB
			f = 1 kHz		67		
			f = 10 kHz		55		
eN	Output noise voltage	B = 10 Hz to 100 kHz			50		µV
V_d	Dropout voltage (1)	I_O = 250 mA			0.4	0.6	V
		I_O = 250 mA, T_A= -40 to 125 °C				0.8	V
V_{IL}	Control input logic low	T_A = -40 to 125 °C				0.8	V
V_{IH}	Control input logic high	T_A = -40 to 125 °C		2			V
I_I	Control input current	V_I = 6 V, V_C = 6 V			10		µA
C_O	Output bypass capacitance	ESR = 0.1 to 10 Ω, I_O = 0 to 250 mA		2	10		µF

1. For SO-8 package the maximum limit of load regulation and dropout should be increased by 20 mV.

(Refer to the test circuits, T_A = 25 °C, C_I = 0.1 µF, C_O = 2.2 µF unless otherwise specified).

Table 11. L4931Cxx50 electrical characteristics

Symbol	Parameter	Test conditions		Min.	Typ.	Max.	Unit
V_O	Output voltage	I_O = 5 mA, V_I = 7 V		4.9	5	5.1	V
		I_O = 5 mA, V_I = 7 V, T_A=-25 to 85 °C		4.8		5.2	
V_I	Operating input voltage	I_O = 250 mA				20	V
I_{out}	Output current limit				300		mA
ΔV_O	Line regulation	V_I = 5.8 to 20 V, I_O = 0.5 mA			3.5	17.5	mV
ΔV_O	Load regulation (1)	V_I = 6 V, I_O = 0.5 to 250 mA			3	15	mV
I_d	Quiescent current ON mode	V_I = 6 to 20 V, I_O = 0 mA			0.6	1	mA
		V_I = 6 to 20 V, I_O = 250 mA			4	6	
	OFF mode	V_I = 6 V			50	100	µA
SVR	Supply voltage rejection	I_O = 5 mA V_I = 7 ± 1 V	f = 120 Hz		70		dB
			f = 1 kHz		67		
			f = 10 kHz		55		
eN	Output noise voltage	B = 10 Hz to 100 kHz			50		µV
V_d	Dropout voltage (1)	I_O = 250 mA			0.4	0.6	V
		I_O = 250 mA, T_A= -40 to 125 °C				0.8	V
V_{IL}	Control input logic low	T_A = -40 to 125 °C				0.8	V
V_{IH}	Control input logic high	T_A = -40 to 125 °C		2			V
I_I	Control input current	V_I = 6 V, V_C = 6 V			10		µA
C_O	Output bypass capacitance	ESR = 0.1 to 10 Ω, I_O = 0 to 250 mA		2	10		µF

1. For SO-8 package the maximum limit of load regulation and dropout should be increased by 20 mV.

(Refer to the test circuits, T_A = 25 °C, C_I = 0.1 µF, C_O = 2.2 µF unless otherwise specified).

Table 12. L4931ABxx80 electrical characteristics

Symbol	Parameter	Test conditions		Min.	Typ.	Max.	Unit
V_O	Output voltage	I_O = 5 mA, V_I = 10 V		7.92	8	8.08	V
		I_O = 5 mA, V_I = 10 V, T_A=-25 to 85 °C		7.84		8.16	
V_I	Operating input voltage	I_O = 250 mA				20	V
I_{out}	Output current limit				300		mA
ΔV_O	Line regulation	V_I = 8.8 to 20 V, I_O = 0.5 mA			4	20	mV
ΔV_O	Load regulation (1)	V_I = 9 V, I_O = 0.5 to 250 mA			3	15	mV
I_d	Quiescent current ON mode	V_I = 9 to 20 V, I_O = 0 mA			0.8	1.6	mA
		V_I = 9 to 20 V, I_O = 250 mA			4.5	7	
	OFF mode	V_I = 6 V			70	140	µA
SVR	Supply voltage rejection	I_O = 5 mA V_I = 10 ± 1 V	f = 120 Hz		67		dB
			f = 1 kHz		64		
			f = 10 kHz		55		
eN	Output noise voltage	B = 10 Hz to 100 kHz			50		µV
V_d	Dropout voltage (1)	I_O = 250 mA			0.4	0.6	V
		I_O = 250 mA, T_A= -40 to 125 °C				0.8	V
V_{IL}	Control input logic low	T_A = -40 to 125 °C				0.8	V
V_{IH}	Control input logic high	T_A = -40 to 125 °C		2			V
I_I	Control input current	V_I = 6 V, V_C = 6 V			10		µA
C_O	Output bypass capacitance	ESR = 0.1 to 10 Ω, I_O = 0 to 250 mA		2	10		µF

1. For SO-8 package the maximum limit of load regulation and dropout should be increased by 20 mV.

(Refer to the test circuits, T_A = 25 °C, C_I = 0.1 µF, C_O = 2.2 µF unless otherwise specified).

Table 13. L4931Cxx80 electrical characteristics

Symbol	Parameter	Test conditions		Min.	Typ.	Max.	Unit
V_O	Output voltage	I_O = 5 mA, V_I = 10 V		7.84	8	8.16	V
		I_O = 5 mA, V_I = 10 V, T_A=-25 to 85 °C		7.68		8.32	
V_I	Operating input voltage	I_O = 250 mA				20	V
I_{out}	Output current limit				300		mA
ΔV_O	Line regulation	V_I = 8.9 to 20 V, I_O = 0.5 mA			4	24	mV
ΔV_O	Load regulation (1)	V_I = 9.1 V, I_O = 0.5 to 250 mA			3	18	mV
I_d	Quiescent current ON mode	V_I = 9.1 to 20 V, I_O = 0 mA			0.8	1.6	mA
		V_I = 9.1 to 20 V, I_O = 250 mA			4.5	7	
	OFF mode	V_I = 6 V			70	140	µA
SVR	Supply voltage rejection	I_O = 5 mA V_I = 10.1 ± 1 V	f = 120 Hz		67		dB
			f = 1 kHz		64		
			f = 10 kHz		55		
eN	Output noise voltage	B = 10 Hz to 100 kHz			50		µV
V_d	Dropout voltage (1)	I_O = 250 mA			0.4	0.6	V
		I_O = 250 mA, T_A= -40 to 125 °C				0.8	V
V_{IL}	Control input logic low	T_A = -40 to 125 °C				0.8	V
V_{IH}	Control input logic high	T_A = -40 to 125 °C		2			V
I_I	Control input current	V_I = 6 V, V_C = 6 V			10		µA
C_O	Output bypass capacitance	ESR = 0.1 to 10 Ω, I_O = 0 to 250 mA		2	10		µF

1. For SO-8 package the maximum limit of load regulation and dropout should be increased by 20 mV.

(Refer to the test circuits, T_A = 25 °C, C_I = 0.1 µF, C_O = 2.2 µF unless otherwise specified).

Table 14. L4931ABxx120 electrical characteristics

Symbol	Parameter	Test conditions		Min.	Typ.	Max.	Unit
V_O	Output voltage	I_O = 5 mA, V_I = 14 V		11.88	12	12.12	V
		I_O = 5 mA, V_I = 14 V, T_A=-25 to 85 °C		11.76		12.24	
V_I	Operating input voltage	I_O = 250 mA				20	V
I_{out}	Output current limit				300		mA
ΔV_O	Line regulation	V_I = 12.8 to 20 V, I_O = 0.5 mA			4	20	mV
ΔV_O	Load regulation (1)	V_I = 13 V, I_O = 0.5 to 250 mA			3	15	mV
I_d	Quiescent current ON mode	V_I = 13 to 20 V, I_O = 0 mA			0.8	1.6	mA
		V_I = 13 to 20 V, I_O = 250 mA			4.5	7	
	OFF mode	V_I = 6 V			90	180	µA
SVR	Supply voltage rejection	I_O = 5 mA V_I = 14 ± 1 V	f = 120 Hz		64		dB
			f = 1 kHz		61		
			f = 10 kHz		55		
eN	Output noise voltage	B = 10 Hz to 100 kHz			50		µV
V_d	Dropout voltage (1)	I_O = 250 mA			0.4	0.6	V
		I_O = 250 mA, T_A= -40 to 125 °C				0.8	V
V_{IL}	Control input logic low	T_A = -40 to 125 °C				0.8	V
V_{IH}	Control input logic high	T_A = -40 to 125 °C		2			V
I_I	Control input current	V_I = 6 V, V_C = 6 V			10		µA
C_O	Output bypass capacitance	ESR = 0.1 to 10 Ω, I_O = 0 to 250 mA		2	10		µF

1. For SO-8 package the maximum limit of load regulation and dropout should be increased by 20 mV.

(Refer to the test circuits, T_A = 25 °C, C_I = 0.1 µF, C_O = 2.2 µF unless otherwise specified).

Table 15. L4931Cxx120 electrical characteristics

Symbol	Parameter	Test conditions		Min.	Typ.	Max.	Unit
V_O	Output voltage	I_O = 5 mA, V_I = 14 V		11.76	12	12.24	V
		I_O = 5 mA, V_I = 14 V, T_A=-25 to 85 °C		11.52		12.48	
V_I	Operating input voltage	I_O = 250 mA				20	V
I_{out}	Output current limit				300		mA
ΔV_O	Line regulation	V_I = 12.9 to 20 V, I_O = 0.5 mA			4	24	mV
ΔV_O	Load regulation (1)	V_I = 13.1 V, I_O = 0.5 to 250 mA			3	18	mV
I_d	Quiescent current ON mode	V_I = 13.1 to 20 V, I_O = 0 mA			0.8	1.6	mA
		V_I = 13.1 to 20 V, I_O = 250 mA			4.5	7	
	OFF mode	V_I = 6 V			90	180	µA
SVR	Supply voltage rejection	I_O = 5 mA V_I = 14.1 ± 1 V	f = 120 Hz		64		dB
			f = 1 kHz		61		
			f = 10 kHz		55		
eN	Output noise voltage	B = 10 Hz to 100 kHz			50		µV
V_d	Dropout voltage (1)	I_O = 250 mA			0.4	0.6	V
		I_O = 250 mA, T_A= -40 to 125 °C				0.8	V
V_{IL}	Control input logic low	T_A = -40 to 125 °C				0.8	V
V_{IH}	Control input logic high	T_A = -40 to 125 °C		2			V
I_I	Control input current	V_I = 6 V, V_C = 6 V			10		µA
C_O	Output bypass capacitance	ESR = 0.1 to 10 Ω, I_O = 0 to 250 mA		2	10		µF

1. For SO-8 package the maximum limit of load regulation and dropout should be increased by 20 mV.

6 Typical application

Figure 4. Line regulation vs temperature

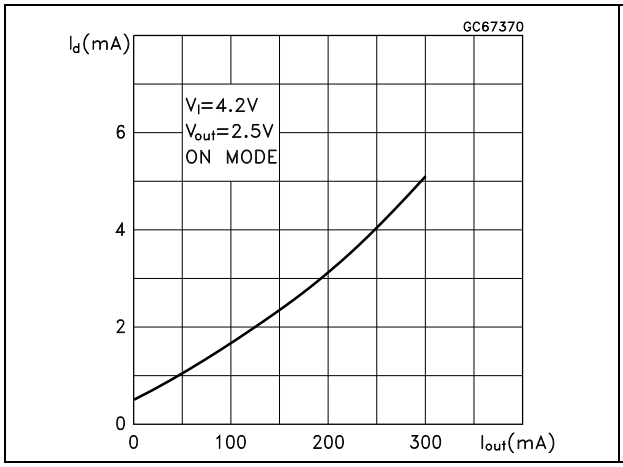


Figure 5. Dropout voltage vs temperature

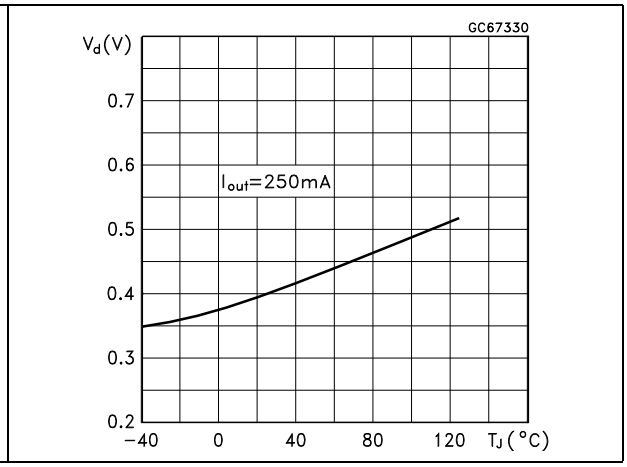


Figure 6. Supply current vs input voltage

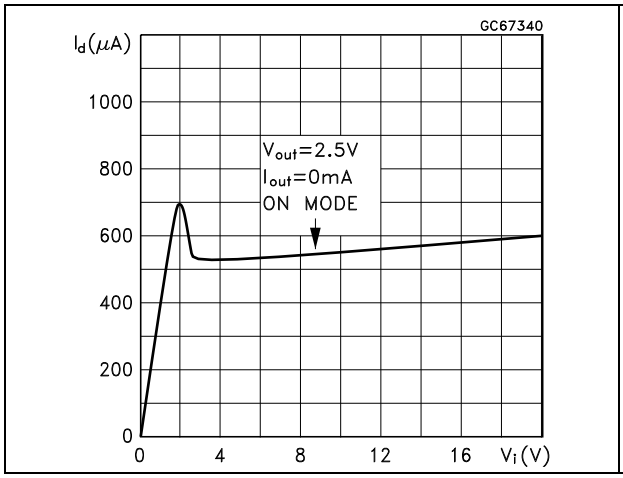


Figure 7. Supply current vs temperature

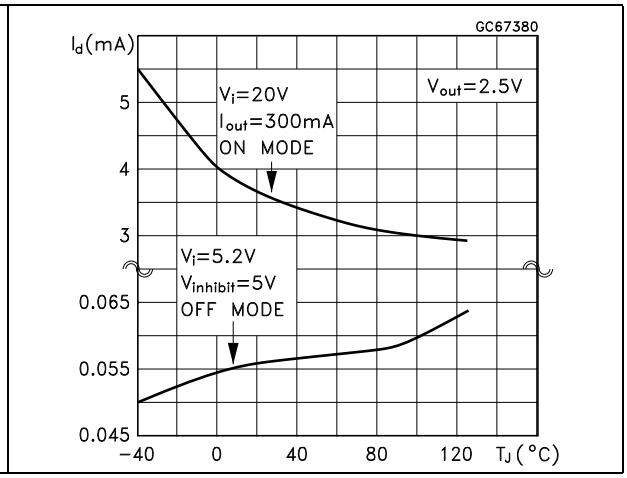


Figure 8. Short-circuit current vs dropout voltage

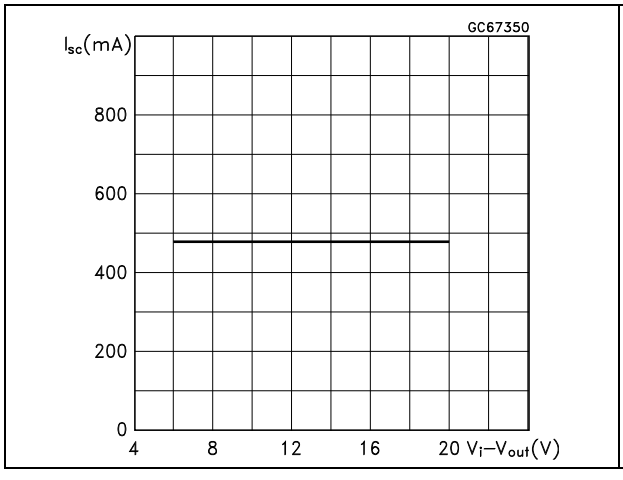
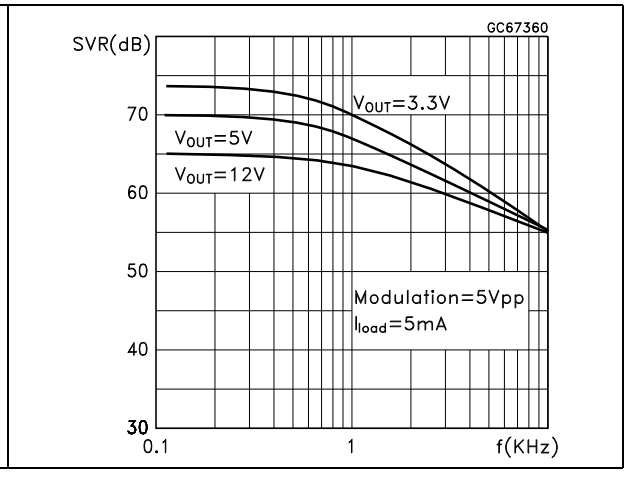


Figure 9. SVR vs input voltage signal frequency



7 Package mechanical data

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK® packages, depending on their level of environmental compliance. ECOPACK® specifications, grade definitions and product status are available at: *www.st.com*. ECOPACK® is an ST trademark.

Table 16. TO-92 mechanical data

Dim.	mm		
	Min.	Typ.	Max.
A	4.32		4.95
b	0.36		0.51
D	4.45		4.95
E	3.30		3.94
e	2.41		2.67
e1	1.14		1.40
L	12.70		15.49
R	2.16		2.41
S1	0.92		1.52
W	0.41		0.56
V		5°	

Figure 10. TO-92 drawings

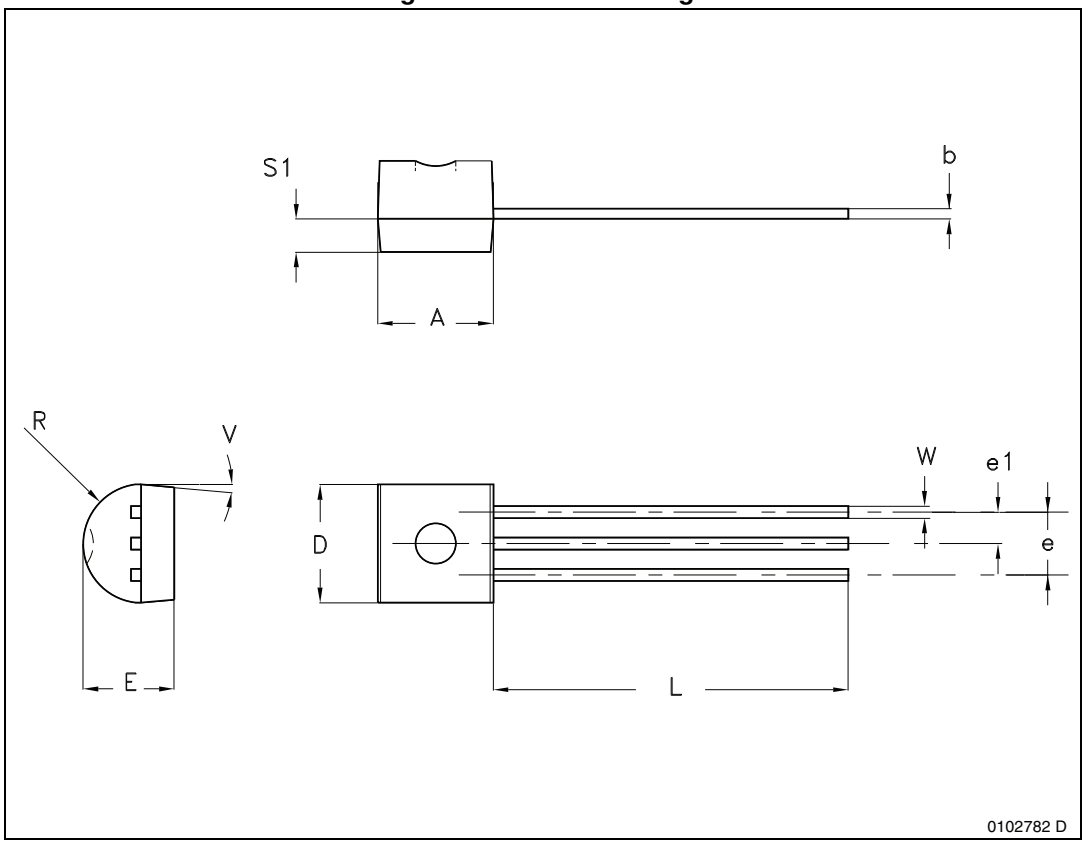


Table 17. PPAK mechanical data

Dim.	mm		
	Min.	Typ.	Max.
A	2.2		2.4
A1	0.9		1.1
A2	0.03		0.23
B	0.4		0.6
B2	5.2		5.4
C	0.45		0.6
C2	0.48		0.6
D	6		6.2
D1		5.1	
E	6.4		6.6
E1		4.7	
e		1.27	
G	4.9		5.25
G1	2.38		2.7
H	9.35		10.1
L2		0.8	1
L4	0.6		1
L5	1		
L6		2.8	
R		0.20	
V2	0°		8°