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# SANYO Semiconductors DATA SHEET



# **Monolithic Linear IC LA42052** — Audio Output for TV application $5W \times 2ch$ Power Amplifier

#### **Overview**

LA42052 is 5W 2-channel AF power amplifier intended for televisions.

#### **Functions**

- 5W  $\times$  2 channels (V<sub>CC</sub> = 18V, R<sub>L</sub> = 8 $\Omega$ )
- Standby function
- Pop noise reduction function
- Ripple filter
- Thermal protection circuit

LA42000 series is power IC which made Pin compatible altogether in 5 to 15W. They consist of four kinds of power ICs (mono, stereo, mono with volume function, stereo with volume function. They realized PCB layout communalization of an audio power block of TV).

Madalasana	PO	Cha	Mahana	
Model name		Monaural	Stereo	Volume
LA42051	5W	0		
LA42052	5W		0	
LA42351	5W	0		0
LA42352	5W		0	0
LA42071	7W	0		
LA42072	7W		0	
LA42152	15W		0	

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# Specifications

### **Maximum Ratings** at $Ta = 25^{\circ}C$

Parameter	Symbol	Conditions	Ratings	unit
Maximum supply voltage	V <sub>CC</sub> max	No signal	24	V
Allowable power dissipation	Pd max	Infinitely heat sink	15	W
Maximum junction temperature	Tj max		150	°C
Thermal resistance	θјс		3	°C/W
Operating temperature	Topr		-25 to +75	°C
Storage temperature	Tstg		-40 to +150	°C

#### **Operating Conditions** at $Ta = 25^{\circ}C$

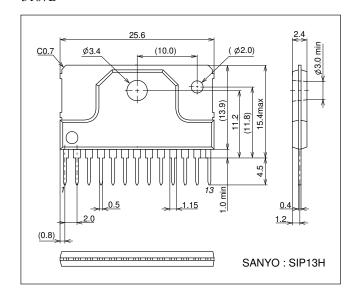
Parameter	Symbol	Conditions	Ratings	unit
Recommended supply voltage	V <sub>CC</sub>		18	V
Recommended load resistance	RL		8	Ω
Allowable operating voltage range	V <sub>CC</sub> op		5.5 to 22	V

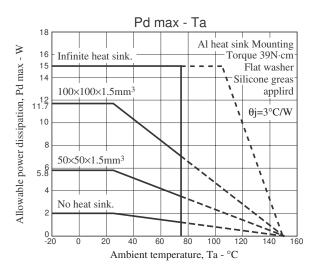
#### **Electrical Characteristics** at Ta = 25°C, $V_{CC}$ = 18V, $R_L$ = 8 $\Omega$ , f = 1kHz, Rg=600 $\Omega$

Poromotor	Parameter Symbol Conditions	Quaditiona	Ratings			11-24
Parameter		min	typ	max	Unit	
Standby current	ISTB	Amplifier off		1	10	μA
Quiescent current	Icco	Rg = 0	18	50	100	mA
Output power	PO	THD = 10%	4	5		W
Total harmonic distortion	THD	P <sub>O</sub> = 1W		0.15	0.4	%
Voltage gain	VG	$V_{O} = 0 dBm$	33	35	37	dB
Output noise voltage	V <sub>NO</sub>	Rg = 0, $BPF = 20Hz$ to $20kHz$		0.05	0.25	mVrms
Ripple rejection	SVRR	$Rg = 0, f_R = 100Hz, V_{CC}R = 0dBm$	50	60		dB
Channel separation	CH Sep.	$Rg = 10k\Omega, V_O = 0dBm$	50	60		dB
Standby control voltage	V <sub>STB</sub> -H	Amplifier on	1.0		5	V
(The Pin 5 voltage)	V <sub>STB</sub> -L	Amplifier off	0		0.5	V
Input resistance	Ri		21	30	39	kΩ

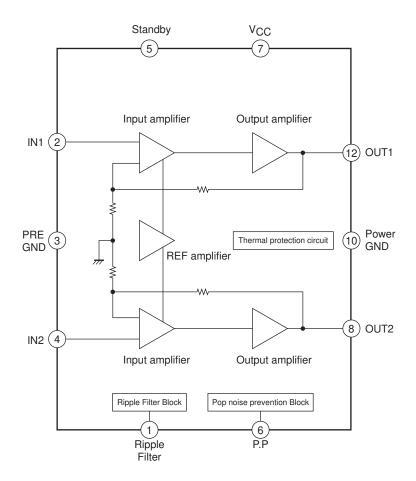
# Package Dimensions

unit : mm 3107B

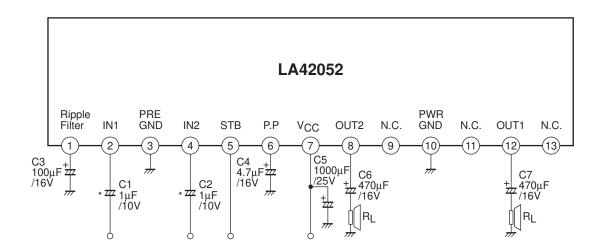




## **Block Diagram**



# **Application Circuit Example**



#### **External Components**

C1, C2 : Input coupling capacitors, which are recommended to be  $1.0\mu F$  or less.

The input pin voltage  $2V_{BE}$  ( about 1.4V).

Determine the polarity orientation of these capacitors based on the DC current from the circuit connected to the LA42052 front end.

- C3 : The recommended value for the Ripple filter capacitor is  $100\mu$ F.
- C4 : The recommended value for the P.P capacitor  $4.7\mu$ F. It is a capacitor for there being small Pop sound.
- C5 : Power supply capacitor.
- C6, C7 : The recommended value for the output capacitor is  $470\mu F$ .
- 1. Standby Function (Pin 5)

By means of controlling pin 5 to high and low, the power Supply can be set to ON and OFF.

Control Voltage of pin 5

Pin 5 voltage	Amplifier	Standby		
0 to 0.5	OFF	ON		
1.0 to 5.0	ON	OFF		

When the impression voltage of VS is high, I want to stop 5 pin inflow current. Restriction resistance (RSTB) is inserted in a case.

 $VS = 5V, R_{STB} = 5.1k\Omega$ 5 pin inflow current = about 750µA 5 pin voltage = about 1.2V

2. Ripple filter and Mute function (Pin 1)

Pin voltage is approx.  $1/2 V_{CC}$ 

The recommended value for the Ripple filter capacitor is  $100\mu F$ . Muting :

The output signal can be controlled by connecting pin 1(Ripple filter) to ground via a resistance of 300 to  $500\Omega$ . If resistance is higher than  $750\Omega$ , the suppression ratio will decrease.

3. Input Pin (Pin 2,4)

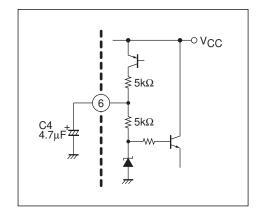
Voltage at the input pins is approx.  $2V_{BE}(about 1.4V)$ Determine the polarity orientation of these capacitors based on the DC current from the circuit Connected to the LA42052 front end. Input resistance is approx.  $30k\Omega$  (typ)

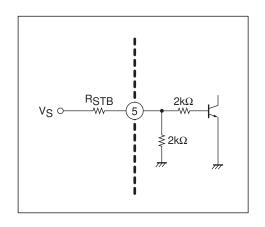
The recommended value for the input capacitor is  $1.0 \mu F.$ 

4. P.P (Pin 6)

The Pin 6 is Terminal for there being small pop sound at the time of power supply ON/OFF. C4 recommends  $4.7\mu$ F. When capacitance is higher  $10\mu$ F, the sound will not be cut off when setting the power supply OFF.

Pin 6 voltage = 
$$\frac{V_{CC} - V_{CE} (about 0.3V) - 5.6V}{2} + 5.6V$$



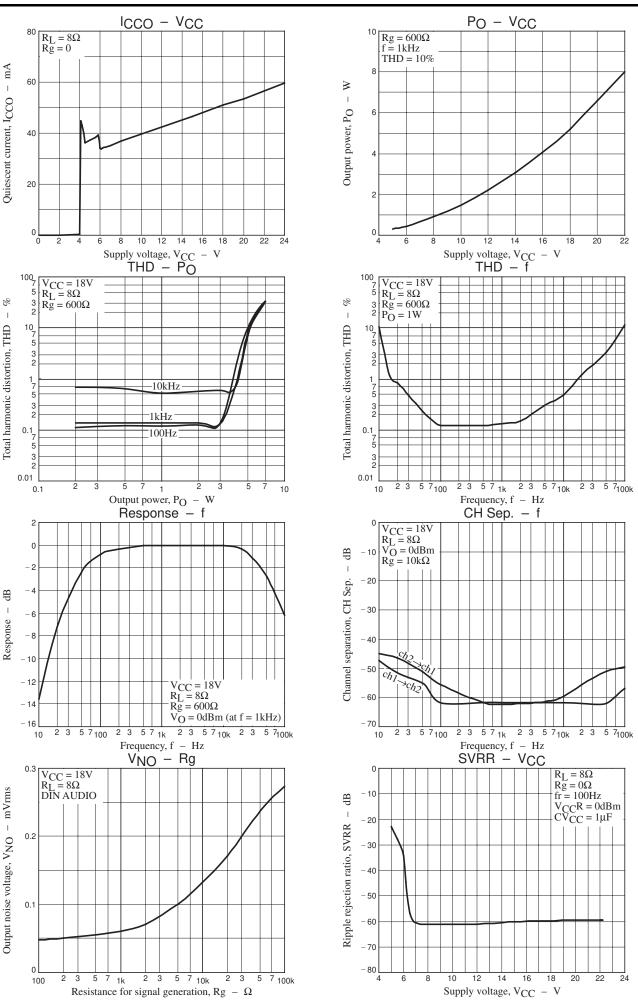


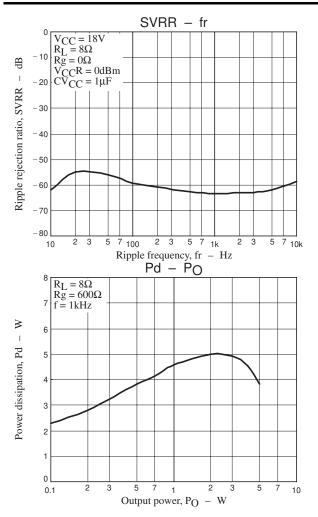
## **Usage Notes**

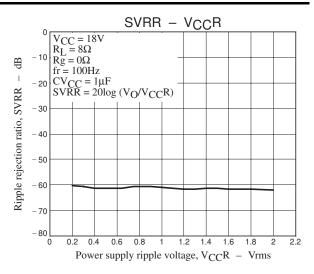
- 1. Lightening (power supply output short-circuit) and ground fault (GND output short-circuit) protective circuits are not incorporated. Due care must be taken during handling of the product.
- 2. The thermal protective circuit is incorporated and activated when the junction temperature (Tj) rises above about 160°C.

This circuit controls the output gradually to the attenuated condition.

3. Always ensure the sufficient margin for the supply voltage, etc. to operate the product within the area where the maximum rating will never be exceeded. Namely, the maximum rating may be exceeded due to the slight change in conditions, resulting in damage, if the product is used around the maximum rating.







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