

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



# Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

Email & Skype: info@chipsmall.com Web: www.chipsmall.com

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China







# **LA6585FA**

**Monolithic Linear IC** 

# BTL Drive Single-Phase Full-Wave Fan Motor Driver



http://onsemi.com

#### Overview

The LA6585FA is a single-phase bipolar fan motor driver that achieves quite operation, power savings, silent operation and high efficiency that suppresses reactive current through BTL output linear drive. It provides lock protection and rotation signal circuits on chip, and is optimal for applications that require high reliability and low noise, such as notebook personal computers, power supplies in consumer electronic equipment, car audio, and CPU cooling systems.

#### **Features**

- BTL output single-phase full-wave linear drive (gain resistor : 1 to  $360k\Omega$ , 51dB)
- Supports low-voltage drive and features a wide usable voltage range (2.2 to 14.0V)
- Low saturation output (high side + low side saturation voltage : Vosat (total) = 1.2V (typical), IQ = 250mA)
- Built-in lock protection and automatic return circuits
- Built-in FG output
- Built-in Hall sensor bias (VHB = 1.5V)
- Thermal protection circuit
- Small-sized, high thermal capacity package

#### **Specifications**

#### **Absolute Maximum Ratings** at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Supply voltage	V <sub>CC</sub> max		15	٧
Output current	I <sub>OUT</sub> max		0.5	Α
Output voltage	V <sub>OUT</sub> max		15	٧
FG output pin oiutput withstand voltage	V <sub>FG</sub> max		15	V
FG output current	I <sub>FG</sub> max		10	mA
Allowable power dissipation	Pd max	When mounted on a circuit board *1	400	mW
Operating temperature	Topr		-30 to +90	°C
Storage temperature	Tstg		-55 to +150	°C

<sup>\*1</sup> Specified circuit board :  $114.3 \times 76.1 \times 1.6 \text{mm}^3$ , paper phenol.

Caution 1) Absolute maximum ratings represent the value which cannot be exceeded for any length of time.

Caution 2) Even when the device is used within the range of absolute maximum ratings, as a result of continuous usage under high temperature, high current, high voltage, or drastic temperature change, the reliability of the IC may be degraded. Please contact us for the further details.

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

### **LA6585FA**

#### **Recommended Operating Conditions** at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Supply voltage	V <sub>CC</sub>		2.2 to 14.0	V
Common-phase input voltage	VICM		0 to V <sub>CC</sub> -1.5	٧
range of hall input				

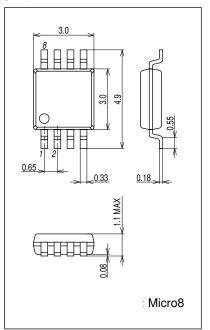
## **Electrical Characteristics** at Ta = 25 °C, $V_{CC} = 12V$ , Unless otherwise specified.

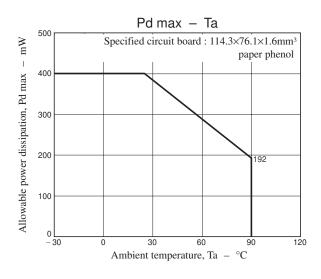
Parameter	Symbol	0 - 111	Ratings			11.2
Parameter		Conditions	min	typ	max	Unit
Circuit current	I <sub>CC</sub> 1	Drive mode (CT = low)	3	6	9	mA
	I <sub>CC</sub> 2	Lock protection mode (CT = high)	2.5	5	7.5	mA
Lock detection capacitor charge current	ICT1		0.9	1.2	1.5	μΑ
Capacitor discharge current	ICT2		0.10	0.18	0.25	μΑ
Capacitor charge/discharge current ratio	RCT	RCD = ICT1/ICT2	5	6.5	8	
CT charge voltage	VCT1		1.3	1.5	1.7	V
CT discharge voltage	VCT2		0.3	0.5	0.7	V
OUT output low saturation voltage	V <sub>OL</sub>	I <sub>O</sub> = 200mA		0.25	0.45	V
OUT output high saturation voltage	V <sub>OH</sub>	I <sub>O</sub> = 200mA		0.95	1.2	V
Hall input sensitivity	VHN	Zero peak value (including offset and hysteresis)		7	15	mV
FG output pin low-level voltage	VFG	IFG = 5mA		0.15	0.3	V
FG output pin leakage current	IFGL	VFG = 15V		1	30	μΑ

## **Package Dimensions**

unit: mm (typ)

3427

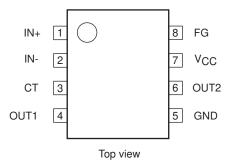




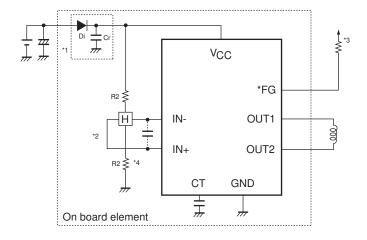
#### **Truth Table**

IN-	IN+	CT	OUT1	OUT2	FG	Mode
High	Low	Low	High	Low	Low	During rotation
Low	High		Low	High	High	
-	-	High	Off	Off	-	Lock protection

#### **Pin Assignment**



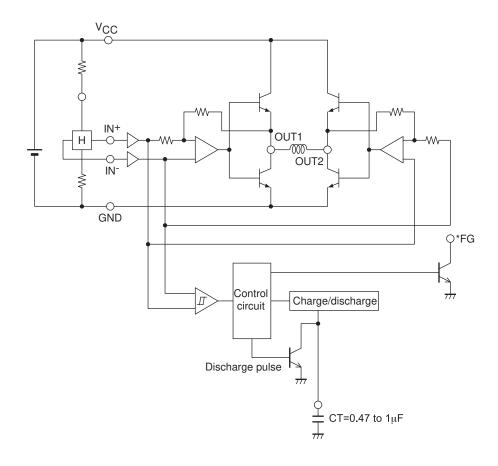
#### **Application Circuit Example**



- \*1. If the diode Di (which protects the IC destruction by reverse connection) is used, it is necessary to insert the capacitor Cr and provide a regenerative current route. Similarly, if there is no nearby capacitor on the fan power supply line, Cr will also be necessary to improve reliability.
- \*2. If the Hall sensor bias is taken from V<sub>CC</sub>, a 1/2 V<sub>CC</sub> bias, as shown in the figure, must be used.

  Linear drive is implemented by amplifying the Hall sensor output and applying voltage control to the coil. If the Hall effect sensor provides a strong output, the startup characteristics and efficiency will be good, then even quieter operation will be achieved by adjusting the Hall effect sensor.
- \*3. This pin must be left open if unused.
- \*4. If the line from the Hall sensor output to the Hall sensor input of IC are long, noise may enter the system from that line. If that becomes a problem, insert a capacitor as shown in the figure.

#### **Internal Equivalent Circuits**



ON Semiconductor and the ON logo are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of SCILLC's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equa