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LA6587FA

Monolithic Linear IC Fan Motor Driver BTL Driver Single-Phase Full-Wave



Overview

The LA6587FA is single-phase bipolar fan motor is driven, through BTL output linear drive, at high efficiency, low power, and low sound by suppressing the reactive power. Lock protection, rotary signal (FG, 1/2FG) circuits are incorporated, which is optimum for the notebook PC, consumer equipment power supply, car audio system, CPU cooler, etc. that require high reliability and low noise.

Functions

- Single-phase full-wave linear drive by BTL output (gain resistance $1k\Omega$ -360k Ω , 51dB)
- : No switching noise, which is optimum for equipment requiring silence, such as consumer equipment power supply, car audio system, etc.
- Low-voltage operation possible, with wide operating voltage range (2.2 to 14.0V)
- Low saturation output (Upper + lower saturation voltage : V_{OSat} (total) = 1.1Vtyp, I_{O} = 100mA)
- : High coil efficiency with low current drain. Additionally, IC itself generates only small heat.
- Built-in lock protection and automatic reset circuits
- Built-in FG & 1/2FG outputs
- Built-in Hall bias ($V_{HB} = 1.5V$)
- Thermal protection circuit
- : When the large current flows due to output short-circuit and the IC chip temperature exceeds 180°C, this protective circuit suppresses the drive current to prevent burn and damage to IC.
- Extra-small package (Micro10)

Specifications

Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit	
Output voltage V _{CC} max			15	V	
Allowable power dissipation	Pd max	Mounted on a specified board*	400	mW	
Output current	I _{OUT} max		0.6	А	
Output withstand voltage VOUT max			15	V	
RD/FG output pin output withstand voltage	V _{RD/FG} max		15	V	
RD/FG output current	I _{RD/FG} max		10	mA	
HB output current	I _B max		10	mA	
Operating temperature	Topr		-30 to +90	°C	
Storage temperature	Tstg		-55 to +150	°C	

* Mounted on a specified board : 20mm×10mm×0.8mm3, paper phenol

Caution 1) Absolute maximum ratings represent the value which cannot be exceeded for any length of time.

Caution 2) Even when the device is used within the range of absolute maximum ratings, as a result of continuous usage under high temperature, high current, high voltage, or drastic temperature change, the reliability of the IC may be degraded. Please contact us for the further details.

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

LA6587FA

Operating Conditions at $Ta = 25^{\circ}C$

Parameter	Symbol	Conditions	Ratings	Unit
Supply voltage	V _{CC}		2.2 to 14.0	V
Common-phase input voltage	VICM		0 to V _{CC} -1.5	V
range of Hall input				

Electrical Characteristics at $Ta = 25^{\circ}C$, $V_{CC} = 12V$, unless especially specified.

Deverselar	Symbol			Ratings		
Parameter		Conditions	min	typ	max	Unit
Circuit Current	ICC1	During drive (CT = L)	4	6	8	mA
	I _{CC} 2	During lock protection (CT = H)	3	4.5	6	mA
Lock detection capacitor charge current	I _{CT} 1		0.9	1.2	1.5	μA
Capacitor discharge current	I _{CT} 2		0.11	0.18	0.25	μA
Capacitor charge and discharge current ratio	R _{CT}	$RCD = I_{CT}1/I_{CT}2$	5	6.5	8	-
CT charge voltage	V _{CT} 1		1.3	1.5	1.6	V
CT discharge voltage	V _{CT} 2	0.3 0.5		0.6	V	
OUT output L saturation voltage	V _{OL}	I _O = 200mA	0.25 0.4		0.45	V
OUT output H saturation voltage	VOH	I _O = 200mA	1.0 1		1.2	V
Hall input sensitivity	V _{HN}	Zero peak value (including offset and hysteresis)			15	mV
FG/1/2FG output pin L voltage	V _{FG}	I _{FG} = 5mA	0.15 0.3		0.3	V
FG/1/2FG output pin leak current	IFGL	V _{FG} = 15V	1 30		30	μA

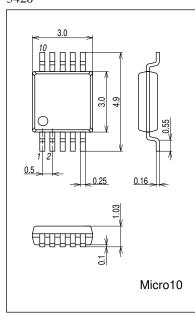
Truth Table

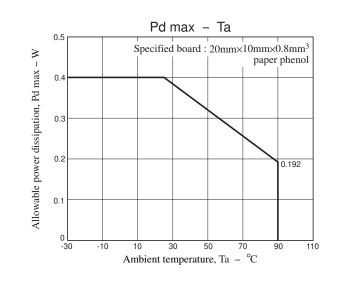
IN-	IN+	СТ	OUT1	OUT2	FG	1/2FG	Mode
Н	L	L	Н	L	L	*	During rotation
L	Н		L	Н	Н		
-	-	Н	off	off	-	-	During overheat protection

 $\ast\,$ 1/2FG provides the output equivalent to the divided FG output.

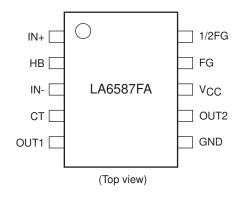
Package Dimensions

unit : mm (typ) 3428

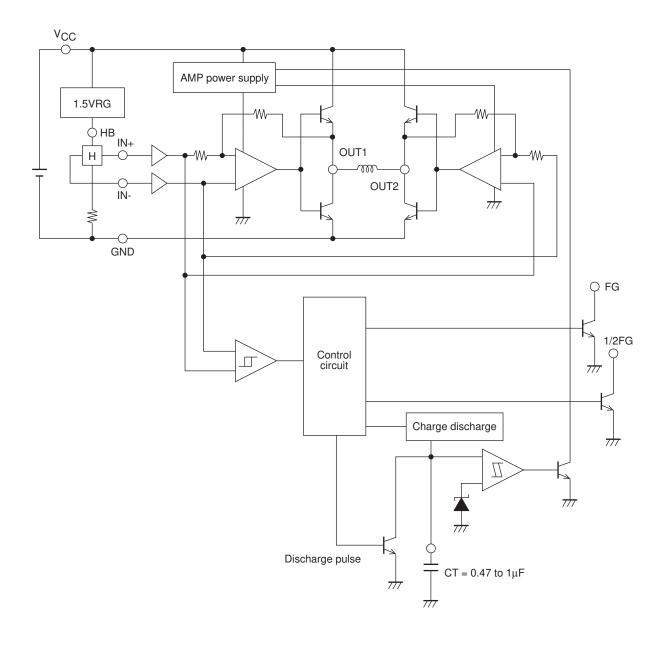




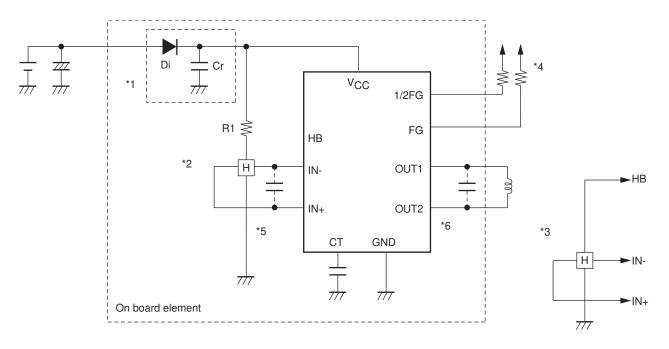
Pin Assignment



Block Diagram



Sample Application Circuit



- *1: When the breakdown protective DI at reverse connection is to be used, it is necessary to insert the capacitor Cr to secure the regenerative current route. Similarly, Cr must be provided to enhance the reliability when there is no capacitor near the fan power line.
- *2 : To obtain Hall bias from V_{CC}, carry out 1V_{CC} bias as shown in the figure. Linear driving is made through voltage control of the coil by amplifying the Hall output. When the Hall element output is large, the startup performance and efficiency are improved. Adjustment of the Hall element can reduce the noise further.
- *3: To obtain Hall bias from the HB pin, carry out constant-voltage bias at about 1.5V, which enables the Hall element to generate the stable Hall output satisfactory in temperature characteristics.
- *4: Keep this open when not using.
- *5: When the wiring from the Hall output to IC Hall input is long, noise may be carried through the wiring. In this case, insert the capacitor as shown in the figure.

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