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With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

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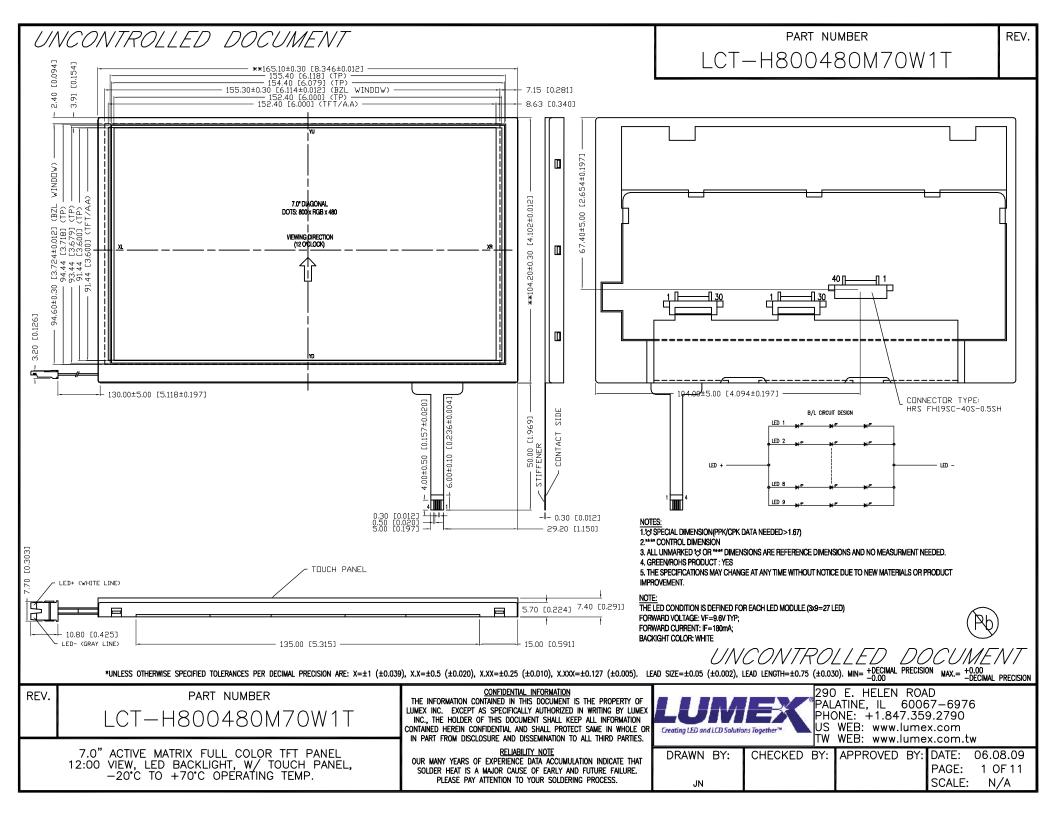
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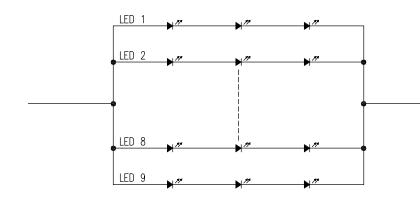




BACKLIGHT SPECIFICATIONS

ITEM	SYMBOL	STA	NDARD VA	ALUE	LINIT	DEMARK	
IILM	SIMBOL	MIN	TYP.	MAX	UNIT	REMARK	
LED VOLTAGE	VL	_	9.6	12	V	NOTE 2	
LED CURRENT	L	-	180	_	mA	NOTE 2	
LED LIFE TIME	ı	20,000	1	_	Hr	NOTE 1	

NOTE (1): THE "LED LIFE TIME" IS DEFINED AS THE MODULE BRIGHTNESS DECREASE TO 50% ORIGINAL BRIGHTNESS THAT THE AMBIENT TEMPERATURE IS 25°C AND IL-20mA. NOTE (2): THE LED DRIVING CONDITION IS DEFINED FOR EACH LED MODULE. (3 LED SERIAL)



ITEM	CONTENTS	UNIT
DISPLAY TECHNOLOGY	7.0" (DIAGONAL) TFT NORMALLY WHITE TRANSMISSIVE LCD	- 1 -
MODULE OUTER DIMENSION	165.1(H) x 104.2 (V) x 6.0(D)(MAX)	mm
PIXEL SIZE	120(H) x 360(V)	μm
ACTIVE DISPLAY AREA	152.40(H) x 91.44(V)	mm
NUMBER OF DOTS	800RGBx480	DOTS
VIEWING DIRECTION	12	DICLOCK
PIXEL ARRANGEMENT	RGB STRIPE	-
PIXEL PITCH	0.0635 (H) x 0.1905 (V)	mm
BACKLIGHT	LED BACK-LIGHT/WHITE	-
PIXEL DRIVING ELEMENT	a-Si TFT	
MPU INTERFACE	TTL DIGITAL	-
DRIVE IC	HX8232-A02(SOCURE) X3 + HX8643-A00(GATE) X1	-
T-CON IC	NT39703-5	-
NUMBER OF COLORS	262K COLORS	-
OPERATING TEMPERATURE	-20~70	ე ი
STORAGE TEMPERATURE	-30~80	°C
WEIGHT	TBD	g

TOUCH PANEL SPECIFICATIONS

ITEM	DESCRIPTION	STANDARD VALUE			CONDITIONS		
II EM	DESCRIPTION	MIN	TYP.	MAX	CONDITIONS		
TOTAL TRANSMITTANCE	_	80%	-	_	JIS K7105		
OPERATING FORCE	PEN INPUT	80g	_	100g	DIAMETER 3.0mm,SR 0.8mm		
UPERATING FURCE	FINGER INPUT	80g	-	100g	DIAMETER 12.0mm,SR 12.5mm		
LOOP RESISTANCE	X AXIS	200	-	900	-		
LUUP RESISTANCE	Y AXIS	400	1	900	-		
INSULATION RESISTANCE		20	_	-	At DC 25V		
KNOCKING LIFE		1000000	_	-	250g, 2HZ SHAPE OF PEN END R 0.8mm		
CHATTERING		_	_	10ms	-		



*UNLESS OTHERWISE SPECIFIED TOLERANCES PER DECIMAL PRECISION ARE: X=±1 (±0.039), X.X=±0.5 (±0.020), X.XX=±0.25 (±0.010), X.XXX=±0.127 (±0.005). LEAD SIZE=±0.05 (±0.002), LEAD LENGTH=±0.75 (±0.030). MIN\(\frac{1}{2}\)DECIMAL PRECISION OF ARE: X=±1 (±0.039), X.X=±0.5 (±0.020), X.XX=±0.127 (±0.005). LEAD SIZE=±0.05 (±0.002), LEAD LENGTH=±0.75 (±0.030). MIN\(\frac{1}{2}\)DECIMAL PRECISION OF ARE: X=±1 (±0.039), X.X=±0.5 (±0.020), X.XX=±0.127 (±0.005). LEAD SIZE=±0.05 (±0.002), LEAD LENGTH=±0.75 (±0.030). MIN\(\frac{1}{2}\)DECIMAL PRECISION OF ARE: X=±1 (±0.039), X.X=±0.5 (±0.010), X.XX=±0.127 (±0.005). LEAD SIZE=±0.05 (±0.002), LEAD LENGTH=±0.75 (±0.030). MIN\(\frac{1}{2}\)DECIMAL PRECISION OF ARE: X=±1 (±0.039), X.X=±0.5 (±0.010), X.XX=±0.127 (±0.005). LEAD SIZE=±0.05 (±0.002), LEAD LENGTH=±0.75 (±0.030). MIN\(\frac{1}{2}\)DECIMAL PRECISION OF ARE: X=±1 (±0.039), X.X=±0.127 (±0.005). LEAD SIZE=±0.05 (±0.002), X.XX=±0.127 (±0.005). LEAD SIZE=±0.05 (±0.002), X.XX=±0.127 (±0.005). LEAD SIZE=±0.05 (±0.002), X.X=±0.127 (±0.005). LEAD SIZE=±0.05 (±0.002), X.X=±0.05 (±0

REV. PART NUMBER LCT-H800480M70W1T

7.0" ACTIVE MATRIX FULL COLOR TFT PANEL 12:00 VIEW, LED BACKLIGHT, W/ TOUCH PANEL, -20°C TO +70°C OPERATING TEMP.

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CHECKED BY: APPROVED BY: DATE: 06.08.09 PAGE: 2 OF 11 SCALE:

N/A

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INTERENCE DIN CONNECTION

PIN NAME DESCRIPTION 1 GND POWER GROUND 2 GND POWER GROUND 3 NC NO CONNECTION 4 VCC POWER SUPPLY FOR DIGITAL CIRCUIT 5 VCC POWER SUPPLY FOR DIGITAL CIRCUIT 6 VCC POWER SUPPLY FOR DIGITAL CIRCUIT 7 VCC POWER SUPPLY FOR DIGITAL CIRCUIT 8 NC NO CONNECTION 9 DE DATA ENABLE 10 GND POWER GROUND 11 GND POWER GROUND 12 GND POWER GROUND 13 B5 BLUE DATA INPUT (MSB) 14 B4 BLUE DATA INPUT 15 B3 BLUE DATA INPUT 16 GND POWER GROUND 17 B2 BLUE DATA INPUT 18 B1 BLUE DATA INPUT 19 B0 BLUE DATA INPUT 20 GND POWER GROUND 21 G5 GREEN	/ INTER	RFACE PIN CO	ONNECTION \
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32 GND POWER GROUND 33 R2 RED DATA INPUT	30		
33 R2 RED DATA INPUT			
	34	R1	RED DATA INPUT
35 RO RED DATA INPUT (LSB)			
36 GND POWER GROUND			
37 GND POWER GROUND			
38 DCLK SAMPLE CLOCK			
39 GND POWER GROUND			
40 GND POWER GROUNG	40	GND	POWER GROUNG

/ BACH	KLIGHT UNIT		
PIN	NAME	DESCRIPTION	REMARK
1	LED+	HIGH VOLTAGE	WHITE LINE
2	LED-	LOW VOLTAGE	GRAY LINE

/ 1	/ TOUCH PANEL PIN CONNECTION \									
PIN	SYMBOL	FUNCTION								
1	XR	TOUCH PANEL OUTPUT RIGHT								
2	YD	TOUCH PANEL OUTPUT DOWN								
3	XL	TOUCH PANEL OUTPUT LEFT								
4	YU	TOUCH PANEL OUTPUT UPPER								

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REV.

PART NUMBER LCT-H800480M70W1T

7.0" ACTIVE MATRIX FULL COLOR TFT PANEL 12:00 VIEW, LED BACKLIGHT, W/ TOUCH PANEL, -20°C TO +70°C OPERATING TEMP.

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PAGE: 3 OF 11 SCALE: N/A

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PART NUMBER

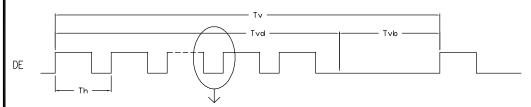
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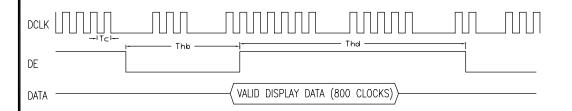
LCT-H800480M70W1T

INTERFACE TIMING

/							
SIGNAL	ITEM	SYMBOL	STA	NDARD VA	ALUE	UNIT	NOTE
SIGNAL	IIEM	SIMBOL	MIN	TYP.	MAX	UNII	NOTE
	FREQUENCY	FC	-	33.3	50	MHz	_
CLOCK	PERIOD	Tc	-	30	25	ns	-
CLUCK	HIGH TIME	Tch	12	-	-	ns	_
	LOW TIME	Tcl	13	-	-	ns	_
DATA	SETUP TIME	Tlvs	5	-	-	ns	-
DATA	HOLD TIME	Tlvh	10	_	-	ns	_
	FRAME RATE	Fr	_	60	72	Hz	Tv=Tvd+Tvb
VERTICAL ACTIVE DISPLAY	TOTAL	Tv	-	525	-	Th	_
TERM	DISPLAY	Tvd	_	480	-	Th	_
	BLANK	Tvb	_	45	_	Th	_
HODIZONIAL ACTIVE DIODIAY	TOTAL	Th	_	928	-	Tc	Th=Thd+Thb
HORIZONTAL ACTIVE DISPLAY	DISPLAY	Thd	_	800	-	Tc	_
1 - 1 / 1/41	BLANK	Thb	_	128	_	Tc	_

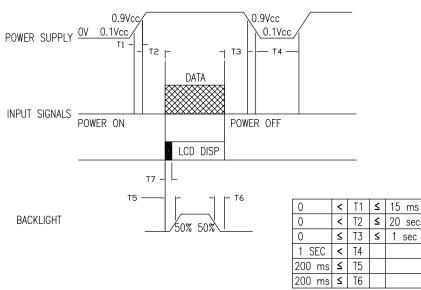
NOTE (1): BECAUSE THIS MODULE IS OPERATED BY DE ONLY MODE, HSYNC AND VSYNC INPUT SIGNALS SHOULD BE SET TO LOW LOGIC LEVEL OR GROUND. OTHERWISE, THIS MODULE WOULD OPERATE ABNORMALLY.





POWER ON/OFF SEQUENCE

TO PREVENT A LATCH-UP OR DC OPERATION OF LCD MODULE, THE POWER ON/OFF SEQUENCE SHOULD FOLLOW THE CONDITIONS SHOWN IN THE FOLLOWING DIAGRAM.



NOTES:

- (1) THE SUPPLY OF THE EXTERNAL SYSTEM FOR THE MODULE INPUT SHOULD BE THE SAME AS THE DEFINITION OF VCC.
- (2) PLEASE APPLY THE LAMP VOLTAGE WITHIN THE LCD OPERATION RANGE. WHEN THE BACKLIGHT TURNS ON BEFORE THE LCD OPERATION OF THE LCD TURNS OFF, THE DISPLAY MAY, INSTANTLY, FUNCTION ABNORMALLY.
- (3) IN CASE OF VCC=OFF LEVEL, PLEASE KEEP THE LEVEL OF INPUT SIGNALS ON THE LOW OR KEEP A HIGH IMPEDANCE.
- (4) T4 SHOULD BE MEASURED AFTER THE MODULE HAS BEEN FULLY DISCHARGED BETWEEN POWER ON/OFF PERIODS.
- (5) INTERFACE SIGNAL SHALL NOT BE KEPT AT HIGH IMPEDANCE WHEN THE POWER IS ON.

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PART NUMBER

LCT-H800480M70W1T

7.0" ACTIVE MATRIX FULL COLOR TFT PANEL 12:00 VIEW, LED BACKLIGHT, W/ TOUCH PANEL, -20°C TO +70°C OPERATING TEMP.

REV.

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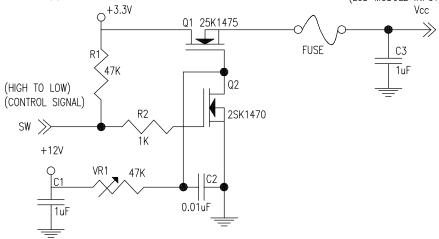
06.08.09

DC CHARACTERISTICS						
PARAMETER	SYMBOL	STA	NDARD VA	ALUE	UNIT	NOTE
FANAMETEN	STWIDUL	MIN	TYP.	MAX	UNII	NOIL
POWER SUPPLY VOLTAGE	VCC	+3.0	+3.3	+3.6	٧	(1)
RIPPLE VOLTAGE	VRP	_		TBD	m۷	_
RUSH CURRENT	IRUSH	-	ı	TBD	Α	(2)
POWER SUPPLY CURRENT	ICC	-	TBD	-	m۷	(3)
TTL INPUT LOW VOLTAGE	VIL	0	1	0.3Vcc	V	(4)
TTL INPUT HIGH VOLTAGE	VIH	0.7Vcc	_	Vcc	V V	(4)
TTL INPUT LEAK CURRENT (LOW)	Iμ	_	_	TBD	m۷	VI=0V(4)
TTL INPUT LEAK CURRENT (HIGH)	lн	_	_	TBD	mV	VI=3.3V(4)

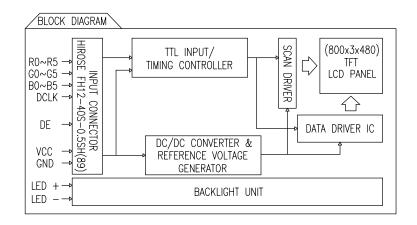
NOTE (1): THE MODULE IS RECOMMENDED TO OPERATE WITHIN SPECIFICATION RANGES LISTED ABOVE FOR NORMAL FUNCTION.

NOTE (2): MEASURED CONDITIONS:

(LCD MODULE INPUT)



NOTE (3): THE SPECIFIED POWER SUPPLY CURRENT IS UNDER THE CONDITIONS AT Vcc=5.0V. Ta=25±2°C,fv=60Hz, WHEREAS A GRAY LEVEL VERTICAL STRIPE PATTERN IS DISPLAYED. NOTE (4): CK,RO~R5, GO~G5, BO~B5, ENAB, HSYNC,VSYNC.



ABSOLUTE MAXIMUM RATINGS

ITFM	SYMBOL	STA	NDARD VA	UNIT	NOTE	
ITEM	SIMBUL	MIN	TYP.	MAX	UNIT	NOTE
POWER SUPPLY VOLTAGE	VCC	0	-	+6.0	٧	(1)
LOGIC INPUT VOLTAGE	VIN	-0.3	-	+6.3	٧	(1)
STORAGE TEMPERATURE	TST	-20	-	+70	.C	(2)
OPERATIN AMBIENT TEMPERATURE	TOP	-30	-	+80	°C	(4),(3)

- (1) PERMANENT DAMAGE MIGHT OCCUR IF THE MODULE IS OPERATED AT CONDITIONS EXCEEDING THE MAXIMUM VALUES.
- (2) TEMPERATURE AND RELATIVE HUMIDITY RANGE IS SHOWN IN THE FIGURE BELOW.
 - (A) 95%RH MAX. (Ta≤40°C).
 - (B) WET-BULB TEMPERATURE SHOULD BE 39°C MAX. (Ta>40°C).
 - (C) NO CONDENSATION.
- (3) THE TEMPERATURE OF PANEL SURFACE SHOULD BE O'C MIN. AND 75°C MAX.
- (4) IT IS A NORMAL CHARACTERISTIC THAT THE LCD COLOR AND CONTRAST WILL CHANGE (SOMETIMES UNEVENNESS CAN BE SEEN) UNDER LOW TEMPERATURE AND HIGH TEMPERATURE ENVIRONMENT. WHEN THE APPLICATION TEMPERATURE RETURNS BACK TO ROOM TEMPERATURE, LCD COLOR AND CONTRAST WILL REVERSE BACK TO ITS ORIGINAL COLOR AND CONTRAST WITHIN 24 HOURS AND ITS FUNCTIONALITY IS NOT AFFECTED.

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-DECIMAL PRECISION

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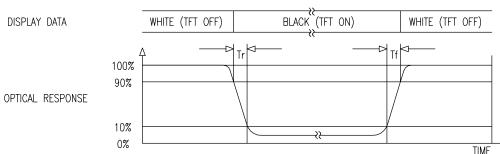
					STA	ANDARD VA	ALUF	UNIT	
ITEM		SYMBOL	CONDI	TION	MIN	TYP.			NOTE
DECDONCE TIME	_	Tr	0-0 •°C	25°C	_	10	20	ms	1
RESPONSE TIME	-	Tf	10−0 0−0 C	236	-	15	30	ms	
CONTRAST RATIO)	CR	-	25°C	400	500	-	_	2
		RIGHT	ф= 0°C	25°C	60	70	_	DEG	3,4
VIEWING ANGLE		LEFT	ф= 180°C	25°C	60	70	_	DEG	
(CR≥10)		UPPER	∳= 90°C	25°C	40	50	_	DEG	
		LOWER	¢= 270°C	25°C	60	70	_	DEG	
LUMINANCE OF WHITE (CENTER POINT)		L		25°C	250	300	_	Cd/m²	4
TRANSMISSIVE RATE		T%	0-6-	0°C	_	7.3	_	%	4,5
COLOR CROMACITY		Rx				TBD		_	4 502
(CIE1931)		Ry				TBD		_	NTSC =50%
		Gx				TBD		_	
		Gy	0-4- 0	•r	TBD	TBD	TBD	_	
"SIMULATION DATA		Bx				TBD		_	
REFERENCE ONLY"		Ву				TBD		_	
		Wx				TBD		_	
		Wy				TBD		-	

NOTE(1): DEFINITION OF RESPONSE TIME

OPTIMUM VIEWING DIRECTION

LUMINANCE UNIFORMITY

OPTICAL CHARASTERISTICS



0−0°C

12 O'CLOCK

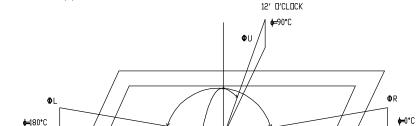
70

%

4.5

NOTE(2): DEFINITION OF CONTRAST RATIO

CR=BRIGHTNESS AT ALL PIXELS "WHITE" / BRIGHTNESS AT ALL PIXELS "BLACK"



NOTE(5): DEFINITION OF LUMINACE UNIFORMITY

ACTIVE AREA IS DIVITED INTO 9 MEASURING AREAS.

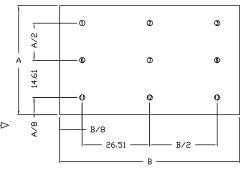
EVERY MEASURING POINT IS PLACED AT THE CENTER OF EACH MEASURING AREA.

ΦD

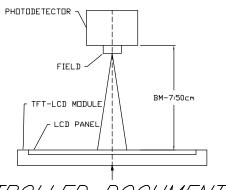
6' D'CLOCK

NOTE(3): DEFINITION OF VIEWING ANGLE

LUMINANCE UNIFORMITY (YU)= Bmin/Bmax
L----ACTIVE AREA LENGTH W----ACTIVE AREA WIDTH



NOTE(4): AFTER STABILIZING AND LEAVING THE PANEL ALONE AT GIVEN TEMPERATURE FOR 30 MIN, THE MEASUREMENT SHOULD BE EXECUTED. MEASUREMENT SHOULD BE EXECUTED IN STABLE, WINDLESS, AND DARK ROOM 30 MINS AFTER LIGHTING THE BACKLIGHT. THIS SHOULD BE MEASURED IN THE CENTER OF SCREEN. ENVIRONMENT CONDITION: Ta=25±2°C BACK-LIGHT ON CONDITION



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STANDARD SPECIFICATION FOR REABILITY

STANDARD SPECIFICATION OF REABILITY TEST

NO	TEST ITEM	CONTENT OF TEST	TEST CONDITION	APPLICABLE STANDARD					
1	HIGH TEMPERATURE STORAGE	ENDURANCE TEST APPLYING THE HIGH STORAGE TEMPERATURE FOR A LONG TIME.	80+/-3°C 240HRS						
2	LOW TEMPERATURE STORAGE	ENDURANCE TEST APPLYING THE LOW STORAGE TEMPERATURE FOR A LONG TIME.	-30+/-3°C 240HRS						
3	HIGH TEMPERATURE OPERATION	ENDURANCE TEST APPLYING THE ELECTRIC STRESS (VOLTAGE & CURRENT) AND THE THERMAL STRESS TO THE ELEMENT FOR A LONG TIME.	70+/-3°C 240HRS						
4	LOW TEMPERATURE OPERATION	ENDURANCE TEST APPLYING THE ELECTRIC STRESS UNDER LOW TEMPERATURE FOR A LONG TIME.	-20+/-3°C 240HRS						
5	HIGH TEMPERATURE/ HUMIDITY OPERATION	ENDURANCE TEST APPLYING THE ELECTRIC STRESS (VOLTAGE & CURRENT) AND TEMPERATURE / HUMIDITY STRESS TO THE ELEMENT FOR A LONG TIME.	40°C, 90%RH 120HRS	MIL-202E-103B JIS-C5023					
6	TEMPERATURE CYCLE	ENDURANCE TEST APPLYING THE LOW AND HIGH TEMPERATURE CYCLE. -20°C \(\sum \frac{25°C}{5 \text{ MIN}} \) \(\frac{70°C}{30 \text{ MIN}} \) 1 CYCLE	-20°C/ 70°C 10 CYCLES						
	MECHANICAL TEST								
7	DROP TEST		PACKED,100cm FREE FALL(6 SIDES, 1 CORNER, 3 EDGES)						

REMARKS:

- 1. FOR OPERATION TEST, ABOVE SPECIFICATION IS APPLICABLE WHEN TEST PATTERN IS CHANGING DURING ENTIRE OPERATION TEST.
- 2. INSPECTIONS AFTER RELIABILITY TESTS ARE PERFORMED WHEN THE DISPLAY TEMPERATURE RESUMES BACK TO ROOM TEMPERATURE.
- 3. IT IS A NORMAL CHARACTERISTIC THAT SOME DISPLAY ABNORMALITY CAN BE SEEN DURING REABILITY TEST. IF THE DISPLAY ABNORMALITY CAN RESUME BACK TO NORMAL CONDITION AT ROOM TEMPERATURE WITHIN 24 HOURS, THERE IS NO PERMANENT DESTRUCTION OVER THE DISPLAY. THE DISPLAY STILL POSSESSES ITS FUNCTIONALITY AFTER RELIABILITY TESTS.

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QUALITY ASSURANCE

ACCEPTABLE QUALITY LEVEL (AQL)

EACH LOT SHOULD SATISFY THE QUALITY LEVEL DEFINED AS FOLLOWS:

A. INSPECTION METHOD: MIL-SDT-105E LEVEL II NORMAL ONE TIME SAMPLING. B. AQL LEVEL.

CATEGORY	AQL	DEFINITION
MAJOR	0.25%	FUNCTIONAL DEFECTIVE AS PRODUCT.
MINOR	1.00%	SATISFY ALL FUNCTIONS AS PRODUCT BUT NOT SATISFY COSMETIC STANDARD.

COSMETIC SCREENING CRITERIA

N0	DEFECT	JUDGMENT CRITERIA	CATEGORY
1	SPOTS/DUST /BUBBLE (ROUND TYPE)	SIZE, D(mm) ACCEPTABLE QUANTITY IN ACTIVE AREA D≤0.15 DISREGARD 0.15<0≤0.20 3 D>0.20 0	MINOR
2		ACCEPTABLE QUANTITY WIDTH, W(mm) LENGTH, L(mm) IN ACTIVE AREA W≤0.02 DISREGARD DISREGARD W≤0.03 L ≤ 1.0 DISREGARD W≤0.05 L ≤ 2.0 3 W>0.05 DISREGARD 0	MINOR
3	ALLOWABLE DENSITY	ABOVE DEFECTS SHOULD BE SEPARATED MORE THAN 5mm EACH OTHER.	MINOR
4		OBVIOUS UNVEN COLOR (RAINBOW) SHALL NOT BE NOTICEABLE.	MINOR
5	DISPLAY CONDITION	DIM DISPLAY ON THE PATTERNS, EXTRA PATTERN AND SHORT CIRCUIT ARE NOT ACCEPTABLE.	MAJOR
6	NO DISPLAY OR MISSING DISPLAY	THE PATTERNS OF DISPLAY SHALL LIGHT UP AS REQUIRED. NO DISPLAY OR MISSING DISPLAY ARE NOT ACCEPTABLE.	MAJOR

NOTE: D= (LONG LENGTH + SORTH LENGTH)/2

FAILURE JUDGMENT CRITERIA

AFTER REABILITY TEST ABOVE, TEST SAMPLE SHALL BE LET RETURN TO ROOM TEMPERATURE AND HUMIDITY AT LEAST 4 HOURS BEFORE FINAL TESTS ARE CARRIED OUT.

	CRITERION ITEM	FAILURE JUDGMENT CRITERIA		
ELECTRICAL CHARACTERISTIC ELECTRICAL SHORT AND OPEN.				
	MECHANICAL CHARACTERISTIC	OUT OF MECHANICAL SPECIFICATION.		
	OPTICAL CHARACTERISTIC	OUT OF APPEARANCE STANDARD.		

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PRECAUTIONS FOR USING LCD MODULE

HANDLING PRECAUTIONS

- 1, THE DISPLAY PANEL IS MADE OF GLASS AND POLARIZER. DO NOT SUBJECT IT TO MECHANICAL SHOCK BY 1, OBSERVE THE FOLLOWING WHEN SOLDERING LEAD WIRE, CONNECTOR CABLE AND ETC. TO THE LCD DROPPING OR IMPACT WHICH MAY CAUSE CHIPPING ESPECIALLY ON THE EDGES.
- 2. DO NOT TOUCH, PUSH OR RUB THE EXPOSED POLARIZERS WITH ANYTHING HARDER THAN AN HB PENCIL SOLDERING IRON TEMPERATURE: 300~350°C. LEAD (GLASS, TWEEZERS, ETC.). THE POLARIZER COVERING THE DISPLAY SURFACE OF THE LCD MODULE IS SOFT AND EASILY SCRATCHED. HANDLE THIS POLARIZER CAERFULLY.
- 3. IF THE DISPLAY SURFACE BECOMES CONTAMINATED, BREATHE ON THE SURFACE AND GENTLY WIPE IT WITH ABOVE IS A RECOMMENDED APPROACH. DUE TO DIFFERENT SOLDER COMPOSITION AND PROCESSING METHOD, A SOFT DRY CLOTH. IF IT IS HEAVILY CONTAMINATED, MOISTEN CLOTH WITH ISOPROPYL ALCOHOL OR ETHYL ALCOHOL. AVOID USING SOLVENTS LIKE ACETONE (KETENE), WATER, TOLUENE, ETHANOL TO CLEAN THE POLARIZER SURFACE.
- 4. PLEASE KEEP THE TEMPERATURE WITHIN SPECIFIED RANGE FOR USE AND STORAGE, POLARIZATION DEGRADATION, BUBBLE GENERATION OR POLARIZER PEEL-OFF MAY OCCUR WITH HIGH TEMPERATURE AND
- 5. DO NOT APPLY EXCESSIVE FORCE TO THE DISPLAY SURFACE OR THE ADJOINING AREAS SINCE THIS MAY CAUSE THE COLOR TONE TO VARY.
- 6. INSTALL THE LCD MODULE BY USING THE MOUNTING HOLES. WHEN MOUNTING THE LCD MODULE MAKE SURE IT IS FREE OF TWISTING, WARPING AND DISTORTION.
- 7. EXERCISE CARE TO MINIMIZE CORROSION OF THE ELECTRODE. CORROSION OF THE ELECTRODES IS ACCELERATED BY WATER DROPLETS, MOISTURE CONDENSATION OR A CURRENT FLOW IN A HIGH-HUMIDITY FNVIRONMENT
- 8. NC TERMINAL SHOULD BE OPEN. DO NOT CONNECT ANYTHING.
- 9. IF THE LOGIC CIRCUIT POWER IS OFF, DO NOT APPLY THE INPUT SIGNALS.
- 10. AVOID CONTACTING OIL AND FATS.
- 11. CONDENSATION ON THE SURFACE AND CONTACT WITH TERMINALS DUE TO COLD WILL DAMAGE, STAIN OR HOWEVER, IT WILL RETURN TO NORMAL IF IT IS TURNED OFF AND THEN BACK ON. DIRTY THE POLARIZERS. AFTER PRODUCTS ARE TESTED AT LOW TEMPERATURE THEY MUST BE WARMED UP IN 5. WHEN TURNING THE POWER ON, INPUT EACH SIGNAL AFTER THE POSITIVE/NEGATIVE VOLTAGE BECOMES A CONTAINER BEFORE COMING IN CONTACT WITH ROOM TEMPERATURE AIR.
- 12. WIPE OFF SALIVA OR WATER DROPS IMMIDEATLY, CONTACT WITH WATER OVER A LONG PERIOD OF TIME MAY CAUSE DEFORMATION OR COLOR FADING.

ELECTRO-STATIC DISCHARGE CONTROL

- 1, SINCE THIS MODULE USES A CMOS LSI, THE SAME CAERFUL ATTENTION SHOULD BE PAID TO ELECTROSTATIC DISCHARGE AS FOR AN ORDINARY CMOS IC.
- 2. BE SURE TO GROUND THE BODY WHEN HANDLING THE LCD MODULES. TOOLS REQUIRED FOR ASSEMBLING. SUCH AS SOLDERING IRONS, MUST BE PROPERLY GROUNDED.
- 3. TO REDUCE THE AMOUNT OF STATIC ELECTRICITY GENERATED, DO NOT CONDUCT ASSEMBLING AND OTHER WORK UNDER DRY CONDITIONS. TO REDUCE THE GENERATION OF STATIC ELECTRICITY, BE CARFUL THAT THE AIR IN THE WORK IS NOT TOO DRIED. A RELATIVE HUMIDITY OF 50%-60% IS RECOMMENDED.
- 4. THE LCD MODULE IS COATED WITH A FILM TO PROTECT THE DISPLAY SURFACE. EXERCISE CARE WHEN PEELING OFF THIS PROTECTIVE FILM SINCE STATIC ELECTRICITY MAY BE GENERATED.
- 5. WHEN SOLDERING THE TERMINAL OF LCM. MAKE CERTAIN THE AC POWER SOURCE FOR THE SOLDERING IRON DOES NOT LEAK.

PRECAUTION OF SOLDERING TO THE LCM

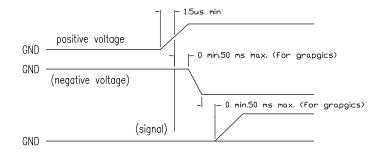
- MODULE.
- SOLDERING TIME: ≤3 SEC.
- SOLDER: EUTECTIC SOLDER.

IT IS RECOMMENDED THAT CUSTOMER TO STUDY AND FINE TUNING THEIR SOLDERING PROCESS PARAMETERS ACCORDINGLY.

2. IF SOLDERING FLUX IS USED, BE SURE TO REMOVE ANY REMANING FLUX AFTER FINISHING TO SOLDERING OPERATION. (THIS DOSE NOT APPLY IN THE CASE OF A NON-HALOGEN TYPE OF FLUX.) IT IS RECOMMENDED THAT YOU PROTECT THE LCD SURFACE WITH A COVER DURING SOLDERING TO PREVENT ANY DAMAGE DUE TO FLUX SPATTERS.

PRECAUTION FOR OPERATION

- 1. VIEWING ANGLE VARIES WITH THE CHANGE OF LIQUID CRYSTAL DRIVING VOLTAGE (Vo). ADJUST Vo TO SHOW THE BEST CONTRAST.
- 2. DRIVING THE LCD IN THE VOLTAGE ABOVE THE LIMIT SHORTERNS ITS LIFETIME.
- 3. RESPONSE TIME IS GREATLY DELAYED AT TEMPERATURE BELOW THE OPERATING TEMPERATURE RANGE. HOWEVER, IT WILL RECOVER WHEN IT RETURNS TO THE SPECIFIED TEMPERATURE RANGE.
- 4. IF THE DISPLAY AREA IS PUSHED HARD DURING OPERATION. THE DISPLAY WILL BECOME ABNORMAL.
- STABLE (BELOW FIGURE IS A GENERAL ILLUSRATION WHERE TYPICAL VALUE DEPENDS ON INDIVIDUAL PRODUCT DESIGN).



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> SCALE: N/A

RoHS COMPLIANT PRODUCT

1. CADMIUM AND CADMIUM COMPOUNDS LESS THAN 100PPM 2. HEXAVALENT CHROMIUM COMPOUNDS LESS THAN 1000PPM 3. LEAD AND LEAD COMPOUNDS LESS THAN 1000PPM 4. MERCURY AND MERCURY COPMPOUNDS LESS THAN 1000PPM 5. POLYBROMINATED BIPHENYLS (PBBs) LESS THAN 1000PPM 6. POLYBROMINATED DIPHENYL ETHERS (PBDEs) LESS THAN 1000PPM

PACKAGING STANDARD

PRODUCT NO.	LCT-H800480M70W	RELEASE DATE	TBD
PRODUCT NAME.	7.0" TFT LCD MODULE	PREPARE BY:	
QUANTITY/ EACH BOX	TBD	BOX MATERIAL	PAPER CARTON
OUTER CARTON BOX SIZE	TBD	BOX TYPE	NEW
QUANTITY/ INER BOX QUANTITY/ OUTER BOX	N/A	WEIGHT	N/A KG

THERE ARE 12 PCS LCD PER EACH ANTI-STATIC PLASTIC PLATE. THERE ARE 7 LAYER PLASTIC PLATES PER EACH INNER CARTON BOX. THERE ARE 2 INNER CARTON BOX PER EACH OUTER CARTON BOX.

STORAGE

- 1. WHEN STORING LCDS AS SPARES FOR SOME YEARS, THE FOLLOWING PRECAUCTIONS ARE NECESSARY.
- 2. STORE THEM IN A SEALED POLYETHYLENE BAG. IF PROPERLY SEALED, THERE IS NO NEED FOR DESICCANT.
- 3. STORE THEM IN A DARK PLACE. DO NOT EXPOSE TO SUNLIGHT OR FLUORESCENT LIGHT, KEEP THE TEMPERATURE BETWEEN 0°C AND 35°C.
- 4. ENVIRONMENTAL CONDITIONS:
- 5. DO NOT LEAVE THEM FOR MORE THAN 168HRS, AT 60°C.
- 6. SHOULD NOT BE LEFT FOR MORE THAN 48HRS. AT -20°C.

SAFETY

- 1. ITS RECOMMENDED TO CRUSH DAMAGED OR UNNECESSARY LCD INTO PIECES AND WASH THEM OFF WITH SOLVENTS SUCH AS ACETONE AND ETHANOL, WHICH SHOULD LATER BE BURNED.
- 2. IF ANY LIQUID LEAKS OUT OF DAMAGED GLASS CELL AND COMES IN CONTACT WITH THE HANDS. WASH OFF THOROUGHLY WITH SOAP AND WATER.

*UNLESS OTHERWISE SPECIFIED TOLERANCES PER DECIMAL PRECISION ARE: X=±1 (±0.039), X.X=±0.5 (±0.020), X.XX=±0.25 (±0.010), X.XXX=±0.127 (±0.005). LEAD SIZE=±0.05 (±0.002), LEAD LENGTH=±0.75 (±0.030). MIN= +DECIMAL PRECISION MAX. = +0.00 (±0.002), LEAD SIZE=±0.05 (±0.002), LEAD SIZE=±0.05 (±0.002), LEAD LENGTH=±0.75 (±0.030). MIN= +DECIMAL PRECISION MAX. = +0.00 (±0.002), LEAD SIZE=±0.05 (±0.002), LEAD LENGTH=±0.75 (±0.003). MIN= +DECIMAL PRECISION MAX. = +0.00 (±0.002), LEAD SIZE=±0.05 (±0.002), LEAD SIZE=±0.05 (±0.003). MIN= +DECIMAL PRECISION MAX. = +0.00 (±0.003). MIN= +DECIMAL PRE

REV. PART NUMBER LCT-H800480M70W1T

> 7.0" ACTIVE MATRIX FULL COLOR TFT PANEL 12:00 VIEW, LED BACKLIGHT, W/ TOUCH PANEL, -20°C TO +70°C OPÉRATING TEMP.

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PLEASE PAY ATTENTION TO YOUR SOLDERING PROCESS.

RELIABILITY NOTE OUR MANY YEARS OF EXPERIENCE DATA ACCUMULATION INDICATE THAT SOLDER HEAT IS A MAJOR CAUSE OF EARLY AND FUTURE FAILURE.

Creating LED and LCD Solutions Together

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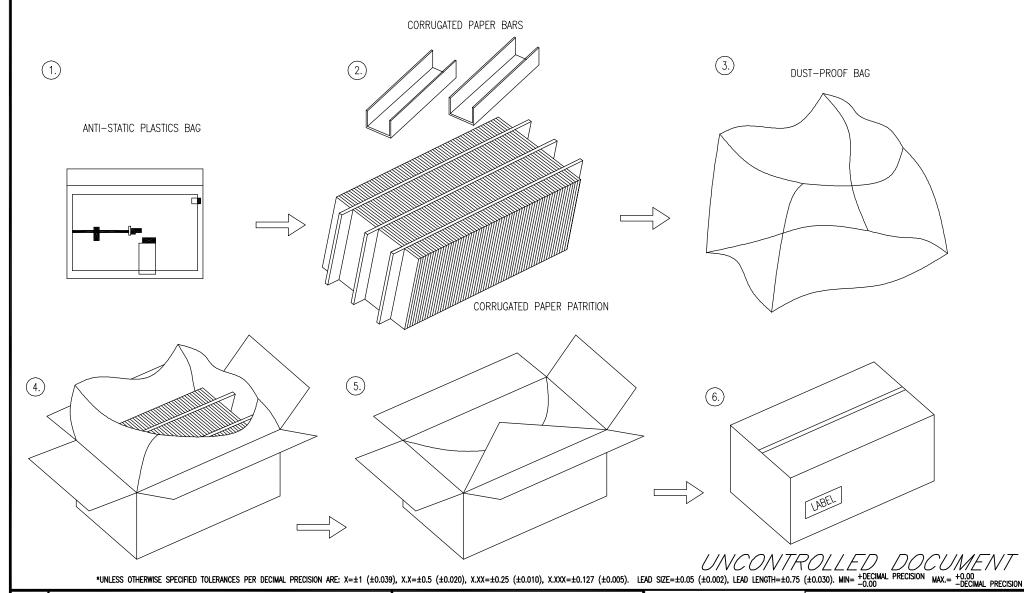
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SCALE: N/A

JN

DRAWN BY:



REV.

PART NUMBER LCT-H800480M70W1T

7.0" ACTIVE MATRIX FULL COLOR TFT PANEL 12:00 VIEW, LED BACKLIGHT, W/ TOUCH PANEL, -20°C TO +70°C OPERATING TEMP.

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SCALE: N/A