

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



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LI0805G201R-10

UNCONTROLLED DOCUMENT

PHYSICAL DIMENSIONS:

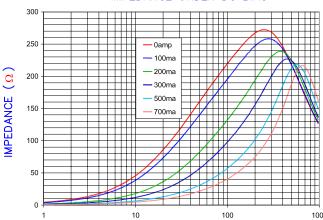
A 2.00 [.079] ± 0.20 [.008]

B 1.25 [.049] ± 0.20 [.008]

C 0.90 [.035] ± 0.20 [.008]

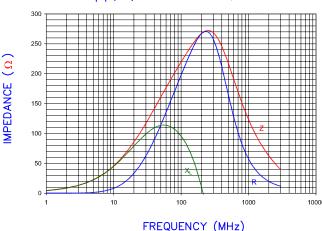
D 0.51 [.020] ± 0.25 [.010]

Z vs FREQUENCY IMPEDANCE UNDER DC BIAS



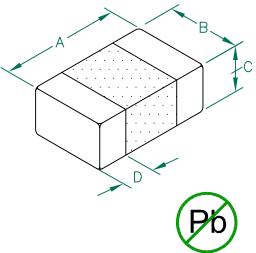
FREQUENCY (MHz)

|Z| , R, AND X vs. FREQUENCY



zer

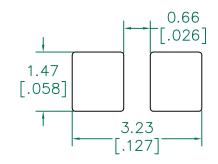
10000 2002/99



NOTES: UNLESS OTHERWISE SPECIFIED

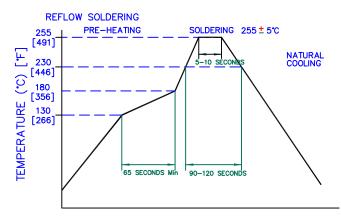
- 1. TAPED AND REELED per CURRENT EIA SPECIFICATIONS 7" REELS, 4000 PCS/REEL, PAPER TAPE.
- 2. COMPONENTS SHOULD BE ADEQUATELY PREHEATED BEFORE SOLDERING.
- 3. TERMINATION FINISH IS 100% TIN.
- 4. OPERATING TEMP. RANGE: -40°C~+125°C. (INCLUDING SELF-HEATING)

LAND PATTERNS FOR REFLOW SOLDERING



(For wave soldering, add 0.763 [.030] to this dimension.)

RECOMMENDED SOLDERING CONDITIONS



DIMENSIONS ARE IN mm [INCHES].				This pr
				Tech.
				with th
F	ADD OPERATING TEMPERATURE UPDATE LAIRD LOGO AND REFLOW CURVE	08/05/13	QU	copies written rights
E	CHANGE TO PAPER TAPE	03/03/10	JUN	reserve
D	UPDATE COMPANY LOGO	06/16/09	JRK	PROJECT/
С	ADD ROHS SYMBOL MODIFY NOTE 1	11/28/06	JRK	LIO
В	CORRECT TEST REF #	01/03/06	JRK	DATE:
Α	ORIGINAL DRAFT	04/19/04	JRK	CAD #
REV	DESCRIPTION	DATE	INT	l LI

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PROJECT/PART NUMBER: REV

F CO-FIRE

04/19/04 | SCALE: NTS | SHEET: | 2 of 2

AGILENT E4991A RF Impedance/Material Analyzer HP 16194A Test Fixture. TEST REF. 3255