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AUTOMOTIVE COMPLIANT DUAL AND QUAD DIFFERENTIAL COMPARATORS

Description

The LM2901Q/2903Q series comparators consist of four and two independent precision voltage comparators with very low input offset voltage specification. They are designed to operate from a single power supply over a wide range of voltages; however operation from split power supplies is also possible. They offer low power supply current independent of the magnitude of the power supply voltage.

The LM2901Q/2903Q series comparators are designed to directly interface with TTL and CMOS.

The LM2903Q dual devices are available in SO-8, MSOP-8 and TSSOP-8; and the LM2901Q quad devices are available in SO-14 and TSSOP-14 - all are in industry-standard pinouts.

All use "green" mold compound and are qualified to AEC-Q100 Grade 1 and are Automotive Compliant supporting PPAPs.

Features

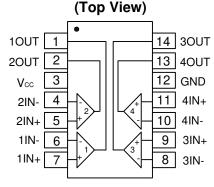
- Wide Power Supply Range:
 - Single Supply: 2V to 36V
 - Dual Supplies: ±1.0V to ±18V
- Very Low Supply Current Drain Independent of Supply Voltage
 - LM2903Q: 0.6mA
 - LM2901Q: 0.9mA
- Low Input Bias Current: 25nA
- Low Input Offset Current: ±5nA
- Typical Offset Voltage:
 - Non-A Device: 2mV
 - A Device: 1mV
- Common-Mode Input Voltage Range Includes Ground
- Differential Input Voltage Range Equal to the Power Supply Voltage
- Low Output Saturation Voltage:
 - LM2903Q: 200mV at 4mA
 - LM2901Q: 100mV at 4mA
- Output Voltage Compatible with TTL, MOS and CMOS
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q100 Grade 1
- PPAP Capable (Note 4)

Pin Assignments

LM2903Q/ LM2903AQ

(Top View) 10UT 8 V_{cc} 2 **20UT** 1IN-3 6 1IN+ 2IN-5 4 2IN+ GND SO-8/TSSOP-8/MSOP-8

LM2901Q/ LM2901AQ



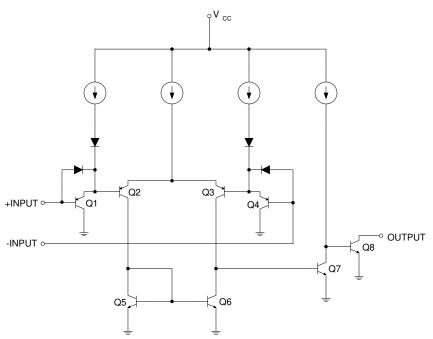
SO-14/TSSOP-14

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. Automotive products are AEC-Q100 qualified and are PPAP capable. Refer http://www.diodes.com/quality/product_compliance_definitions/.



Schematic Diagram



Functional Block Diagram of LM2901Q/2901AQ/2903Q/2903AQ (Each Comparator)

Pin Descriptions

LM2901Q, LM2901	M2901Q, LM2901AQ				
Pin Name	Pin#	Function			
1OUT	1	Channel 1 Output			
2OUT	2	Channel 2 Output			
V _{CC}	3	Chip Supply Voltage			
2IN-	4	Channel 2 Inverting Input			
2IN+	5	Channel 2 Non-Inverting Input			
1IN-	6	Channel 1 Inverting Input			
1IN+	7	Channel 1 Non-Inverting Input			
3IN-	8	Channel 3 Inverting Input			
3IN+	9	Channel 3 Non-Inverting Input			
4IN-	10	Channel 4 Inverting Input			
4IN+	11	Channel 4 Non-Inverting Input			
GND	12	Ground			
4OUT	13	Channel 4 Output			
3OUT	14	Channel 3 Output			
LM2903Q, LM2903	AQ				
1OUT	1	Channel 1 Output			
1IN-	2	Channel 1 Inverting Input			
1IN+	3	Channel 1 Non-inverting Input			
GND	4	Ground			
2IN+	5	Channel 2 Non-Inverting Input			
2IN-	6	Channel 2 Inverting Input			
2OUT	7	Channel 2 Output			
V _{CC}	8	Chip Supply Voltage			



Absolute Maximum Ratings (Note 5) (@TA = +25°C, unless otherwise specified.)

Symbol	Parameter	•	Rating	Unit
Vcc	Supply Voltage		36	V
V _{ID}	Differential Input Voltage		36	V
V _{IN}	Input Voltage		-0.3 to +36	V
I _{IN}	Input Current (V _{IN} < -0.3V)		50	mA
Vo	Output Voltage		36	V
Io	Output Current		20	mA
_	Duration of Output Short Circuit to Ground	(Note 6)	Unlimited	_
		LM2903_QS-13	150	
	De classes. The sweet large side as a	LM2903_QTH-13	175	
θ_{JA}	Package Thermal Impedance (Note 7)	LM2903_QM8-13	200	°C/W
	(Note 7)	LM2901_QS14	89	
		LM2901_QT14	100	
T _A	Operating Temperature Range		-40 to +125	°C
TJ	Operating Junction Temperature		+150	°C
T _{ST}	Storage Temperature Range			°C
T _{LEAD}	Lead Temperature (Soldering, 10 seconds)		+260	°C

Notes:

- 5. Stresses beyond those listed under absolute maximum ratings may cause permanent damage to the device. These are stress ratings only; functional operation of the device at these or any other conditions beyond those indicated under recommended operating conditions is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.
- 6. Short circuits from outputs to VCC can cause excessive heating and eventual destruction.
- 7. Maximum power dissipation is a function of $T_{J(MAX)}$, θ_{JA} , and T_A . The maximum allowable power dissipation at any allowable ambient temperature is $P_D = (T_{J(MAX)} T_A)/\theta_{JA}$. Operating at the absolute maximum T_J of +150°C can affect reliability.

ESD Ratings

	LM2901_QS14	500	
	LM2901_QT14	500	
Human Body Mode ESD Protection (Note 8)	LM2903_QS-13	500	
	LM2903_QTH-13	500	
	LM2903_QM8-13	<500	V
	LM2901_QS14		V
	LM2901_QT14		
Charge Device Mode ESD Protection	LM2903_QS-13	1,000	
	LM2903_QTH-13		
	LM2903_QM8-13		

Note:

8. Human body model, $1.5k\Omega$ in series with 100pF.

Recommended Operating Conditions (Over Operating Free-Air Temperature Range, unless otherwise noted.)

Parameter	Min	Max	Units	
Cumply Voltage	Single Supply	2	36	W
Supply Voltage	Dual Supply	±1	±18]
Ambient Temperature Range		-40	+125	°C
Junction Temperature Range		-40	+125	C



$\textbf{Electrical Characteristics} \ \, (\text{Notes 9 \& 10}) \ \, (@V_{\text{CC}} = 5.0\text{V}, \, \text{GND} = 0\text{V}, \, \text{T}_{\text{A}} = +25^{\circ}\text{C}, \, \text{unless otherwise specified.})$

LM2901Q, LM2901AQ

	Parameter	Conditio	ns	TA	Min	Тур	Max	Unit
		V _{IC} = V _{CMR} Min,	Non-A Device	$T_A = +25$ °C	_	2	7	
V/	Input Offset Voltage	$V_O = 1.4V$	Non-A Device	Full Range	_	1	15	mV
V_{1O}	input Onset Voltage	$V_{CC} = 5V \text{ to } 30V$	A-Suffix Device	$T_A = +25$ °C	_	1	2	IIIV
		(Note 11) Full Ra		Full Range	_	1	4	
I_	Input Bias Current	I _{IN+} or I _{IN} - with OUT in Li	near Range,	$T_A = +25$ °C	_	25	250	nA
lΒ	input bias Guirent	V _{CM} = 0V (Note 12)		Full Range	_	-	500	IIA
l	Input Offset Current	los los Vasa OV		T _A = +25°C	_	5	50	nA
lio	input Onset Guirent	I_{IN+} - I_{IN-} , $V_{CM} = 0V$		Full Range	_	-	200	IIA
	Input Common-Mode			T _A = +25°C	0 to V _{CC} -1.5	_	_	
V_{CMR}	Voltage Range	V _{CC} = 30V (Note 13)		Full Range	0 to V _{CC} -2	_	_	V
			.,	T _A = +25°C	_	1.2	2.5	
	Supply Current	$R_L = \infty$ on Quad	$V_{CC} = 30V$	Full range	_	_	3.5	1
Icc	(Four Comparators)	Channels	.,	T _A = +25°C	_	0.9	2	mA
			$V_{CC} = 5V$	Full Range	_	_	3.0	
Av	Voltage Gain	$V_{CC} = 15V$, $V_{OUT} = 1V$ to $R_L \ge 15k\Omega$,	11V,	T _A = +25°C	50	200	_	V/mV
_	Large Signal Response Time	V_{IN} = TTL Logic Swing, V V_{RL} = 5V, R_L = 5.1kΩ	REF = 1.4V,	T _A = +25°C	_	300	_	ns
_	Response Time	$V_{RL} = 5V$, $R_L = 5.1k\Omega$ (No	ote 14)	T _A = +25°C	_	1.3	_	μs
I _{O(SINK)}	Output Sink Current	$V_{IN-} = 1V, V_{IN+} = 0, V_O \le$	1.5V	T _A = +25°C	6	16	_	mA
	Caturation Valtage	V 4V V 0 1	4.4	T _A = +25°C	_	100	400	0 ,
V_{SAT}	Saturation Voltage	$V_{IN-} = 1V$, $V_{IN+} = 0$, I_{SINK}	≤ 4mA	Full Range	_	-	700	mV
1	Output Lookaga Current	V _{IN} -= 0V, V _{IN} += 1, V _O =	5V	T _A = +25°C	_	0.1	_	nA
I _{O(LEAK)}	Output Leakage Current	$V_{IN-} = 0V, V_{IN+} = 1, V_O =$	30V	Full Range	_	-	1	μΑ
V _{ID}	Differential Input Voltage	All V _{IN} ≥0V (or V- if used)	(Note 15)	Full Range	_	_	36	V

Notes:

- 9. Typical values represent the most likely parametric norm as determined at the time of characterization. Actual typical values may vary over time and will also depend on the application and configuration. The typical values are not tested and are not guaranteed on shipped production material.
- 10. All limits are guaranteed by testing or statistical analysis. Limits over the full temperature (-40 ≤ T_A ≤ +125°C) are guaranteed by design, but not tested in production.
- 11. $V_O \cong 1.4V$, $R_S = 0\Omega$ with V_{CC} from 5V to 30V.
- 12. The direction of the input current is out of the IC due to the PNP input stage. This current is essentially constant, independent of the state of the output so no loading change exists on the input lines.
- 13. The input common-mode voltage of either input signal voltage should not be allowed to go negative by more than 0.3V (@ +25°C). The upper end of the common-mode voltage range is V_{CC} -1.5V (@ +25°C), but either or both inputs can go to +36V without damage, independent of the magnitude of V_{CC}.
- The response time specified is for a 100mV step input with 5mV overdrive. For larger overdrive signals 300ns can be obtained, see typical performance characteristics.
- 15. Positive excursions of input voltage may exceed the power supply level. As long as other voltages remain within the common mode range, the comparator will provide a proper output stage. The low voltage state must not be less than -0.3V (or 0.3V below the magnitude of the negative power supply, if used).



$\textbf{Electrical Characteristics} \ \, \text{(continued)} \ \, \text{(Notes 9 \& 10)} \ \, \text{(@V_{CC} = 5.0V, GND = 0V, T_{A} = +25^{\circ}\text{C}, unless otherwise specified.)}$

LM2903Q, LM2903AQ

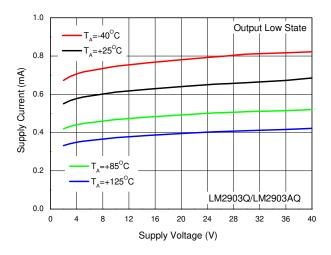
Parameter		Condi	tions	TA	Min	Тур	Max	Unit
		V _{IC} = V _{CMR} Min,	Non-A Device	T _A = +25°C	_	2	7	
\/	Input Offset Voltage	$V_O = 1.4V$,	Non-A Device	Full Range	_	_	15	m\/
V_{IO}	Imput Offset Voltage	$V_{CC} = 5V \text{ to } = 30V$	A-Suffix Device	T _A = +25°C	_	1 2 mV		
		(Note 11)	A-Sullix Device	Full Range	_	_	4	
l ₌	Input Bias Current	I _{IN+} or I _{IN} - with OUT in	n Linear Range,	$T_A = +25$ °C	_	25	250	nA
lΒ	Input bias Guirent	V _{CM} = 0V (Note 12)		Full Range	_	_	500	ША
l.a	Input Offset Current	$I_{IN+} - I_{IN-}, V_{CM} = 0V$		$T_A = +25$ °C		5	50	nA
I _{IO}	input Onset Ourient	IIN+ - IIN-, VCM = OV		Full Range	_	_	200	ША
V	Input Common-Mode Voltage)/ 00\/ (Nlata 10\		T _A = +25°C	0 to V _{CC} -1.5	_	_	V
V _{CMR}	Range	V _{CC} = 30V (Note 13)		Full Range	0 to V _{CC} -2	_	_	V
		$R_L = \infty$ on Both	V_{CC} = 30V T_A = +25°C Full Range	T _A = +25°C	_	0.7	1.7	
	Cumply Current			_	_	3.0	m A	
Icc	Supply Current	Channels	\/ E\/	T _A = +25°C	_	0.6	1	mA
			$V_{CC} = 5V$	Full Range	_	_	2.0	
A _V	Voltage Gain	$V_{CC} = 15V, V_{OUT} = 1V$ $R_L \ge 15k\Omega$,	to 11V,	T _A = +25°C	50	200	_	V/mV
_	Large Signal Response Time	V_{IN} = TTL Logic Swing V_{RL} = 5V, R_L = 5.1k Ω		T _A = +25°C	_	300	_	ns
_	Response Time	$V_{RL} = 5V$, $R_L = 5.1k\Omega$	(Note 14)	T _A = +25°C	_	1.3	_	μs
I _{O(SINK)}	Output Sink Current	$V_{IN-} = 1V, V_{IN+} = 0, V_0$	_O ≤ 1.5V	T _A = +25°C	6	16	_	mA
		V 4V V 0 I	4 A A	T _A = +25°C	_	200	400	
V_{SAT}	Saturation Voltage	$V_{IN-} = I V, V_{IN+} = U, I_S$	$V_{IN-} = 1V, V_{IN+} = 0, I_{SINK} \le 4mA$		_	_	700	mV
	Outrout Lankson Course	$V_{IN-} = 0V, V_{IN+} = 1, V_0$	_O = 5V	T _A = +25°C	_	0.1	_	nA
O(LEAK)	Output Leakage Current	$V_{IN-} = 0V, V_{IN+} = 1, V$	O = 30V	Full Range	_	_	1	μΑ
V _{ID}	Differential Input Voltage	All V _{IN} ≥0V (or V- if us		Full Range	_	_	36	V

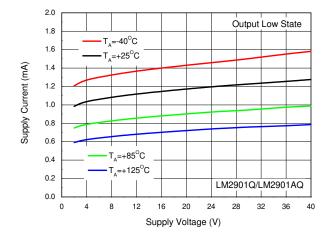
Notes:

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- 10. All limits are guaranteed by testing or statistical analysis. Limits over the full temperature (-40 ≤ T_A ≤ +125°C) are guaranteed by design, but not tested in production.
- 11. $V_O \cong 1.4V$, $R_S = 0\Omega$ with V_{CC} from 5V to 30V.
- 12. The direction of the input current is out of the IC due to the PNP input stage. This current is essentially constant, independent of the state of the output so no loading change exists on the input lines.
- 13. The input common-mode voltage of either input signal voltage should not be allowed to go negative by more than 0.3V (@ +25°C). The upper end of the common-mode voltage range is V_{CC} -1.5V (@ +25°C), but either or both inputs can go to +36V without damage, independent of the magnitude of V_{CC}.
- 14. The response time specified is for a 100mV step input with 5mV overdrive. For larger overdrive signals 300ns can be obtained, see typical performance characteristics.
- 15. Positive excursions of input voltage may exceed the power supply level. As long as other voltages remain within the common-mode range, the comparator will provide a proper output stage. The low voltage state must not be less than -0.3V (or 0.3V below the magnitude of the negative power supply, if used).



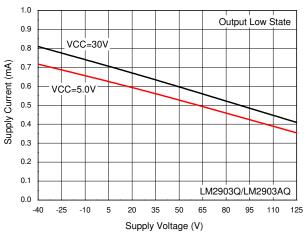
Performance Characteristics



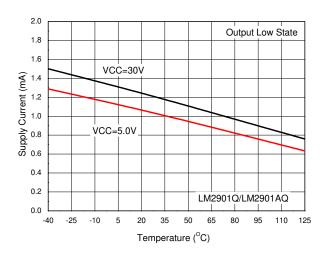


Supply Current vs. Supply Voltage

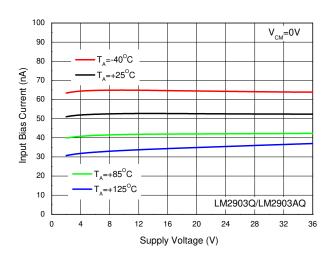
117



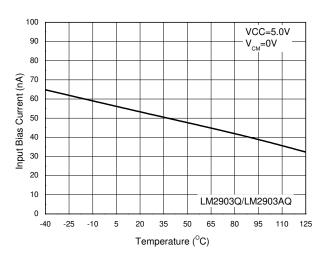
Supply Current vs. Supply Voltage



Supply Current vs. Temperature



Supply Current vs. Temperature

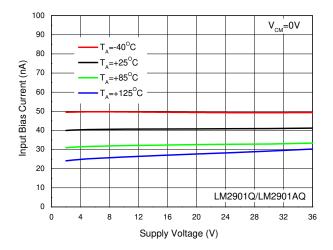


Input Bias Current vs. Supply Voltage

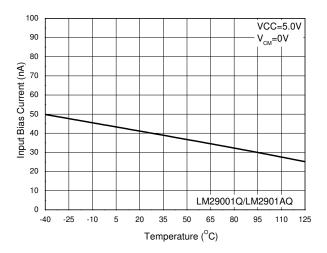
Input Bias Current vs. Temperature



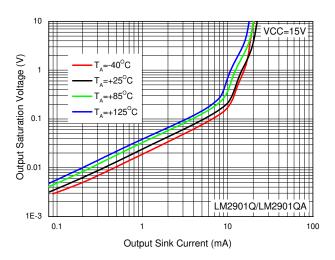
Performance Characteristics (continued)



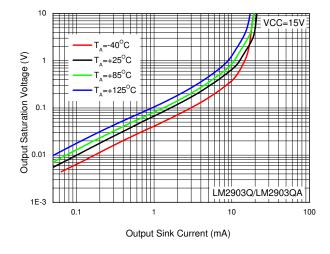
Input Bias Current vs. Supply Voltage



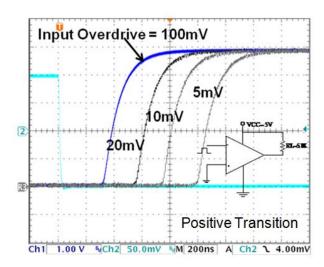
Input Bias Current vs. Temperature



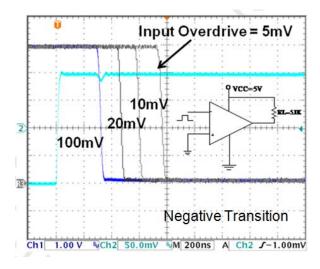
Output Saturation Voltage vs. Sink Current



Output Saturation Voltage vs. Sink Current



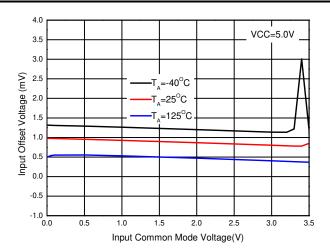
Response Time for Various Input Overdrive

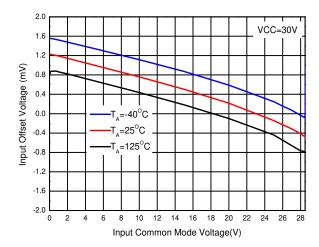


Response Time for Various Input Overdrive



Performance Characteristics (continued)





Input Offset Voltage vs. Input Common-Mode Voltage

Input Offset Voltage vs. Input Common-Mode Voltage



Application Information

General Information

The LM2901Q/2903Q series comparators are high-gain, wide-bandwidth devices, and like most comparators, can easily oscillate if the output lead is inadvertently allowed to capacitive couple to the inputs via stray capacitance. This shows up only during the output voltage transition intervals as the comparator changes states. Standard PC board layout is helpful as it reduces stray input-output coupling. Reducing the input resistors to <10kΩ reduces the feedback signal levels and finally, adding even a small amount (1.0 to 10mV) of positive feedback (hysteresis) causes such a rapid transition that oscillations due to stray feedback are not possible. Simply socketing the IC and attaching resistors to the pins will cause input-output oscillations during the small transition intervals unless hysteresis is used. If the input signal is a pulse waveform, with relatively fast rise and fall times, hysteresis is not required. All input pins of any unused comparators should be tied to the negative supply.

The bias network of the LM2901Q/2903Q series comparators establishes a quiescent current independent of the magnitude of the power supply voltage over the range of from 2.0 V_{DC} to 30 V_{DC} .

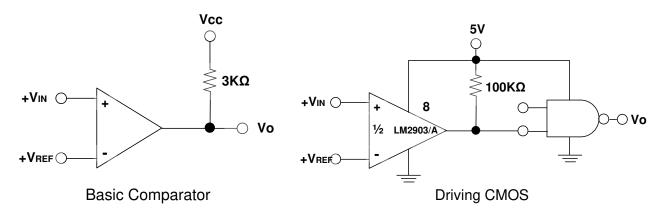
The differential input voltage may be larger than V_{CC} without damaging the device. Protection should be provided to prevent the input voltages from becoming negative more than -0.3 V_{DC} (@ +25°C). An input clamp diode can be used as shown in the applications section.

The output of the LM2901Q/2903Q series comparators is the uncommitted collector of a grounded-emitter NPN output transistor. Many collectors can be tied together to provide an output OR'ing function. An output pull-up resistor can be connected to any available power supply voltage within the permitted supply voltage range and there is no restriction on this voltage due to the magnitude of the voltage applied to the V_{CC} terminal of LM2901/2903 series comparator package. The output can also be used as a simple SPST switch to ground (when a pull-up resistor is not used).

The amount of current the output device can sink is limited by the drive available (which is independent of V_{CC}) and the β of this device. When the maximum current limit is reached (approximately 16mA), the output transistor will come out of saturation and the output voltage will rise very rapidly. The output saturation voltage is limited by the approximately 60Ω R_{SAT} of the output transistor. The low offset voltage of the output transistor (1.0mV) allows the output to clamp essentially to ground level for small load currents.



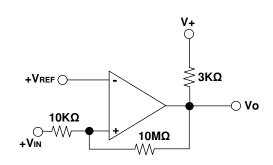
Typical Application Circuit (V_{CC} = 5.0 V_{DC})



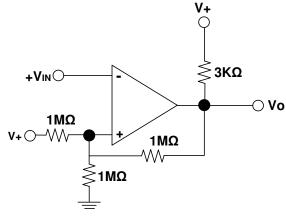
+5V_{DC}

10KΩ

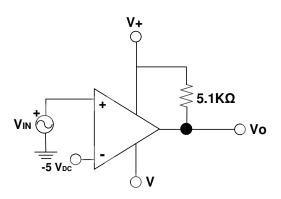
Driving TTL



Non-Inverting Comparator with Hysteresis



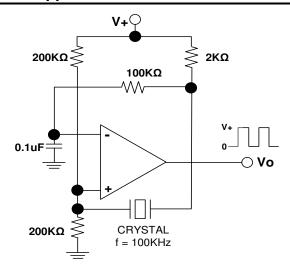
Inverting Comparator with Hysteresis



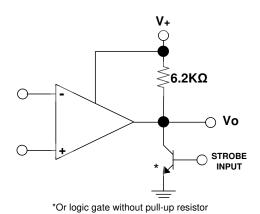
Comparator with a Negative Reference



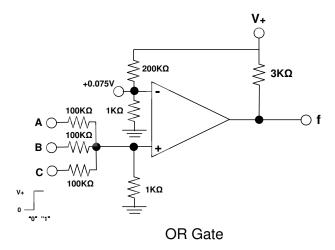
Typical Application Circuit (continued) ($V_{CC} = 5.0 V_{DC}$)

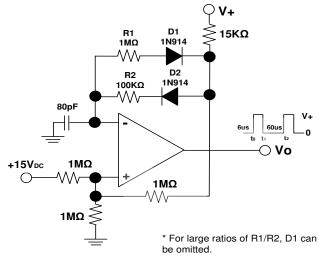


Crystal Controlled Oscillator

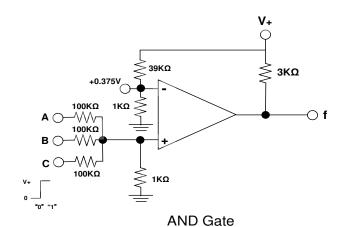


Output Strobing





Pulse Generator

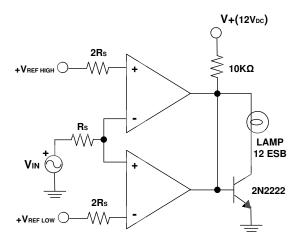


V₊
0
100KΩ
3KΩ
V_{out}
D1
100KΩ
V_{out}
D2
D3
C D4
D4
D D4

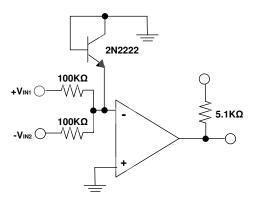
Large Fan-in AND Gate



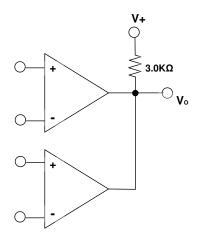
Typical Application Circuit (cont.) (V_{CC} = 5.0 V_{DC})



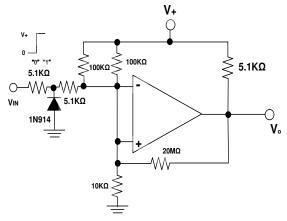
Limit Comparator



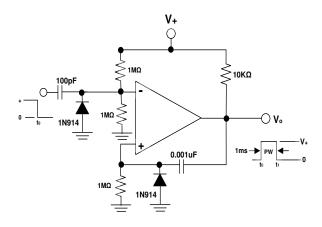
Comparing Input Voltage of Opposite Polarity



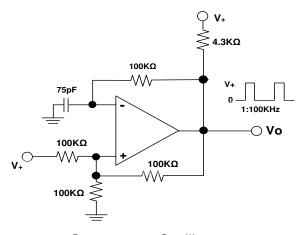
ORing the Outputs



Zero Crossing Detector (Single Power Supply)



One-Shot Multivibrator



Squarewave Oscillator



Ordering Information

LM290<u>X X Q XXX</u> - <u>XX</u>

Channel Offset Grade Qualification Grade Packages Packing

1: Quad channel Blank: Normal Q: Automotive T14: TSSOP-14 -13: 13" Tape & Reel

3 : Dual channel A : Low V_{IO} S14 : SO-14

S:SO-8 TH:TSSOP-8 M8:MSOP-8

Part Number	Package	Packaging	13" Tape and Reel		Qualification Grade
Part Number	Code	(Note 16)	Quantity	Part Number Suffix	(Note 17)
LM2901QT14-13	T14	TSSOP-14	2,500/Tape & Reel	-13	Automotive Compliant
LM2901AQT14-13	T14	TSSOP-14	2,500/Tape & Reel	-13	Automotive Compliant
LM2901QS14-13	S14	SO-14	2,500/Tape & Reel	-13	Automotive Compliant
LM2901AQS14-13	S14	SO-14	2,500/Tape & Reel	-13	Automotive Compliant
LM2903QS-13	S	SO-8	2,500/Tape & Reel	-13	Automotive Compliant
LM2903AQS-13	S	SO-8	2,500/Tape & Reel	-13	Automotive Compliant
LM2903QTH-13	TH	TSSOP-8	2,500/Tape & Reel	-13	Automotive Compliant
LM2903AQTH-13	TH	TSSOP-8	2,500/Tape & Reel	-13	Automotive Compliant
LM2903QM8-13	M8	MSOP-8	2,500/Tape & Reel	-13	Automotive Compliant
LM2903AQM8-13	M8	MSOP-8	2,500/Tape & Reel	-13	Automotive Compliant

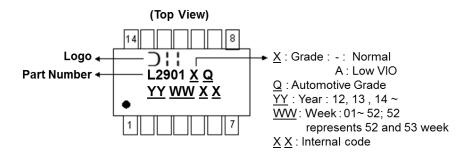
Notes: 16. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

^{17.} LM2901Q/2903Q have been qualified to AEC-Q100 grade 1 and are classified as "Automotive Compliant" which supports PPAP documentation. See LM2901/2903 datasheet for commercial qualified versions.

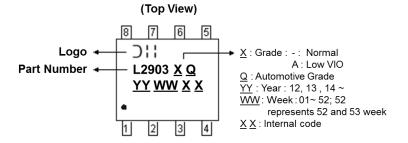


Marking Information

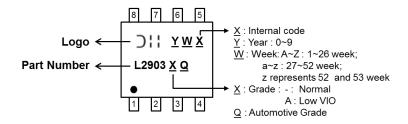
(1) TSSOP-14 and SO-14



(2) SO-8



(3) MSOP-8 and TSSOP-8

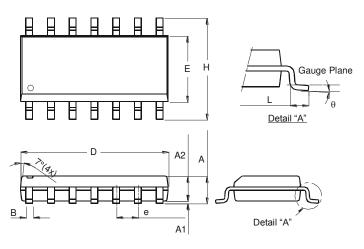




Package Outline Dimensions

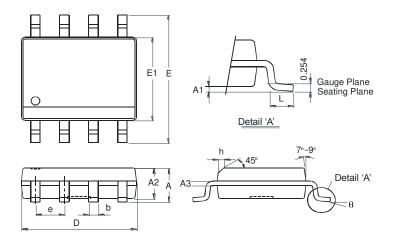
Please see http://www.diodes.com/package-outlines.html for the latest version.

SO-14



	SO-14					
Dim	Min	Max				
Α	1.47	1.73				
A1	0.10	0.25				
A2	1.45	Тур				
В	0.33	0.51				
D	8.53	8.74				
Е	3.80	3.99				
е	1.27	Тур				
Н	5.80	6.20				
L	0.38	1.27				
θ	0°	8°				
All Di	mension	s in mm				

SO-8



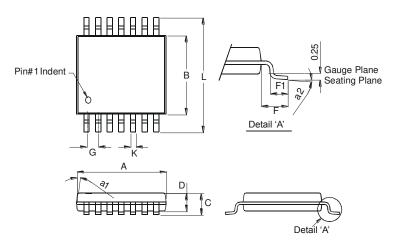
	SO-8				
Dim	Min	Max			
Α	_	1.75			
A1	0.10	0.20			
A2	1.30	1.50			
A3	0.15	0.25			
b	0.3	0.5			
D	4.85	4.95			
Е	5.90	6.10			
E1	3.85	3.95			
е	1.27	Тур			
h	_	0.35			
L	0.62	0.82			
θ	0°	8°			
All Di	All Dimensions in mm				



Package Outline Dimensions (Continued)

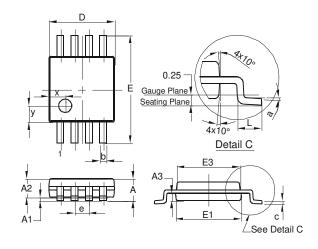
Please see http://www.diodes.com/package-outlines.html for the latest version.

TSSOP-14



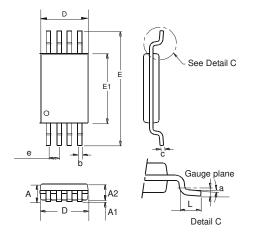
	TSSOP-1	4
Dim	Min	Max
a1	7° (4X)
a2	0°	8°
Α	4.9	5.10
В	4.30	4.50
С	_	1.2
D	0.8	1.05
F	1.00	Тур
F1	0.45	0.75
G	0.65	Тур
K	0.19	0.30
L	6.40	Тур
All Dir	nensions	s in mm

MSOP-8



	MSOP-8						
Dim	Min	Max	Тур				
Α		1.10	_				
A 1	0.05	0.15	0.10				
A2	0.75	0.95	0.86				
A 3	0.29	0.49	0.39				
b	0.22	0.38	0.30				
C	0.08	0.23	0.15				
D	2.90	3.10	3.00				
Е	4.70	5.10	4.90				
E1	2.90	3.10	3.00				
E 3	2.85	3.05	2.95				
е		_	0.65				
L	0.40	0.80	0.60				
а	0°	8°	4°				
X			0.750				
у			0.750				
AII E	Dimen	sions	in mm				

TSSOP-8



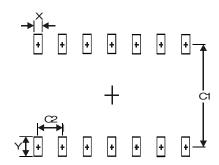
	TSSOP-8					
Dim	Min	Max	Тур			
а	0.09		_			
Α	_	1.20				
A 1	0.05	0.15	_			
A2	0.825	1.025	0.925			
b	0.19	0.30				
С	0.09	0.20	_			
D	2.90	3.10	3.025			
е			0.65			
E			6.40			
E1	4.30	4.50	4.425			
L	0.45	0.75	0.60			
All	Dimens	ions in	mm			



Suggested Pad Layout

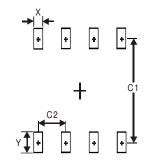
Please see http://www.diodes.com/package-outlines.html for the latest version.

SO-14



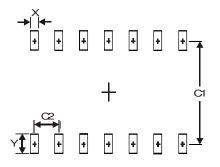
Dimensions	Value (in mm)
X	0.60
Υ	1.50
C1	5.4
C2	1.27

SO-8



Dimensions	Value (in mm)
Х	0.60
Υ	1.55
C1	5.4
C2	1.27

TSSOP-14



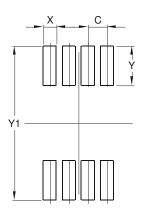
Dimensions	Value (in mm)
Х	0.45
Υ	1.45
C1	5.9
C2	0.65



Suggested Pad Layout (Continued)

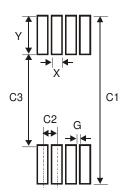
Please see http://www.diodes.com/package-outlines.html for the latest version.

MSOP-8



Dimensions	Value (in mm)
С	0.650
X	0.450
Υ	1.350
Y1	5.300

TSSOP-8



Dimensions	Value (in mm)
Х	0.45
Υ	1.78
C1	7.72
C2	0.65
C3	4.16
Ð	0.20



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