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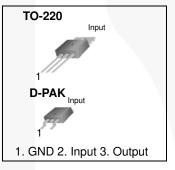
## KA79MXX / LM79MXX 3-Terminal 0.5 A Negative Voltage Regulator

## Features

- No External Components Required
- Output Current in Excess of 0.5 A
- Internal Thermal Overload
- Internal Short-Circuit Current Limiting
- Output Transistor Safe Area Compensation
- Output Voltages: -5 V, -12 V

## Description

The KA79MXX / LM79MXX series of three terminal medium current negative voltage regulators are monolithic integrated circuits designed as fixed-voltage regulators. These regulators employ internal current limiting, thermal shutdown, and safe area compensation.



## Ordering Information<sup>(1)</sup>

Product Number	Package	Packing Method	Operating Temperature	
KA79M05TU	TO-220 (Dual Gauge)	Rail		
KA79M05RTM		Tana and Daal		
KA79M05RTF	D-PAK		0 to +125°C	
KA79M12RTM	- D-FAN	Tape and Reel	0 10 +125 C	
KA79M12RTF				
LM79M05CT	TO-220 (Single Gauge)	Rail		

## Note:

1. Refer to below figure for TM / TF suffix of DPAK packing option.



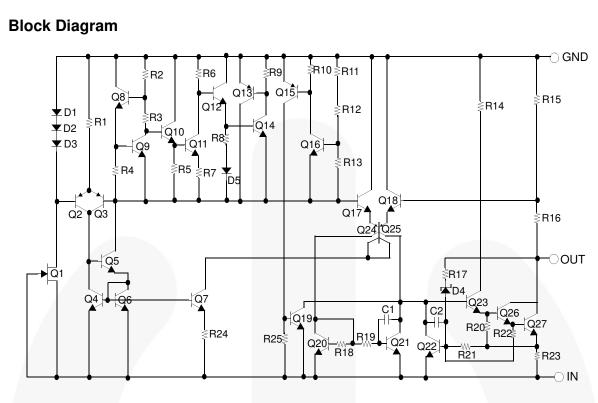


Figure 1. Block Diagram

## Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

Symbol	Parameter		Value	Unit
VI	Input Voltage	$V_0 = -5 V \text{ to } -12 V$	-35	V
R <sub>θJC</sub>	Thermal Resistance, Junction-Case	TO-220	5	°C/W
R <sub>θJA</sub>	Thermal Resistance, Junction-Air	TO-220	65	°C/W
T <sub>OPR</sub>	Operating Temperature Range	0 to +125	°C	
T <sub>STG</sub>	Storage Temperature Range		-65 to +150	°C

## Electrical Characteristics (KA79M05 / KA79M05R / LM79M05)

Refer to test circuit,  $0^{\circ}C \le T_J \le +125^{\circ}C$ ,  $I_O = 350$  mA,  $V_I = -10$  V,  $C_I = 0.33 \ \mu$ F,  $C_O = 0.1 \ \mu$ F unless otherwise specified.

Symbol	Parameter	Conditions		Min.	Тур.	Max.	Unit
Vo Output Voltage		$T_J = +25^{\circ}C$		-4.80	-5.00	-5.20	v
V <sub>O</sub>	Oulput voltage	$I_{O} = 5 \text{ mA to } 350$	) mA, V <sub>I</sub> = -7 V to -25 V	-4.75	-5.00	-5.25	v
$\Delta V_{O}$ Line Regulation <sup>(2)</sup>	T _ 125°C	V <sub>I</sub> = -7 V to -25 V		7	50	mV	
$\Delta V_O$	Line Regulation ?	T <sub>J</sub> =+25°C	$V_{I} = -8 V \text{ to } -25 V$		2	30	mv
$\Delta V_O$	Load Regulation <sup>(2)</sup>	$I_{\rm O} = 5  \rm{mA}  \rm{to}  500$	) mA, T <sub>J</sub> = +25°C		30	100	mV
Ι <sub>Q</sub>	Quiescent Current	T <sub>J</sub> = +25°C			3.0	6.0	mA
A 1		I <sub>O</sub> = 5 mA to 350 mA				0.4	<b>س</b> ۸
$\Delta I_Q$	Quiescent Current Change	$I_{O} = 200 \text{ mA}, V_{I} = -8 \text{ V to } -25 \text{ V}$				0.4	mA
$\Delta Vo/\Delta T$	Output Voltage Drift	I <sub>O</sub> = 5 mA			-0.2		mV/°C
V <sub>N</sub>	Output Noise Voltage	$f = 10 \text{ Hz to } 100 \text{ kHz}, T_A = +25^{\circ}\text{C}$			40		μV
RR	Ripple Rejection	$f = 120 \text{ Hz}, \text{ V}_{\text{J}} = -8 \text{ V to } -18 \text{ V}$		54	60		dB
VD	Dropout Voltage	$T_{\rm J}$ = +25°C, $I_{\rm O}$ = 500 mA			1.1		V
I <sub>SC</sub>	Short-Circuit Current	$T_J = +25^{\circ}C, V_I = -35 V$			140		mA
I <sub>PK</sub>	Peak Current	$T_J = +25^{\circ}C$			650		mA

### Note:

2. Load and line regulation are specified at constant junction temperature. Change in V<sub>O</sub> due to heating effects must be taken into account separately. Pulse testing with low duty is used.

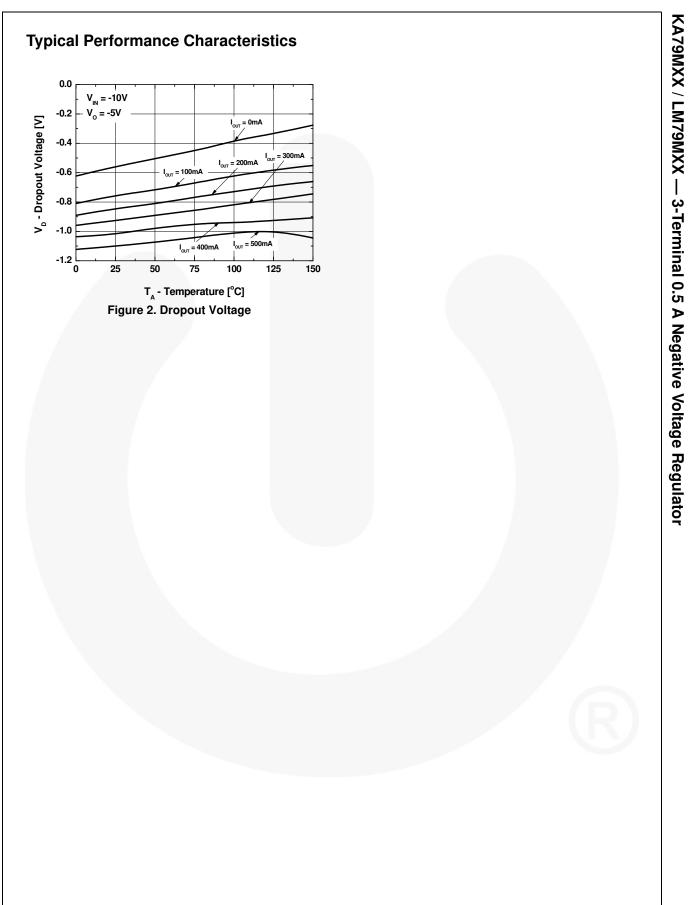
## **Electrical Characteristics (KA79M12R)**

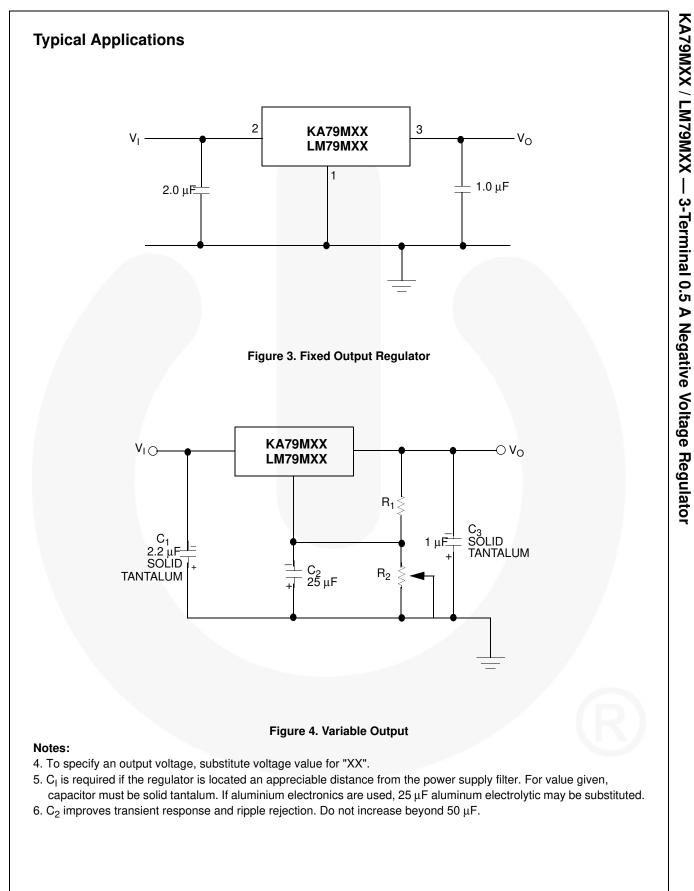
Refer to test circuit,  $0^{\circ}C \le T_J \le +125^{\circ}C$ ,  $I_O = 350$  mA,  $V_I = -19$  V,  $C_I = 0.33$   $\mu$ F,  $C_O = 0.1$   $\mu$ F unless otherwise specified.

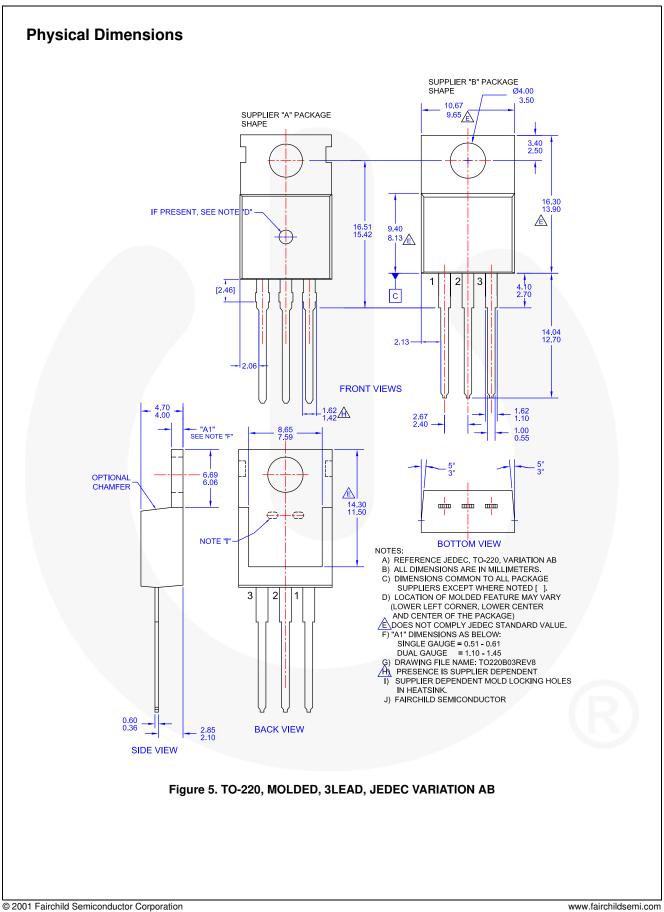
Symbol	Parameter	Conditions		Min.	Тур.	Max.	Unit
			$T_J = +25^{\circ}C$		-12.0	-12.5	
V <sub>O</sub>	Output Voltage	I <sub>O</sub> = 5 mA to 350 mA, V <sub>I</sub> = -14.5 V to -30 V		-11.4	-12.0	-12.6	V
41/	Line Regulation <sup>(3)</sup>	$T_J = +25^{\circ}C \frac{V_I = -14.5 \text{ V to } -30 \text{ V}}{V_I = -15 \text{ V to } -25 \text{ V}}$			8.0	80	mV
ΔvO	$\Delta V_{O}$ Line Regulation <sup>(3)</sup>		V <sub>I</sub> = -15 V to -25 V		3.0	50	
$\Delta V_O$	Load Regulation <sup>(3)</sup>	$T_J = +25^{\circ}C$	$T_{J} = +25^{\circ}C$ $I_{O} = 5.0 \text{ mA to } 500 \text{ mA}$		30	240	mV
Ι <sub>Q</sub>	Quiescent Current	$T_J = +25^{\circ}C$	$T_J = +25^{\circ}C$		3	6	mA
A I		I <sub>O</sub> = 5 mA to 350 mA				0.4	
ΔIQ	ΔI <sub>Q</sub> Quiescent Current Change		′ to -30 V			0.4	mA
$\Delta V_O / \Delta T$	Output Voltage Drift	I <sub>O</sub> = 5 mA			-0.8		mV/°C
V <sub>N</sub>	Output Noise Voltage	$f = 10 \text{ Hz to } 100 \text{ kHz}, T_A = +25^{\circ}\text{C}$			75		μV
RR	Ripple Rejection	f = 120 Hz, V <sub>I</sub> = -15 V to -25 V		54	60		dB
V <sub>D</sub>	Dropout Voltage	l <sub>O</sub> = 500 mA, T <sub>J</sub> = +25°C			1.1		V
I <sub>SC</sub>	Short Circuit Current	$V_{I} = -35 \text{ V},  \text{T}_{J} = +25^{\circ}\text{C}$			140		mA
I <sub>PK</sub>	Peak Current	$T_J = +25^{\circ}C$			650		mA

### Note:

3. Load and line regulation are specified at constant junction temperature. Change in V<sub>O</sub> due to heating effects must be taken into account separately. Pulse testing with low duty is used.

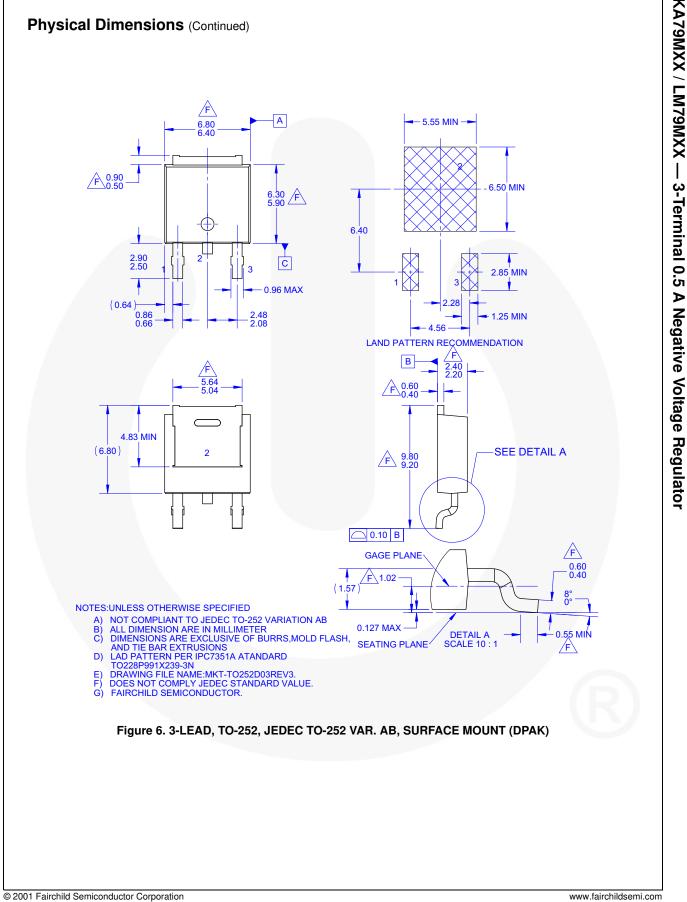






KA79MXX / LM79MXX —

3-Terminal 0.5 A Negative Voltage Regulator



KA79MXX / LM79MXX ---

KA79MXX / LM79MXX Rev. 1.1.1

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