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DEVICE SPECIFICATION

TFT-LCD Module

MODEL No.

LQ170E1LG11

CUSTOMER : _____

CUSTOMER'S APPROVAL

DATE _____

BY _____

PRESENTED

BY *K. Shiono*

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1. Application

This specification applies to the color 17.0 SXGA TFT-LCD module LQ170E1LG11.^{△ 2}

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2. Overview

This module is a color active matrix LCD module incorporating amorphous silicon TFT (Thin Film Transistor). It is composed of a color TFT-LCD panel, driver ICs, control circuit, power supply circuit and a back light unit. Graphics and texts can be displayed on a 1280× RGB× 1024 dots panel with about 16 million colors (6bit + 2bitFRC) by using LVDS (Low Voltage Differential Signaling) and supplying +5.0V DC supply voltages for TFT-LCD panel driving and supply voltage for backlight.

3. Mechanical Specifications

Parameter	Specifications	Unit
Display size	43 (Diagonal)	cm
	17.0 (Diagonal)	Inch
Active area	337.9 (H)× 270.3 (V)	mm
Pixel format	1280 (H)× 1024 (V)	Pixel
	(1 pixel = R+G+B dots)	
Pixel pitch	0.264 (H)× 0.264 (V)	mm
Pixel configuration	R, G, B vertical stripe	
Display mode	Normally white	
Unit outline dimensions *1	358.5(W)× 296.5(H)× 17.0(D) typ.	mm
Mass	1800 (MAX)	g
Surface treatment	Anti-glare and hard-coating 3H (Haze value = 25)	

*1.Note: excluding back light cables, cover and pet sheets.

The thickness of module (D) doesn't contain the projection.

Outline dimensions are shown in Fig.1.

4. Input Terminals

4-1. TFT-LCD panel driving

CN1 (Interface signals and +5.0V DC power supply)

Using connectors : FI-XB30SL-HF10 (Japan Aviation Electronics Ind, Ltd) or
MDF76KBW-30S-1H (Hirose Electric Co., Ltd.) or
Equivalent Δ 2

Corresponding connectors : FI-X30C *, FI-X30H *, FI-X30M * (Japan Aviation Electronics Ind, Ltd) or
MDF76G-30P-1SD (Hirose Electric Co., Ltd.) or equivalent

Corresponding LVDS Transmitter : THC63LVDM83R(Thine) or compatible

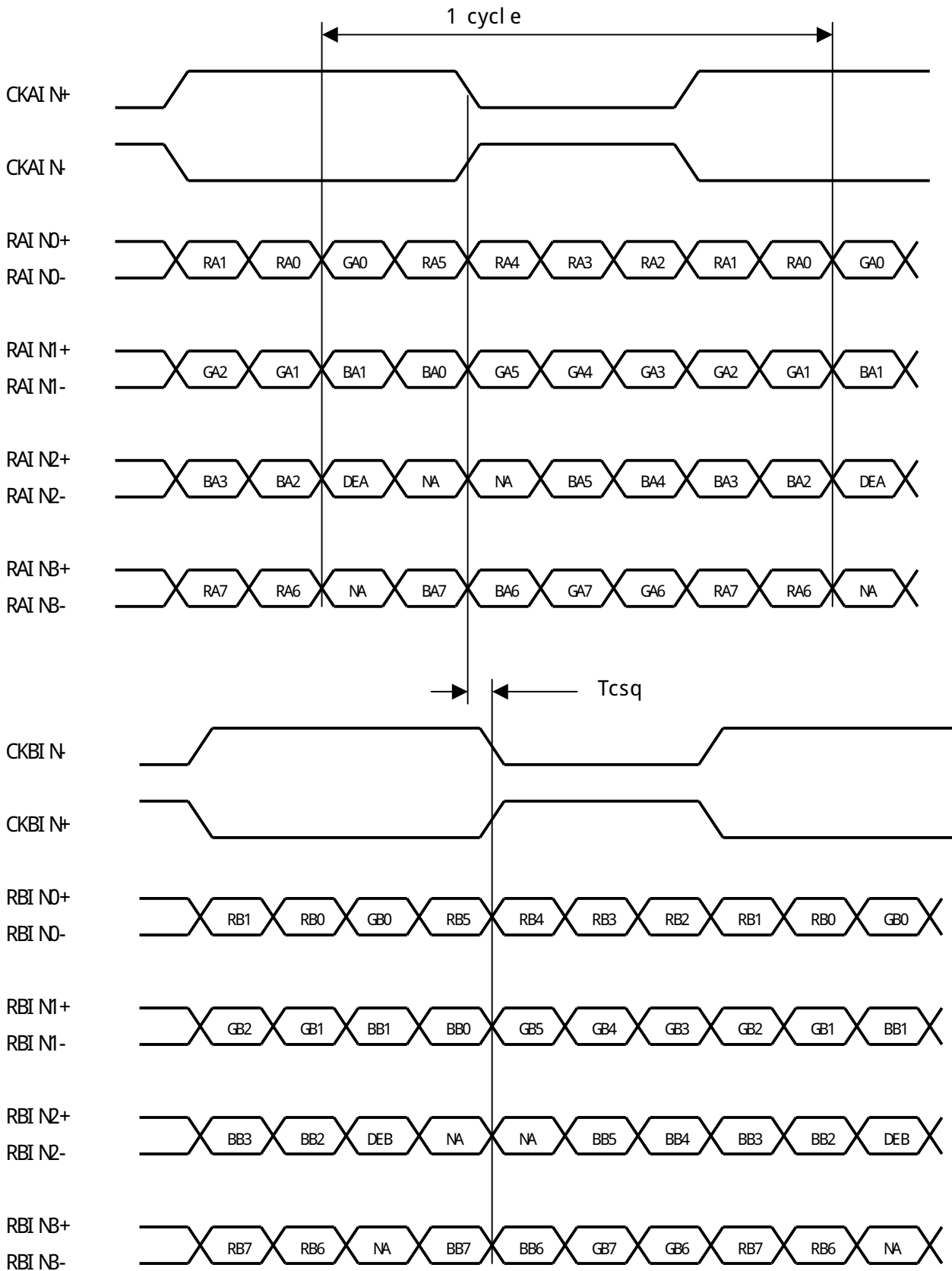
Pin No.	Symbol	Function	Remark
1	RAin0-	Negative (-) LVDS CH0 differential data input (A port)	LVDS
2	RAin0+	Positive (+) LVDS CH0 differential data input (A port)	LVDS
3	RAin1-	Negative (-) LVDS CH1 differential data input (A port)	LVDS
4	RAin1+	Positive (+) LVDS CH1 differential data input (A port)	LVDS
5	RAin2-	Negative (-) LVDS CH2 differential data input (A port)	LVDS
6	RAin2+	Positive (+) LVDS CH2 differential data input (A port)	LVDS
7	GND	GND	
8	CKAin-	Negative (-) LVDS differential clock input (A port)	LVDS
9	CKAin+	Positive (+) LVDS differential clock input (A port)	LVDS
10	RAin3-	Negative (-) LVDS CH3 differential data input (A port)	LVDS
11	RAin3+	Positive (+) LVDS CH3 differential data input (A port)	LVDS
12	RBin0-	Negative (-) LVDS CH0 differential data input (B port)	LVDS
13	RBin0+	Positive (+) LVDS CH0 differential data input (B port)	LVDS
14	GND	GND	
15	RBin1-	Negative (-) LVDS CH1 differential data input (B port)	LVDS
16	RBin1+	Positive (+) LVDS CH1 differential data input (B port)	LVDS
17	GND	GND	
18	RBin2-	Negative (-) LVDS CH2 differential data input (B port)	LVDS
19	RBin2+	Positive (+) LVDS CH2 differential data input (B port)	LVDS
20	CKBin-	Negative (-) LVDS differential clock input (B port)	LVDS
21	CKBin+	Positive (+) LVDS differential clock input (B port)	LVDS
22	RBin3-	Negative (-) LVDS CH3 differential data input (B port)	LVDS
23	RBin3+	Positive (+) LVDS CH3 differential data input (B port)	LVDS
24	GND	GND	
25	NC	No Connection	
26	NC	No Connection	
27	NC	No Connection	
28	VCC	+5V power supply	
29	VCC	+5V power supply	
30	VCC	+5V power supply	

【Note1】 This module has dual pixel port to receive dual pixel data at the same time . A port receives first pixel data and B port receives second pixel data in dual pixel data.

4-2 Data Mapping

【note1】 pin assignment with LVDS_SET pin (Thine:THC63LVDM83R)

Transmitter		Data arrangement
Pin No	Data	
51	TA0	R0(LSB)
52	TA1	R1
54	TA2	R2
55	TA3	R3
56	TA4	R4
3	TA5	R5
4	TA6	G0(LSB)
6	TB0	G1
7	TB1	G2
11	TB2	G3
12	TB3	G4
14	TB4	G5
15	TB5	B0(LSB)
19	TB6	B1
20	TC0	B2
22	TC1	B3
23	TC2	B4
24	TC3	B5
27	TC4	(NA)
28	TC5	(NA)
30	TC6	DE
50	TD0	R6
2	TD1	R7(MSB)
8	TD2	G6
10	TD3	G7(MSB)
16	TD4	B6
18	TD5	B7(MSB)
25	TD6	(NA)

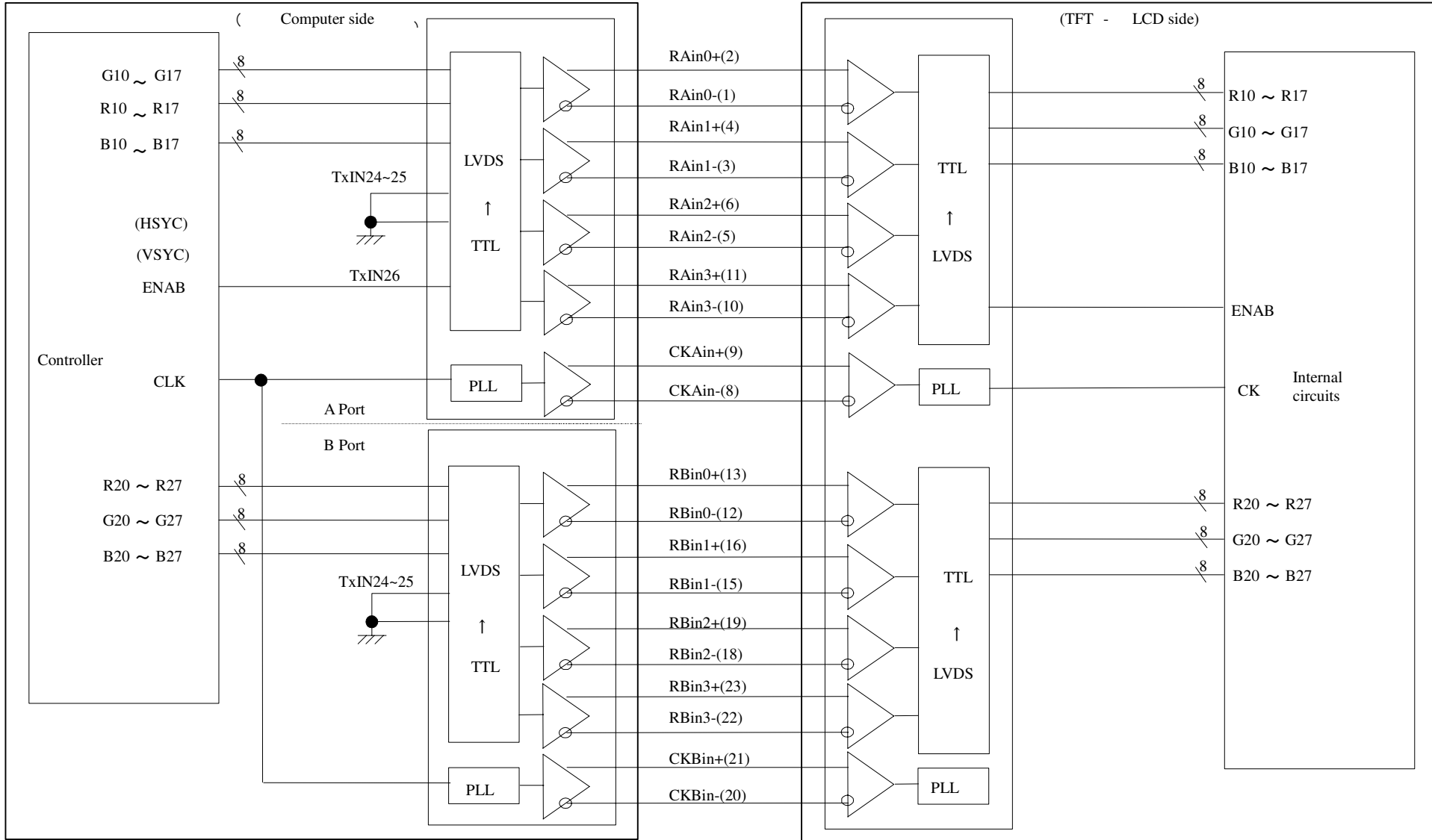


DE: Display Enable

NA: Not Available

Interface block diagram

Using receiver: LVDS6CP_BU72**(ROHM)/THC63LVD824(Thine) built-in control IC, Corresponding Transmitter:THC63LVDM83R(THine electronics) or compatible



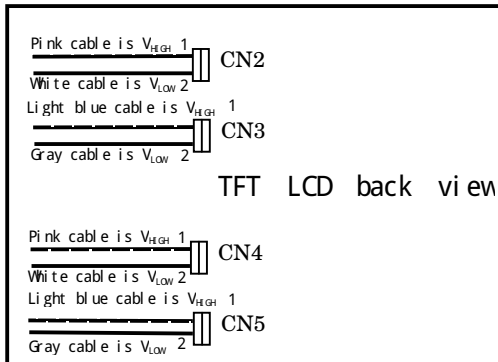
4-4 Backlight

CN 2, 3, 4, 5

The module-side connector : BHSR-02VS-1 (JST)

The user-side connector : SM02B-BHSS-1-TB (JST)

Pin no.	symbol	I/O	Function
1	V _{HIGH}	I	Power supply for lamp (High voltage side)
2	V _{LOW}	I	Power supply for lamp (Low voltage side)



5. Absolute Maximum Ratings

Parameter	Symbol	Condition	Ratings	Unit	Remark
Supply voltage	V _{CC}	T _a =25°C	0 ~ +6.0	V	
Lamp kick-off voltage	V _S	-	MAX.2000	V _{rms}	
Storage temperature	T _{STG}	-	- 25 ~ + 60	°C	【Note1】
Operating temperature (Ambient)	T _{OPA}	-	0 ~ + 50	°C	

【Note1】 Humidity : 95%RH Max. (T_a ≤ 40°C)Maximum wet-bulb temperature at 39°C or less. (T_a>40°C)

No condensation.

6. Electrical Characteristics

6-1. TFT-LCD panel driving

Ta = 25°C

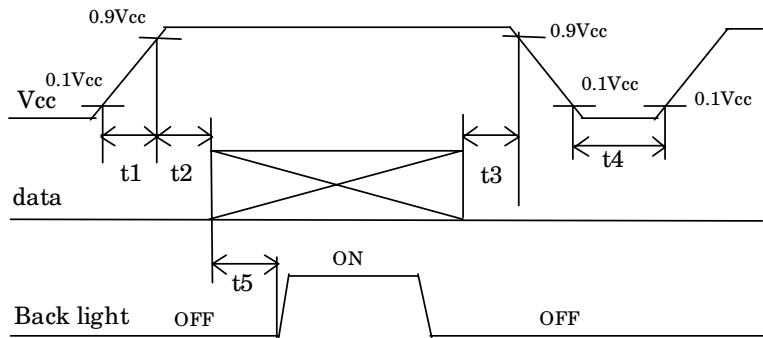
Parameter		Symbol	Min.	Typ.	Max.	Unit	Remark
Vcc	Supply voltage	Vcc	+4.5	+5.0	+5.5	V	【Note2】
	Current dissipation	Icc	-	590	810	mA	【Note3】
	Rush current	Irush	-	640	-	mA	【Note4】 Δ 1
Permissive input ripple voltage		V _{RF}	-	-	100	mVp-p	V _{CC} =+5.0V Δ 3
Differential input threshold voltage	High	V _{TH}	-	-	+100	mV	V _{CM} =+1.2V 【Note1】
	Low	V _{TL}	-100	-	-	mV	
Terminal resistor		R _T	-	100	-	Ω	Differential input

【Note1】 V_{CM} : Common mode voltage of LVDS driver.

【Note2】

1) On-off sequences of Vcc and data

- 0 < t1 ≤ 10ms
- 0 < t2 ≤ 10ms
- 0 < t3 ≤ 1s
- 1s ≤ t4
- 200ms ≤ t5



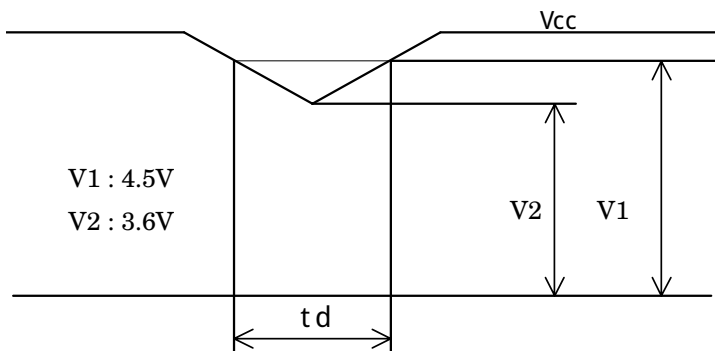
Power sequence for Backlight is not especially specified, however it is recommended to consider some timing difference between LVDS input and Backlight input as shown above.

If the Backlight lights on before LCD starting, or if the Backlight is kept on after LCD stopping, the screen may look white for a moment or abnormal image may be displayed.

This is caused by variation in output signal from timing generator at LVDS input on or off. It does not cause the damage to the LCD module.

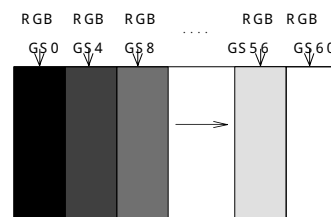
2) Dip conditions for supply voltage

- 1) V2 ≤ Vcc < V1
td ≤ 10ms
- 2) Vcc < V2
Vcc-dip conditions should also follow the on-off conditions.



【Note3】 Typical current situation : 16-gray-bar pattern

V_{CC}=+5.0V, CK=67.5MHz
Horizontal period =12.5 us
Gray scale : GS(4n)
n=0 ~ 15



The explanation of each gray scale, GS(4n), is described below section 8-2.

【Note4】 The rush current is measured at this situation. (t1=470μ sec.) Δ 1

6-2. Backlight

The back light system is an edge-lighting type with 4CCFTs (Cold Cathode Fluorescent Tube).

The characteristics of the lamp are shown in the following table.

The value mentioned below is at the case of one CCFT.

CCFT Model Name : MBT24JB413AX347.3MWRAU/C (HARISON TOSHIBA LIGHTING Corp.)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Remark
Lamp current range	I_L	3.0	6.5	7.0	mArms	【Note1】 【Note.6】
Lamp voltage	V_L	-	630	700	Vrms	$I_L=6.5\text{mArms}$ $T_a=25^\circ\text{C}$ 60kHz 【Note.6】
Lamp power consumption	P_L	-	4.1	4.55	W	【Note2】 $I_L=6.5\text{mArms}$ $T_a=25^\circ\text{C}$ 60kHz
Lamp frequency	F_L	40	60	70	kHz	【Note3】
Kick-off voltage	V_s	-	-	1700	Vrms	$T_a=0^\circ\text{C}$ 【Note4】
Lamp life time	T_L	50,000	-	-	hour	【Note5】

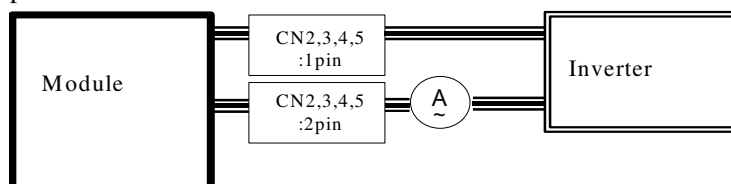
【Note1】 A lamp can be light in the range of lamp current shown above.

Maximum rating for current is measured by high frequency current measurement equipment connected to V_{LOW} at circuit showed below.

(Note : To keep enough kick-off voltage and necessary steady voltage for CCFT.)

Lamp frequency : 40 ~ 70kHz

Ambient temperature : 0 ~ 50°C



【Note2】 Referential data per one CCFT by calculation ($I_L \times V_L$).

The data don't include loss at inverter.

【Note3】 Lamp frequency of inverter may produce interference with horizontal synchronous frequency, and this may cause horizontal beat on the display. Therefore, adjust lamp frequency, and keep inverter as far as from module or use electronic shielding between inverter and module to avoid interference.

【Note4】 It is defined at 27pF for the ballast capacitor of a DC-AC inverter.

The kick-off voltage may rise up in the user set, please decide the open output voltage by checking not to occur lighting failure under operating state.

The open output voltage should be applied to the lamp for more than 1 second to startup.

Otherwise the lamp may not be turned on.

【Note5】 Above value is applicable when the long side of LCD module is placed horizontally.

(Landscape position)

(Lamp lifetime may vary if LCD module is in portrait position due to the change of mercury density inside the lamp.)

Lamp life time is defined as the time when either ① or ② occurs in the continuous operation under the condition of $T_a=25^\circ\text{C}$ and $I_L=6.5\text{ mA rms}$.

① Brightness becomes 50% of the original value under standard condition.

② Kick-off voltage at $T_a=0^\circ\text{C}$ exceeds 1700 V_{rms} value.

【Note6】 Crest Factor of inverter output waveform is defined as

$$0.9 \times \sqrt{2} \times I_{rms} < I_p \& I_p < 1.1 \times \sqrt{2} \times I_{rms}$$

However, please apply a plus/minus symmetrical sine-wave within 10% of un-symmetrical ratio without spike-wave to both space and peak of I_L/V_L waveform.

I_p : The value of plus peak Lamp current.

I_p : The value of minus peak Lamp current.

《Note》

The performance of the backlight, for example lifetime or brightness, is much influenced by the characteristics of the DC-AC inverter for the lamp. When you design or order the inverter, please make sure that a poor lighting caused by the mismatch of the backlight and the inverter (miss-lighting, flicker, etc.) never occurs. When you confirm it, the module should be operated in the same condition as it is installed in your instrument.

Use the lamp inverter power source incorporating such safeguard as overvoltage / overcurrent protective circuit or lamp voltage waveform detection circuit, which should have individual control of each lamp.

In case one circuit without such individual control is connected to more than two lamps, excessive current may flow into one lamp when the other one is not in operation.

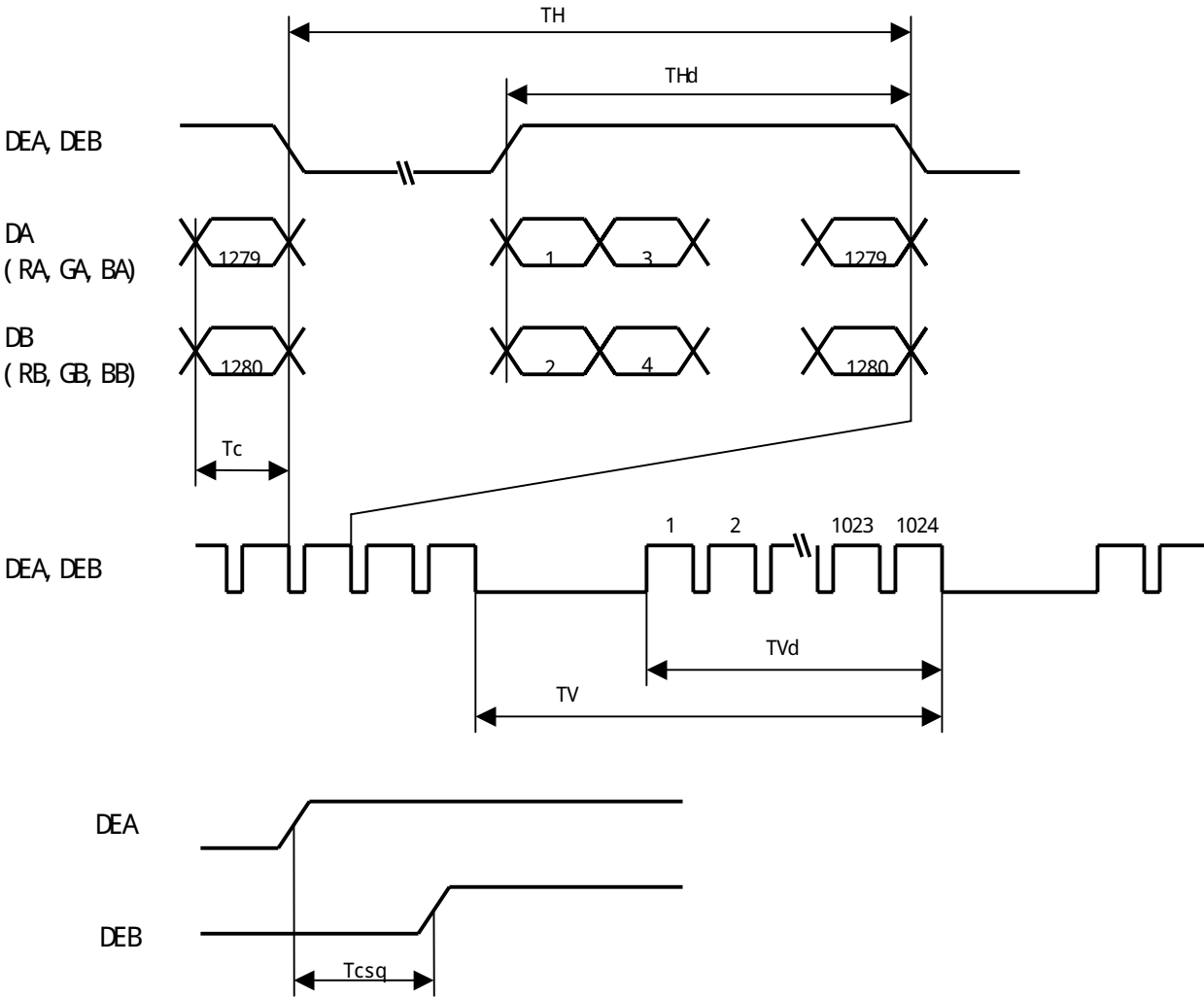
7. Timing characteristics of input signals

7-1. Timing characteristics $\Delta 2$

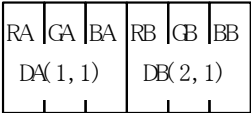
Parameter		Symbol	Min.	Typ.	Max.	Unit	Remark
Clock	Frequency	1/Tc	40	54	67.5	MHz	
	Skew	Tcsq	-2	0	+2	ns	【Note1】
Data enable Signal	Horizontal period	TH	676	844	1010	clock	
			12.3	15.7	-	μ s	
	Horizontal period (High)	THd	640	640	640	clock	
	Vertical period	TV	1031	1066	2043	line	【Note2】
			13.3	16.7	-	ms	
Vertical period (High)	TVd	1024	1024	1024	line		

【Note1】 Lvds (A Side data)– Lvds (B side data) phase difference

【Note2】 In case of using the long vertical period, the deterioration of display quality, flicker, etc., may occur.



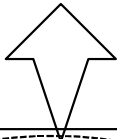
7-2. Input Data Signals and Display Position on the screen



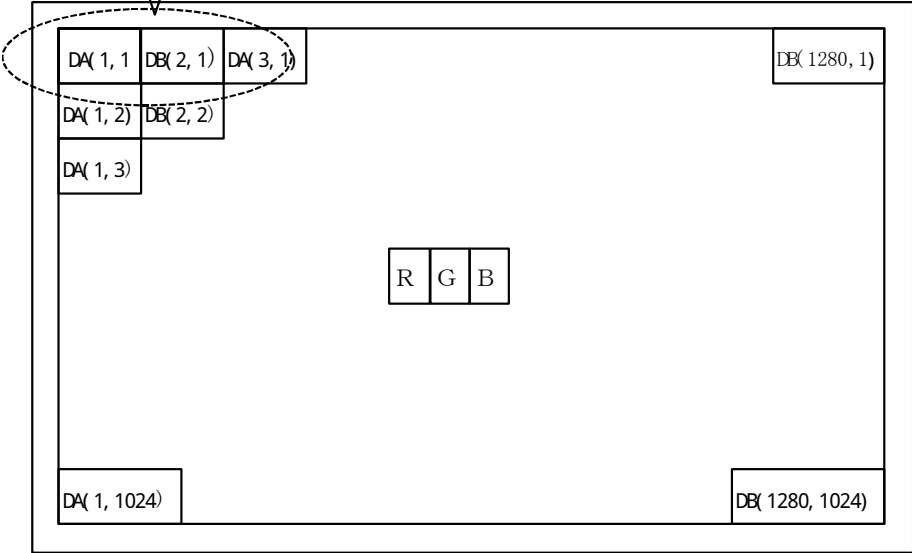
Two pixels-data are sampled at the same time.

* DA: RA0~RA7, GA0~GA7, BA0~BA7

* DB: RB0~RB7, GB0~GB7, BB0~BB7



Display position of input data (H, V)



8. Input Signals, Basic Display Colors and Gray Scale of Each Color

	Colors & Gray scale	Data signal																											
		Gray Scale	R0	R1	R2	R3	R4	R5	R6	R7	G0	G1	G2	G3	G4	G5	G6	G7	B0	B1	B2	B3	B4	B5	B6	B7			
Basic Color	Black	-	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Blue	-	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	X	X	1	1	1	1	1	1	1	1	
	Green	-	0	0	0	0	0	0	0	0	X	X	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	
	Cyan	-	0	0	0	0	0	0	0	0	X	X	1	1	1	1	1	1	X	X	1	1	1	1	1	1	1	1	
	Red	-	X	X	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Magenta	-	X	X	1	1	1	1	1	1	0	0	0	0	0	0	0	0	X	X	1	1	1	1	1	1	1	1	
	Yellow	-	X	X	1	1	1	1	1	1	X	X	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	
	White	-	X	X	1	1	1	1	1	1	X	X	1	1	1	1	1	1	X	X	1	1	1	1	1	1	1	1	
Gray Scale of Red	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	↑	GS1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Darker	GS2	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	↑	↓	↓							↓							↓												
	↓	↓	↓							↓							↓												
	Brighter	GS250	0	1	0	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	↓	GS251	1	1	0	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Red	GS252	X	X	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
Gray Scale of Green	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	↑	GS1	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Darker	GS2	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	↑	↓	↓							↓							↓												
	↓	↓	↓							↓							↓												
	Brighter	GS250	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	
	↓	GS251	0	0	0	0	0	0	0	0	1	1	0	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	
	Green	GS252	0	0	0	0	0	0	0	0	X	X	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	
Gray Scale of Blue	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	↑	GS1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	
	Darker	GS2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	
	↑	↓	↓							↓							↓												
	↓	↓	↓							↓							↓												
	Brighter	GS250	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1	1	
	↓	GS251	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	0	1	1	1	1	1	1	1	
	Blue	GS252	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	X	X	1	1	1	1	1	1	1	1	

0 : Low level voltage, 1 : High level voltage. X :Don't care.

Each basic color can be displayed in 253 gray scales from 8 bit data signals. According to the combination of total 24 bit data signals, the 16-million-color display can be achieved on the screen.

9. Optical Characteristics

Ta=25°C, Vcc =+5.0V

Parameter		Symbol	Condition	Min.	Typ.	Max.	Unit	Remark	
Viewing angle range	Vertical	θ_{11}	$CR \geq 5$	45	65	-	Deg.	【Note1,4】 $\Delta 1$	
		θ_{12}		70	85	-	Deg.		
	Horizontal	θ_{21}, θ_{22}		70	80	-	Deg.		
	Vertical	θ_{11}	$CR \geq 10$	35	50	-	Deg.		
		θ_{12}		50	70	-	Deg.		
	Horizontal	θ_{21}, θ_{22}		50	70	-	Deg.		
Contrast ratio		C R		300	450	-		【Note2,4】	
Response Time		$\tau_d + \tau_r$		-	16	-	ms	【Note3,4】	
Chromaticity of White	x	$\theta = 0^\circ$	0.283	0.313	0.343		【Note4】		
	y		0.299	0.329	0.359				
Chromaticity of Red	x		0.605	0.635	0.665				
	y		0.307	0.337	0.367				
Chromaticity of Green	x		0.257	0.287	0.317				
	y		0.583	0.613	0.643				
Chromaticity of Blue	x		0.114	0.144	0.174				
	y		0.056	0.086	0.116				
Luminance of white			YL	240	300	-		cd/m ²	I _L =6.5mA rms f _L =60kHz 【Note4】
White Uniformity			δ_w	-	-	1.33		-	【Note5】
TCO03 A2.3.4		δ_{w2}	-	-	1.7				
Cross Talk		Dsha	-	-	1.8	%	【Note6】		
γ Curve	V0	$\theta = 0^\circ$	-	0.29	-	%			
	V36		-	0.84	-	%			
	V73		-	4.29	-	%			
	V109		-	12.62	-	%			
	V146		-	28.40	-	%			
	V182		-	51.55	-	%			
	V219		-	81.55	-	%			
	V255		-	100	-	%			

※ The measurement shall be executed 30 minutes after lighting at rating.

The optical characteristics shall be measured in a dark room or equivalent state with the method shown in Fig.2 below.

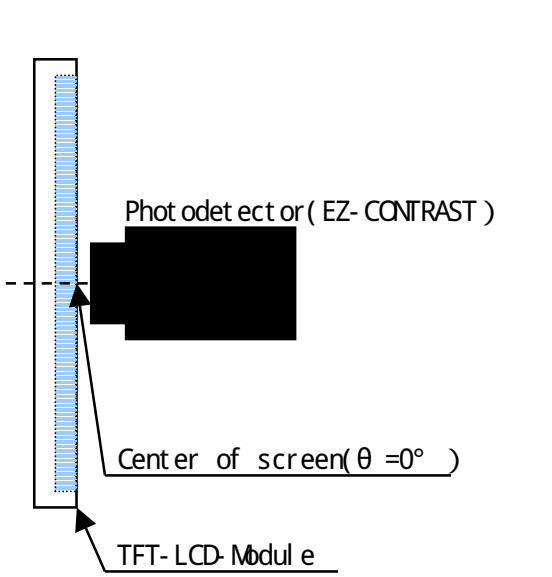


Fig2-1 Viewing angle measurement method

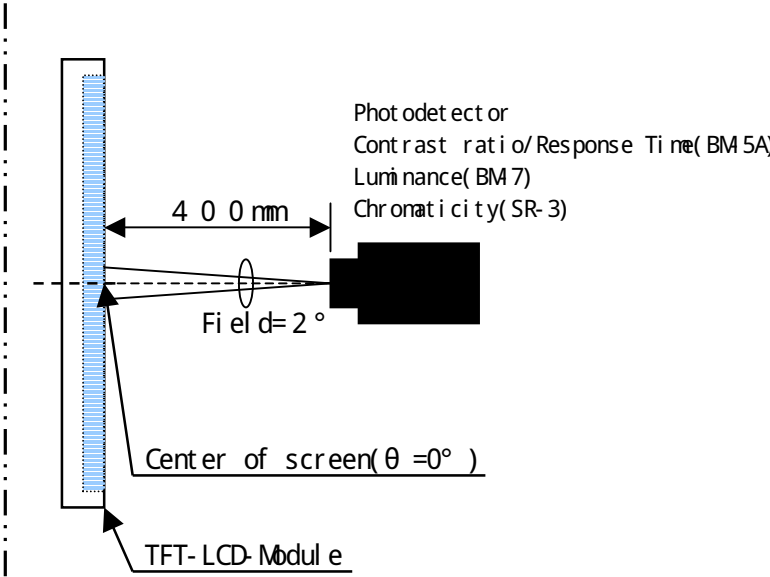
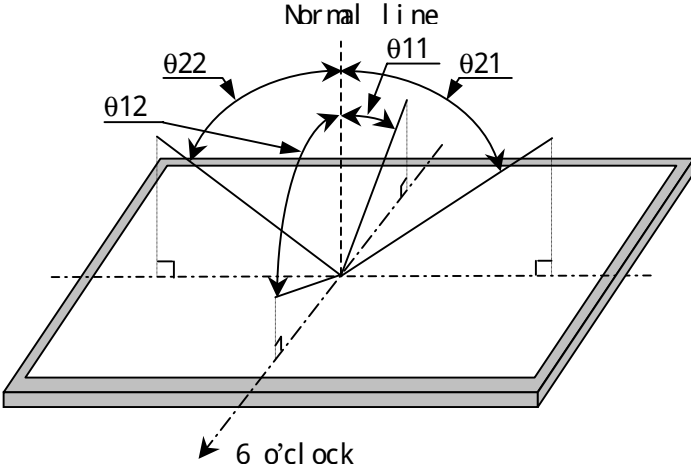


Fig2-2 Luminance/Contrast ratio/Response time/Chromaticity measurement method

Fig2 Optical characteristics measurement method

【Note1】 Definitions of viewing angle range:



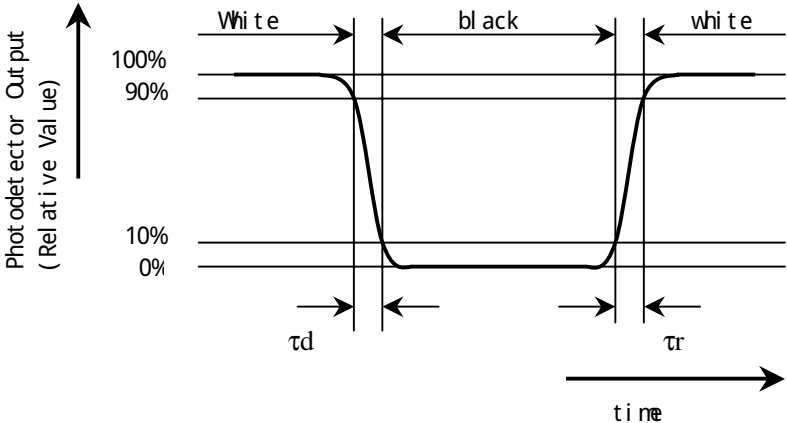
【Note2】 Definition of contrast ratio:

The contrast ratio is defined as the following.

$$\text{Contrast Ratio (CR)} = \frac{\text{Luminance (brightness) with all pixels white}}{\text{Luminance (brightness) with all pixels black}}$$

【Note3】 Definition of response time:

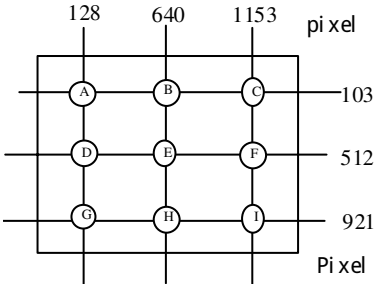
The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".



【Note4】 This shall be measured at center of the screen.

【Note5】 Definition of white uniformity:

White uniformity is defined as the following with nine measurements (A ~ I).

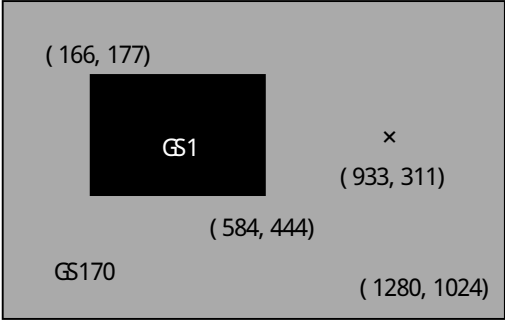


$$\delta W = \frac{\text{Maximum Luminance of nine points (brightness)}}{\text{Minimum Luminance of nine points (brightness)}}$$

【Note6】 Definition of corss talk:

We measured luminance in case there are a window frame and in case there are not a window frame with X points of the following figure. Then, we compared the measured values.

$$Dsha(\%) = \frac{|\text{Lumiance with a window frame} - \text{Lumiance without a window}|}{\text{Lumiance without a window}} \times 100$$



10. Mechanical characteristics

Parameter		Min.	Typ.	Max.	Unit
Mounting part	Tighten torque	0.313	0.343	0.372	N · m
		3.2	3.5	3.8	(kgf · cm)
	Re-screw times	–	–	5	times

11. Handling Precautions

- a) Be sure to turn off the power supply when inserting or disconnecting the cable.
- b) Be sure to design the cabinet so that the module can be installed without any extra stress such as warp or twist.
- c) Since the front polarize is easily damaged, pay attention not to scratch it.
- d) Since long contact with water may cause discoloration or spots, wipe off water drop immediately.
- e) When the panel surface is soiled, wipe it with absorbent cotton or other soft cloth.
- f) Since the panel is made of glass, it may break or crack if dropped or bumped on hard surface. Handle with care.
- g) Since CMOS LSI is used in this module, take care of static electricity and take the human earth into consideration when handling.
- h) Make sure the four mounting holes of the module are grounded sufficiently. Take electro-magnetic interference (EMI) into consideration.
- i) The module has some printed circuit boards (PCBs) on the back side. Take care to keep them from any stress or pressure when handling or installing the module; otherwise some of electronic parts on the PCBs may be damaged.
- j) Observe all other precautionary requirements in handling components.
- k) When some pressure is added onto the module from rear side constantly, it causes display non-uniformity issue, functional defect, etc. So, please avoid such design.
- l) When handling LCD modules and assembling them into cabinets, please be noted that long-term storage in the environment of oxidization or deoxidization gas and the use of such materials as reagent, solvent, adhesive, resin, etc. which generate these gasses, may cause corrosion and discoloration of the LCD modules.

12. Packing form

- a) Piling number of cartons : maximum 4 cartons
- b) Packing quantity in one carton : 5 modules
- c) Carton size : 363(W) × 277(D) × 440 (H) mm
- d) Total mass of one carton filled with full modules : 11.0Kg
- e) Packing form : Fig.3

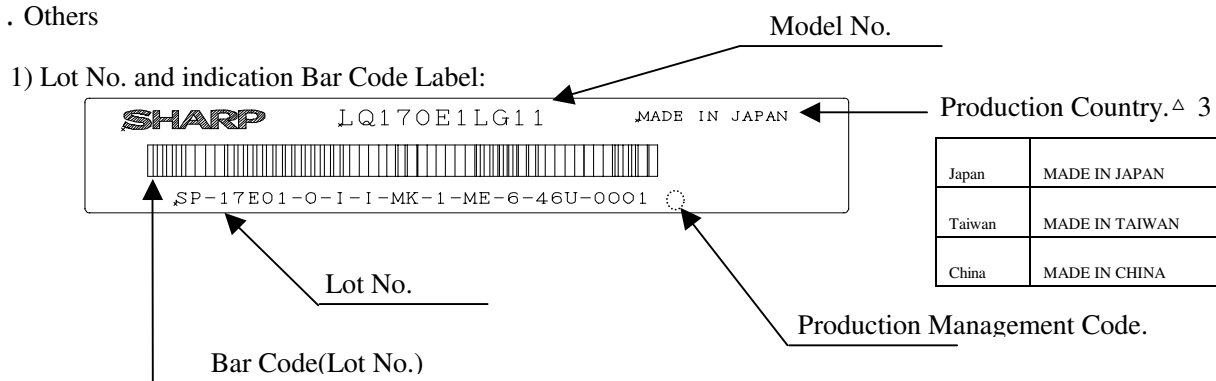
13 . Reliability test items

No	Test item	Conditions
1	High temperature storage test	Ta = 60°C 240h
2	Low temperature storage test	Ta = -25°C 240h
3	High temperature & high humidity operation test	Ta = 40°C ; 95%RH 240h (No condensation)
4	High temperature operation test	Ta = 50°C 240h (The panel temp. must be less than 60°C)
5	Low temperature operation test	Ta = 0°C 240H
6	Vibration test (non- operating)	Waveform : Sine wave Frequency : 10 ~ 57Hz/Vibration width (one side) : 0.075mm : 58 ~ 500Hz/Gravity : 9.8m/s ² Sweep time : 11minutes Test period : 3 hours (1 hour for each direction of X,Y,Z)
7	Shock test (non- operating)	Max. gravity : 490m/s ² Pulse width : 11ms, sine wave Direction : ± X, ± Y, ± Z, once for each direction.
8	Thermal shock test (Storage)	Ta=-25°C ~ 60°C ; 5 cycles Test period : 10 hours (1 hour for each temperature)
9	Altitude	Ta=50°C,70kPa,3,048m(10,000ft), t=24h (Operating) Ta=70°C,12kPa,15,240m(50,000ft), t=24h (Storage)

【Result Evaluation Criteria】

Under the display quality test conditions with normal operation state, these shall be no change which may affect practical display function.

14 . Others



2) Packing Label

社内品番 : (4 S) LQ170E1LG11	
Bar code (①)	
LotNO. :	(1 T) 2 0 0 4 . X . X X
Bar code (②)	
Quantity : (Q)	p c s
Bar code (③)	
ユーザ品番 :	
シャープ物流用ラベルです。	④

- ① Model No. (LQ170E1LG11)
- ② Lot No. (Date)
- ③ Quantity
- ④ Production Management No. Δ 3

3) Adjusting volume have been set optimally before shipment, so do not change any adjusted value.

If adjusted value is changed, the specification may not be satisfied.

4) Disassembling the module can cause permanent damage and should be strictly avoided.

5) Please be careful since image retention may occur when a fixed pattern is displayed for a long time.

6) The chemical compound which causes the destruction of ozone layer is not being used.

7) Cold cathode fluorescent lamp in LCD PANEL contains a small amount of mercury, Please follow local ordinances or regulations for disposal.

<p>COLD CATHODE FLUORESCENT LAMP IN LCD PANEL CONTAINS A SMALL AMOUNT OF MERCURY, PLEASE FOLLOW LOCAL ORDINANCES OR REGULATIONS FOR DISPOSAL 当該液晶ディスプレイパネルは蛍光管が組み込まれていますので、地方自治体の条例、または、規則に従って廃棄ください。</p>

8) This specification document's Japanese language version is also available. It's Number (SPEC. No.) is LD-16703B Δ 3

9) When any question or issue occurs, it shall be solved by mutual discussion.

15. Carton storage condition

Temperature 0°C to 40°C

Humidity 95%RH or less

Reference condition : 20°C to 35°C , 85%RH or less (summer)

: 5°C to 15°C , 85%RH or less (winter)

• the total storage time (40°C,95%RH) : 240H or less

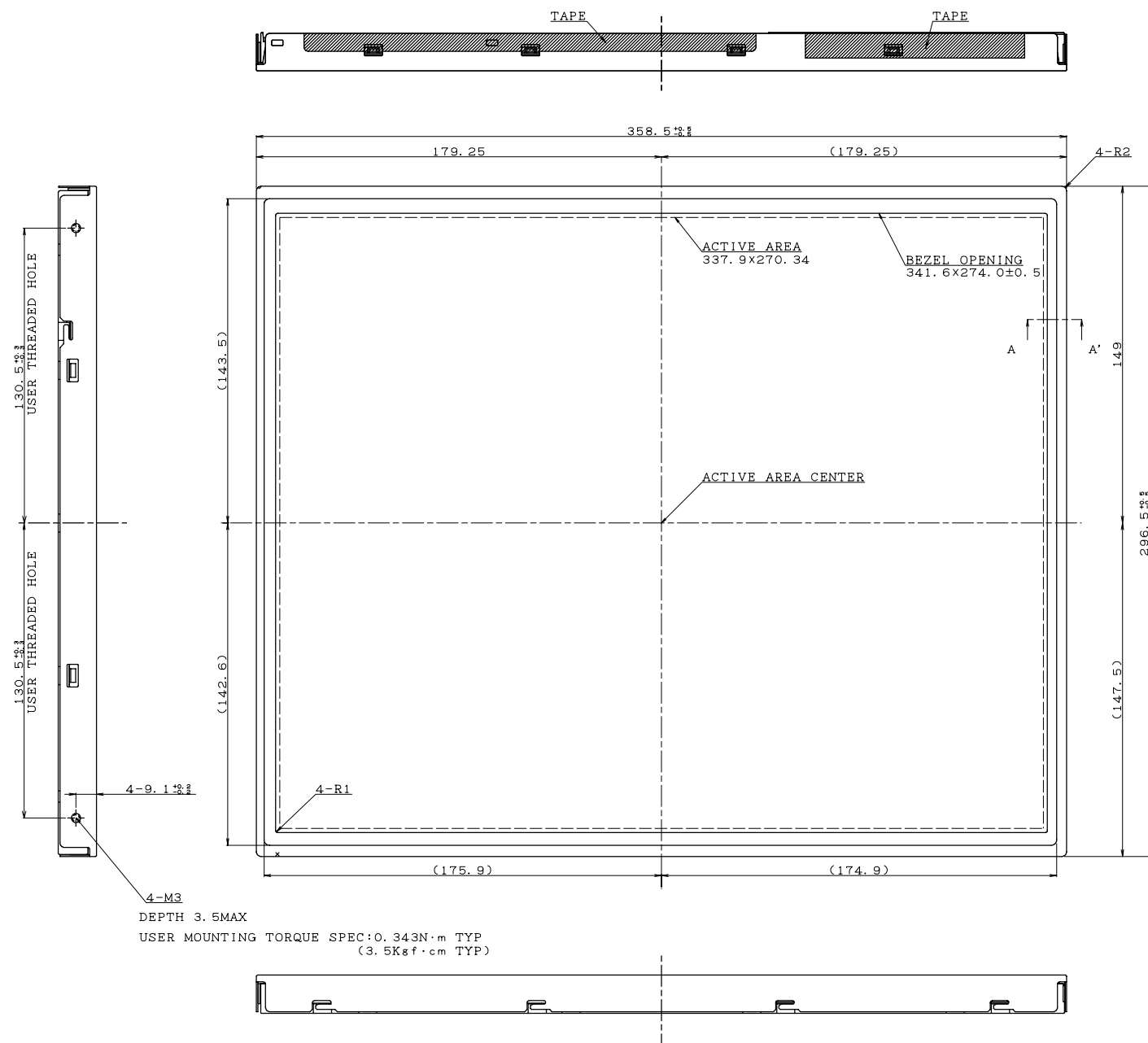
Sunlight Be sure to shelter a product from the direct sunlight.

Atmosphere Harmful gas, such as acid and alkali which bites electronic components and/or wires must not be detected.

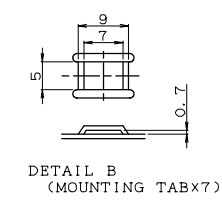
Notes Be sure to put cartons on palette or base, don't put it on floor, and store them with removing from wall

Please take care of ventilation in storehouse and around cartons, and control changing temperature is within limits of natural environment

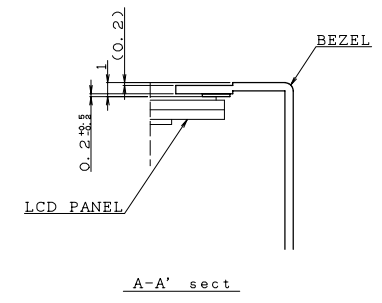
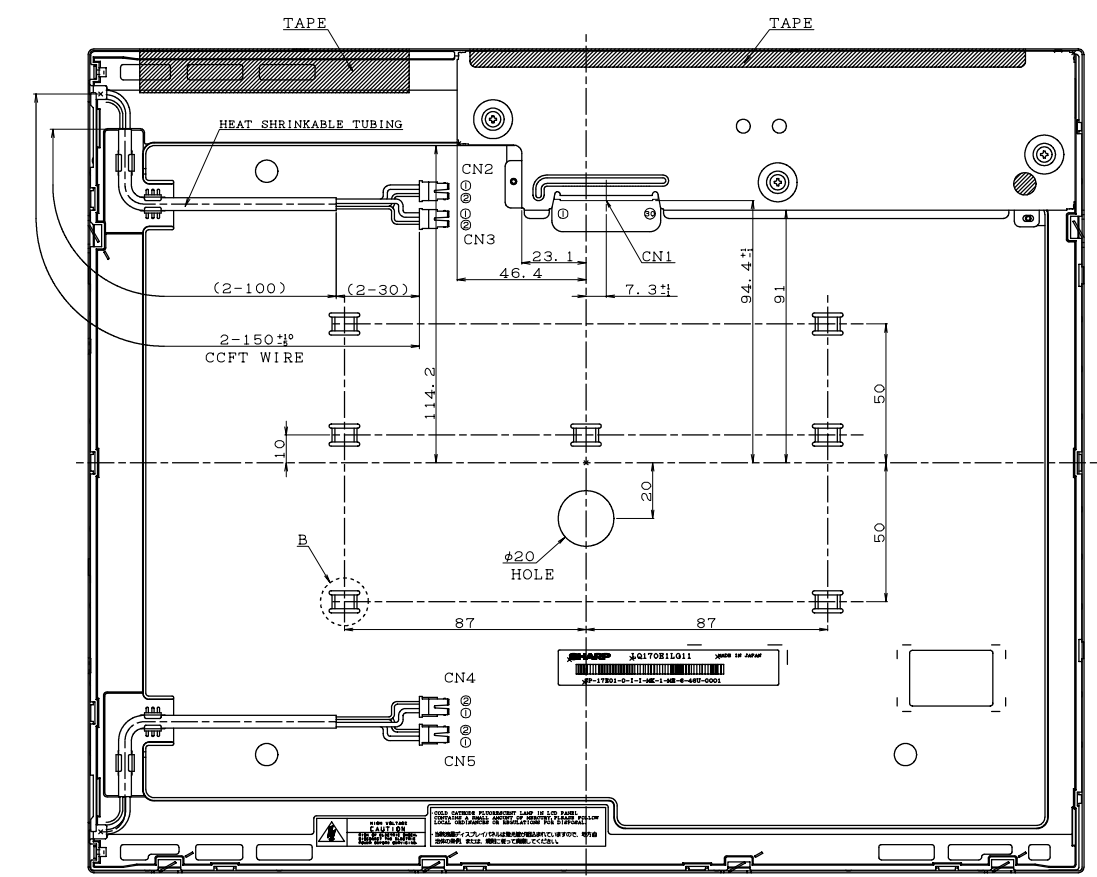
Storage period 1 year



4-M3
DEPTH 3.5MAX
USER MOUNTING TORQUE SPEC: 0.343N·m TYP
(3.5Kgf·cm TYP)



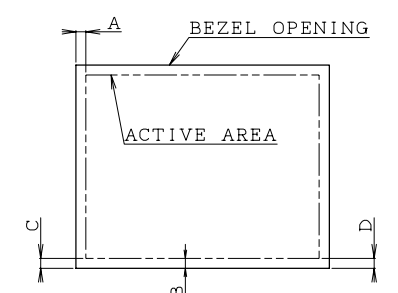
NOTE)
1. UNSPECIFIED TOLERANCE TO BE ± 0.5
2. WARP AND FLATING FOR PWB COVER AND CHASSIS ARE EXCLUDED FROM THICKNESS AND DIMENSION OF THE UNIT.



CN1: I/F CONNECTOR
MDF76KAW-30S-1H (HRS)
or
F1-XB30SL-HF10 (JAE)

CN2~CN5: CCFT CONNECTOR
BHSR-02VS-1 (JST)

1	HIGH
2	LOW



1) TOLERANCE X-DIRECTION A: 1.8 ± 0.8
2) TOLERANCE Y-DIRECTION B: 1.8 ± 0.8
3) OBLIQUITY OF DISPLAY AREA $|C-D| < 0.8$

LQ170E1LG11 OUTLINE DIMENSIONS

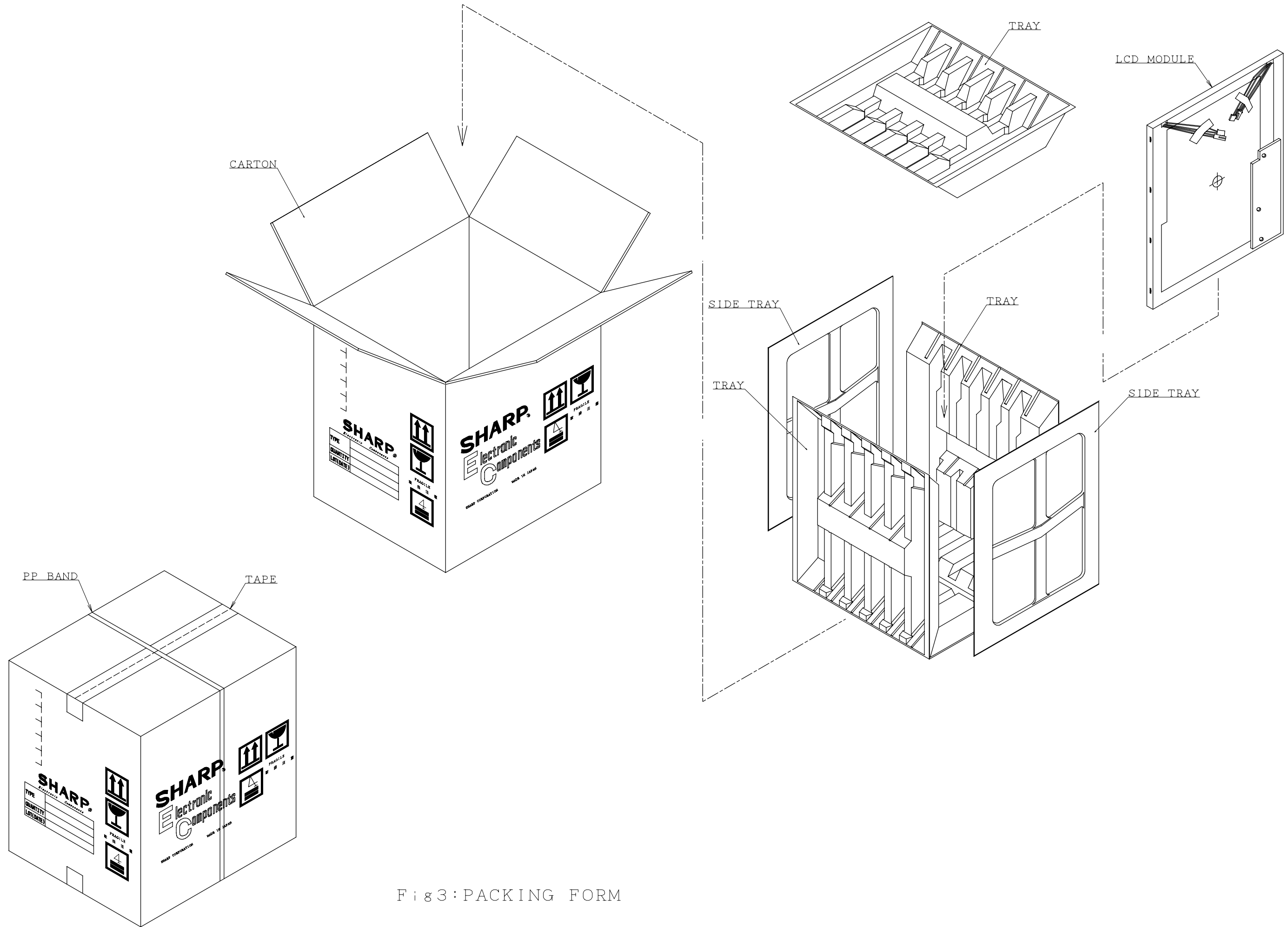


Fig 3: PACKING FORM