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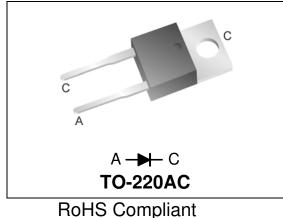


300 V, 16 A Q-Series Diode

Product Summary

I _{F(AVG)}	16	А
V _{RRM}	300	V
Q _{RR} (Typ at 125 °C)	44	nC
I _{RRM} (Typ at 125 °C)	2.65	А
Softness t _b /t _a (Typ at 125 °C)	0.7	

Pin Assignment



Package uses Lead-free plating and Green mold compound. Halogen free per IEC 61249-2-21.

Absolute Maximum Ratings

General Description

This device has the lowest Q_{RR} of any 300 V Silicon diode. Its recovery characteristics increase efficiency, reduce EMI and eliminate snubbers.

Applications

- AC/DC and DC/DC output rectification
 - Output and freewheeling diodes
- DC-AC Inverters

Features

- Low Q_{RR}, Low I_{RRM}, Low t_{RR}
- High dl_F/dt capable (1000A/μs)
- Soft recovery

Benefits

- Increases efficiency
 - Eliminates need for snubber circuits
 - Reduces EMI filter component size & count
- · Enables extremely fast switching

Absolute maximum ratings are the values beyond which the device may be damaged or have its useful life impaired. Functional operation under these conditions is not implied.

Symbol	Parameter	Conditions	Rating	Units
V _{RRM}	Peak repetitive reverse voltage		300	V
		T _J = 150 °C, T _C = 104 °C	16	Α
		60 Hz, 1/2 cycle	100	Α
I _{FSM}	Non-repetitive peak surge current	¹ / ₂ cycle of t=28 μs Sinusoid, T _C =25 °C	350	Α
T _{J(MAX)}	Maximum junction temperature		150	°C
T _{STG}	Storage temperature		-55 to 150	°C
	Lead soldering temperature	Leads at 1.6 mm from case, 10 sec	300	°C
PD	Power dissipation	T _C = 25 °C	65.8	W

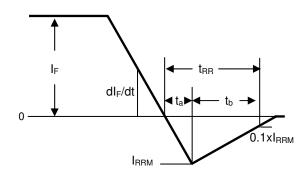
Thermal Resistance

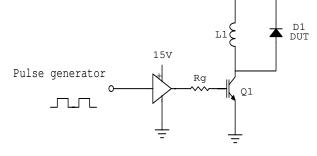
Symbol	Resistance from: Conditions		Rating	Units
$R_{\theta JA}$	Junction to ambient	TO-220	62	°C/W
$R_{\theta JC}$	Junction to case	TO-220	1.9	°C/W

Symbol	Parameter	Conditions		Min	Тур	Max	Units
DC Chara	acteristics						
I _R	Reverse current	V _R = 300V, T _J = 25 °C		-	-	25	μA
		V _R = 300V, T _J = 125 °C		-	0.45	-	mA
VF	Forward voltage	I _F = 16A, T _J = 25 °C		-	1.6	1.9	V
		I _F = 16A, T _J = 150 °C		-	1.4	-	V
CJ	Junction capacitance	V _R = 10V, 1 MHz		-	50	-	pF
Dynamic	Characteristics						
t _{RR}	Reverse recovery time	dI/dt =200A/µs V _R =200V,I _F =16A	TJ=25 °C	-	13	-	ns
			T _J =125 °C	-	25	-	ns
Q _{RR}	Reverse recovery charge		T _J =25 °C	-	11.5	15	nC
			T _J =125 °C	-	44	-	nC
I _{RRM}	Maximum reverse	dl/dt =200A/µs V _R =200V,I _F =16A	T _J =25 °C	-	1.5	1.85	А
	recovery current		T _J =125 °C	-	2.65	-	А
S	Le se th	dl/dt =200A/µs V _R =200V,I _F =16A	T _J =25 °C	-	0.7	-	
	Softness factor = $\frac{t_b}{t_a}$		T _J =125 °C	-	0.7	-	

Electrical Specifications at T_J= 25 °C (unless otherwise specified)

Note to component engineers: Q-Series diodes employ Schottky technologies in their design and construction. Therefore, Component Engineers should plan their test setups to be similar to those for traditional Schottky test setups. (For additional details, see Application Note AN-300.)





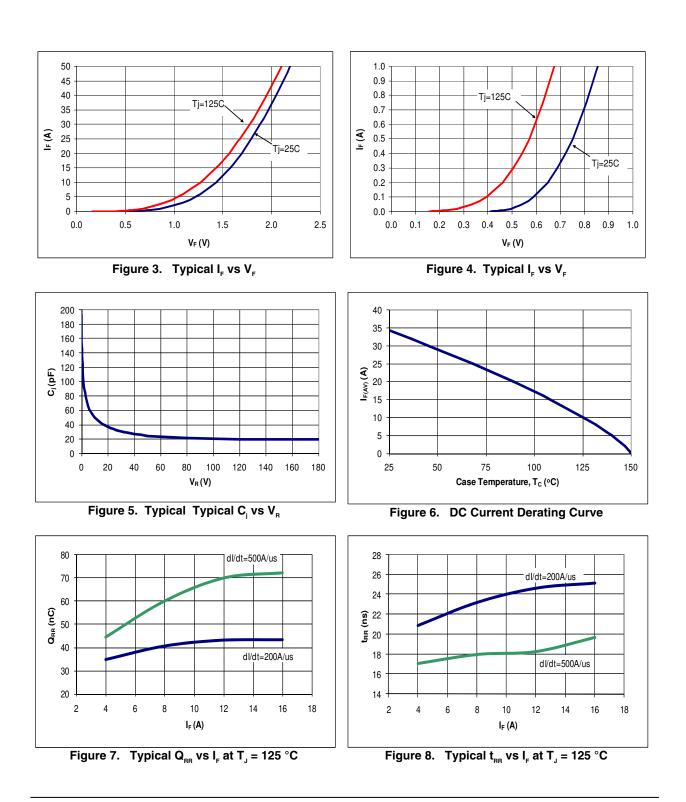
VR Q

Figure 1. Reverse Recovery Definitions









Electrical Specifications at T_J= 25 °C (unless otherwise specified)



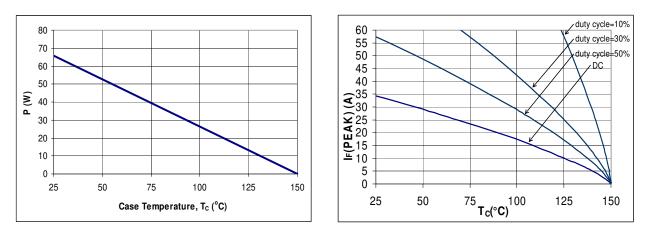


Figure 9. Power Derating Curve



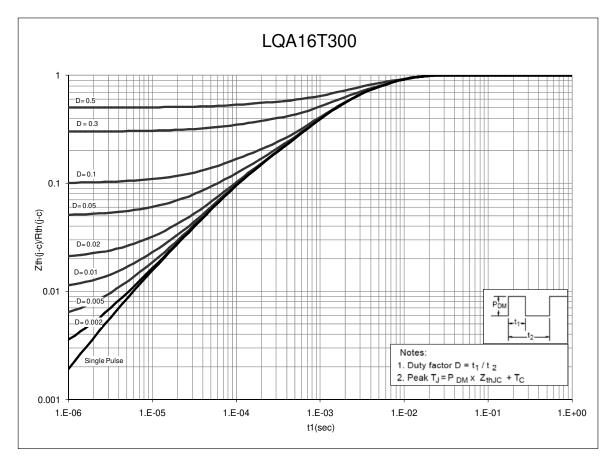
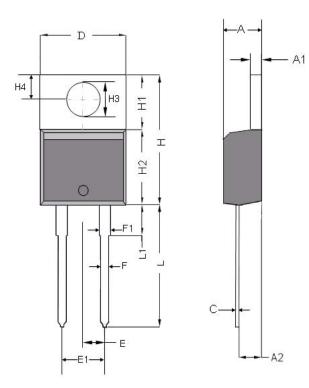


Figure 10. Normalized Maximum Transient Thermal Impedance





Dimensional Outline Drawings



	Millimeters MIN MAX		
Dim			
Α	4.32	4.70	
A1	1.14	1.40	
A2	2.03	2.79	
С	0.34	0.610	
D	9.65	10.67	
E	2.49	2.59	
E1	4.98	5.18	
F	0.508	1.016	
F1	1.14	1.78	
н	14.71	16.51	
H1	5.84	6.55	
H2	8.51	9.25	
H3	3.53	3.96	
H4	2.54	3.05	
L	12.70	14.22	
L1	-	6.35	

Mechanical Mounting Method	Maximum Torque / Pressure specification	
Screw through hole in package tab	1 Newton Meter (nm) or 8.8 inch-pounds (lb-in)	
Clamp against package body	12.3 kilogram-force per square centimeter (kgf/cm ²) or 175 lbf/in ²	

Soldering time and temperature: This product has been designed for use with high-temperature, lead-free solder. The component leads can be subjected to a maximum temperature of 300 °C, for up to 10 seconds. See Application Note AN-303, for more details.

Ordering Information

Part Number	Package	Packing
LQA16T300	TO-220AC	50 units/tube

The information contained in this document is subject to change without notice.





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Revision	Notes	Date
1.6	Released by Qspeed	05/09
1.7	Converted to Power Integrations Document	01/11



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Rev 1.7 01/11

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