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# SANYO Semiconductors DATA SHEET



#### Bi-CMOS IC For Portable Audio Equipment Monaural BTL Power Amplifier

#### **Overview**

LV4991M incorporates the power amplifier circuit operable at low voltage (2.7V or more) and has additionally the standby function to reduce the current drain. This is the best IC for speaker drive for the audio of low-power range system and home appliance. There are The LV4991TT: MSOP8 (150mil) and The LV4991TH: HMSOP8 (150mil) in the package change goods.

#### **Function and Feature**

- Monaural BTL power amplifier incorporated
  - Standard output power 1 = 450mW ( $V_{CC}$  = 3.6V,  $R_L$  = 8 $\Omega$ , THD = 10%) Standard output power 2 = 1000mW ( $V_{CC}$  = 5V,  $R_L$  = 8 $\Omega$ , THD = 10%) Output coupling capacitor not necessary because of differential output type
- Operation at low voltage possible
  - $V_{CC} = 2.7 V$  or more
- Standby function incorporated
- Standard current drain at standby =  $0.1\mu A (V_{CC} = 3.6V)$
- Second amplifier stop control function incorporated : Reducing the pop sound at startup, simple MUTE
- Overheat protection circuit incorporated
- Gain setting possible
  - BTL voltage gain = 0 to 26dB
- Output phase compensation capacitor not necessary

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#### Specifications

**Maximum Ratings** at  $Ta = 25^{\circ}C$ 

Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	V <sub>CC</sub> max		6	V
Allowable power dissipation	Pd max	Mounted on a specified board.*	900	mV
Operating temperature	Topr		-40 to +85	°C
Storage temperature	Tstg		-40 to +150	°C

\* Specified board: 40mm  $\times$  50mm  $\times$  1.6mm, glass epoxy (both side) board

#### **Operating Conditions** at $Ta = 25^{\circ}C$

Parameter	Symbol	Conditions	Ratings	Unit
Recommended supply voltage	V <sub>CC</sub>		3.6	V
Recommended load resistance	RL		8 to 32	Ω
Allowable operating supply voltage range	V <sub>CC</sub> op		2.7 to 5.5	V

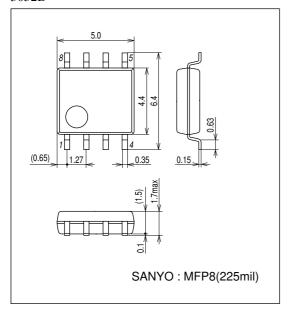
 $^{\ast}$  Determine the supply voltage to be used with due consideration of allowable power dissipation.

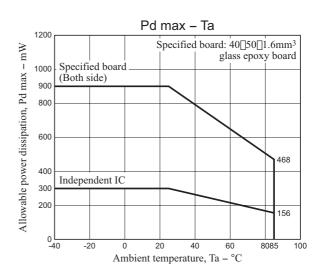
#### **Electrical Characteristics** Ta = 25°C, V<sub>CC</sub> = 3.6V, fin = 1kHz, R<sub>L</sub> = 8 $\Omega$

Devemeter	Symbol	Conditions		Ratings		Unit	
Parameter	Symbol	Conditions	min	typ max		Unit	
Quiescent current drain	ICCOP	No signal, $R_L = \infty$		3.6	6	mA	
Stand-by current drain	ISTBY	No signal, $R_L = \infty$ , $V2 = LOW$		0.1	10	μA	
Maximum output power	P <sub>O</sub> MX1	THD = 10%	300	450		mW	
	P <sub>O</sub> MX2	THD = 10%, V <sub>CC</sub> = 5V	665	1000		mW	
Voltage gain	VG	V <sub>IN</sub> = -30dBV	4.5	6	7.5	dB	
Voltage gain use range	V <sub>GR</sub>		0		26	dB	
Total harmonic distortion ratio	THD	V <sub>IN</sub> = -30dBV		0.3	1	%	
Output noise voltage	V <sub>N</sub> OUT	Rg = 620Ω, 20 to 20kHz		120	280	μVrms	
Ripple removal ratio	SVRR	$Rg = 620\Omega$ , fr = 100Hz, Vr = -20dBV		48		dB	
Output offset voltage	V <sub>O</sub> S	Rg = 620Ω	-50		50	mV	
Reference (pin 3) voltage	VREF			1.81		V	
Pin 2 control HIGH voltage	V <sub>STB</sub> H	Power amplifier operation mode	1.9		V <sub>CC</sub>	V	
Pin 2 control LOW voltage	V <sub>STB</sub> L	Power amplifier standby mode	0		0.3	V	
Pin 4 control HIGH voltage	V <sub>CNT</sub> H	Second power amplifier operation mode	1.6		V <sub>CC</sub>	V	
Pin 4 control LOW voltage	V <sub>CNT</sub> L	Second power amplifier standby mode	0		0.3	V	

#### Package Dimensions

unit : mm (typ) 3032D





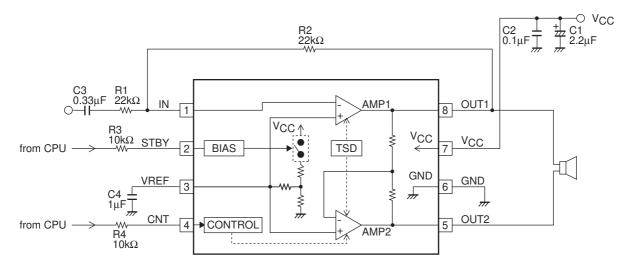
#### **Recommended Board**

Both sided board

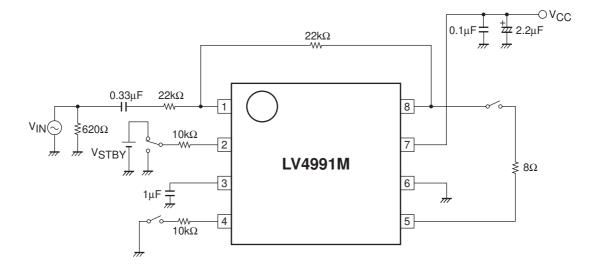
Size : 40mm×50mm×1.6mm  $\bigcirc$  $\bigcirc$ GND OUT2\_SE **O**UT 2 6 0  $\bigcirc$ 0 0 0 0 0 () 0 0 0  $\bigcirc$ 0 6 0 STBY GND  $\bigcirc$ OUT1\_SE GND SANYO  $\bigcirc$  $(\mathbf{O})$ 499XM\_series MEPS Top Layer **Bottom Layer** 

#### LV4991M

#### **Block Diagram and Sample Application Circuit**



#### **Test Circuit**



Pin Des	scription			
Pin No.	Symbol	Pin voltage	Description	Equivalent circuit
PIN NO.	Symbol	V <sub>CC</sub> = 3.6V	Description	Equivalent circuit
1	IN	1.81	Input pin	$V_{CC}$
2	STBY		Standby pin •Standby mode at 0 to 0.3V •Operation mode at 1.9 to VCC	$2 \xrightarrow{21k\Omega} \xrightarrow{VCC} \xrightarrow{VCC} \xrightarrow{VCC} \xrightarrow{VCC} \xrightarrow{VCC} \xrightarrow{VC} \xrightarrow$
3	VREF	1.81	Ripple filter pin (For connection of capacitor for filter)	CC V <sub>CC</sub> 450kΩ GND GND
4	CNT		Second amplifier stop control pin •Second amplifier stopped at 0 to 0.3V	
5 8	OUT2 OUT1	1.81	Power amplifier output pin	V <sub>CC</sub> 5 + 10kΩξ m m m
6	GND		Ground pin	
7	V <sub>CC</sub>		Power pin	

#### **Cautions for use**

1. Input coupling capacitor (C3)

The input coupling capacitor C3 and input resistor R1 make up the high-pass filter, attenuating the bass frequency. Therefore, the capacitance value must be selected with due consideration of the pass band. Note with care that this capacitance value affects the pop sound at startup.

Namely, the increased capacitance value will make the pop sound louder.

2. Pin 3 capacitor (C4)

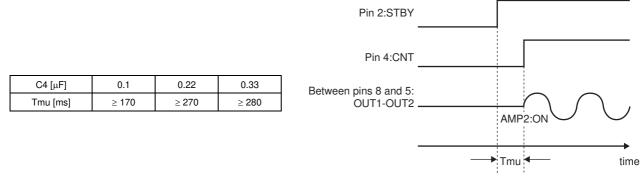
This capacitor C4 is designed to reduce the power ripple. The ripple removal ratio increases when the capacitance is larger. Note however that this capacitor affects the pop sound at startup. Design must therefore be made by taking into both features as above described.

3. Pin 4 control (second amplifier stop control function)

Pin 4 is a pin to turn ON/OFF the operation of second amplifier. By using this function, the pop sound at startup can be reduced. Note that pin 4 can be controlled by applying the voltage described below :

Second amplifier ON  $\Rightarrow$  V4 = 1.6 to V<sub>CC</sub>V or OPEN Second amplifier OFF  $\Rightarrow$  V4 = 0 to 0.3V

When the pin-3 capacitor C4 is downsized, the pop sound becomes louder. The pop sound can be reduced by providing the time Tmu to stop the second amplifier (see Fig. 1) while utilizing this function of the microcomputor. The recommended mute time Tmu is as follows.



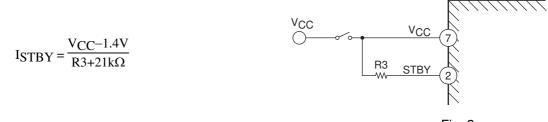


4. Standby pin (pin 2)

By controlling the standby pin, the mode changeover can be made between standby and operation modes.

Standby mode  $\Rightarrow$  V2 = 0 to 0.3V Operation mode  $\Rightarrow$  V2 = 1.9 to V<sub>CC</sub>V

When using the standby pin as interlocked with power supply as shown in Fig. 2, care should be taken because the current ISTBY as expressed by the following equation flows through the standby pin.



- 5. Bypass capacitor (C2) of the power supply block The bypass capacitor attached to the power pin (pin 7) must be arranged as near to this pin as possible.
- 6. Short-circuit between pins

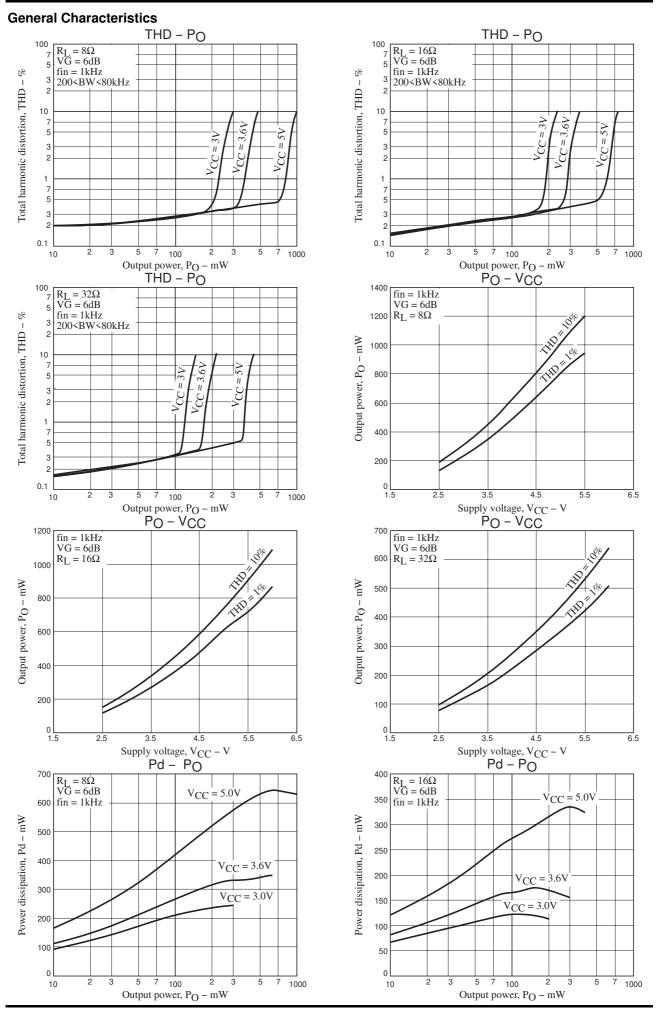
When power is applied with pins left short-circuited, deterioration or damage may result. Therefore, check before power application if pins are short-circuited with solder, etc. during mounting of IC to the substrate.

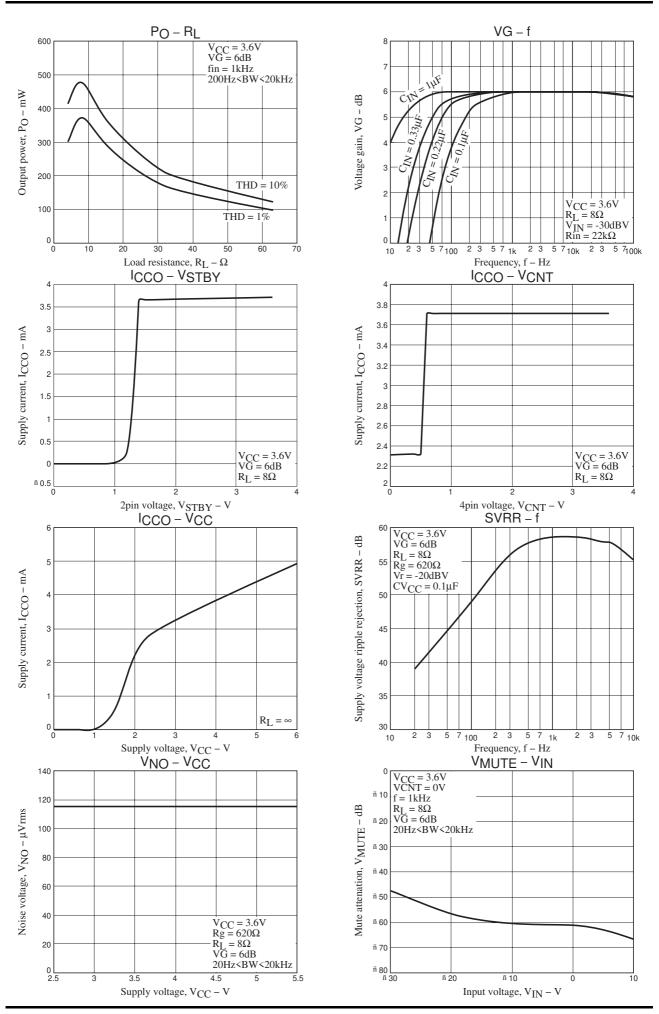
7. Short-circuit of load

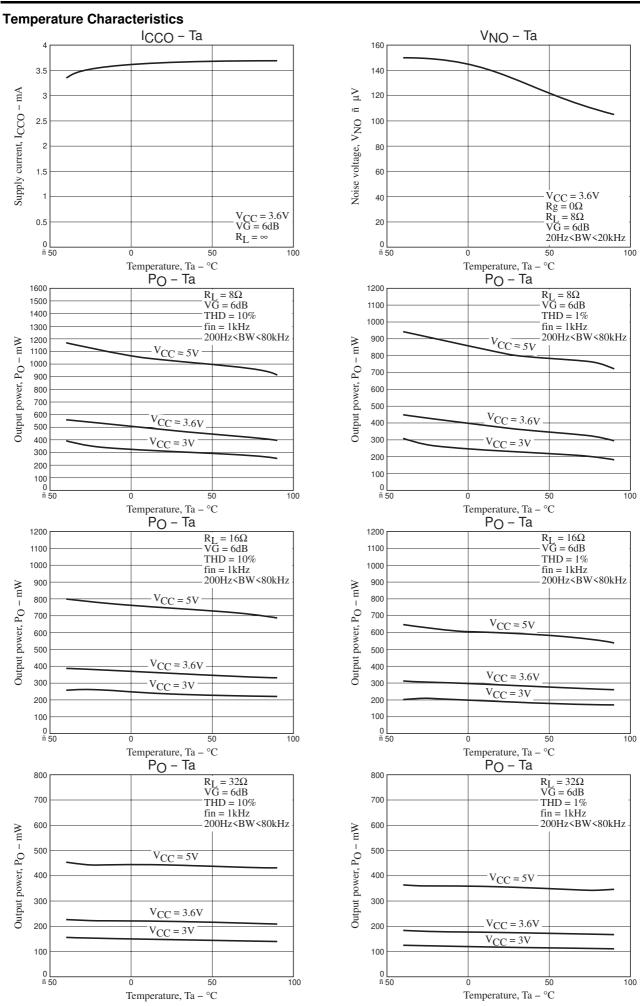
If the load is left short-circuited for a long period of time, deterioration or damage may occur. Never allow the load to short-circuit.

8. Maximum rating

When IC is used near the maximum rating, there is a possibility that the maximum rating may be exceeded even under the smallest change of conditions, resulting in failure. Take the sufficient margin for variation of supply voltage and use IC within a range where the maximum rating will never be exceeded.







#### LV4991M

#### Pop sound



2. Fall

		2pin : 5V/div, DC
STBY→	PWR	 OUT1-OUT2 : 10mV/div, AC
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		3pin : 200mV/div, DC

	2pin : 5V/div, DC
PWR→STBY	OUT1-OUT2 : 10mV/div, AC
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	200ms/div
	3pin : 200mV/div, DC

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