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# InGaP HBT 2.4 - 2.5 GHz Power Amplifier

### **PRODUCTION DATA SHEET**

## **DESCRIPTION**

The LX5514M is a power amplifier optimized for applications in The PA frequency range. implemented as a two-stage monolithic power control in a typical wireless microwave integrated circuit (MMIC) system. on-chip input active bias, matching, and output pre-matching.

InGaP/GaAs Heterojunction Bipolar compact footprint, low profile, and Transistor (HBT) IC (MOCVD). It operates with a single LX5514M an ideal solution for positive voltage supply of 3.3V, and 802.11b/g/n applications. provides power gain of 27dB and output powers of 19dBm at 3.3V for 3% EVM in the 2.4-2.5GHz.

LX5514M also features an on-chip WLAN(802.11b/g/n) power detector at the output port of the 2.4-2.5 GHz the PA to help reduce BOM cost and is PCB space for implementation of

The LX5514M is available in a 6pin 1.5mm x 1.5mm dual flat no lead The device is manufactured with an package (DFN 1.5×1.5mm<sup>2</sup>-6L). The process excellent thermal capability make the

### **KEY FEATURES**

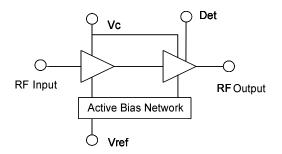
- Advanced InGaP HBT
- 2.4-2.5GHz Operation
- Single-Polarity 3.3V Supply
- Quiescent Current ~ 84mA
- Power Gain ~ 27dB
- 19dBm @3% EVM/3.3V
- Total Ic ~ 130mA @19dBm/3.3V
- Complete On-Chip Input Match
- Simple Output Match
- Small Footprint: 1.5x1.5mm<sup>2</sup>
- Low Profile: 0.4mm

### **APPLICATIONS**

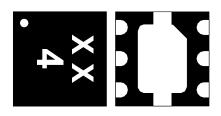
802.11b/g/n

IMPORTANT: For the most current data, consult MICROSEMI's website: http://www.microsemi.com

### **BLOCK DIAGRAM**



#### 1.5X1.5MM MLP PACKAGE PACKAGE ORDER INFO



Note: XX is a date code.



RoHS Compliant / Pb-free

LX5514MLL

Note: Available in Tape & Reel. Append the letters "TR" to the part number. (i.e. LX5514MLL-TR)



# InGaP HBT 2.4 – 2.5 GHz Power Amplifier

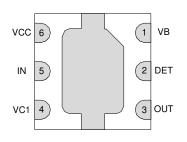
## **PRODUCTION DATA SHEET**

# ABSOLUTE MAXIMUM RATINGS

5 V
500 mA
2 W
+10 dBm
+150°C
40 to +85°C
65 to +150°C
+260°C (+0,-5)

Note: Exceeding these ratings could cause damage to the device. All voltages are with respect to Ground. Currents are positive into, negative out of specified terminal.

### PACKAGE PIN OUT



LQ PACKAGE (Bottom View)

RoHS / Pb-free NiPdAu Lead Finish

# THERMAL DATA

LQ Plastic QFN 3×3 16-Pin

THERMAL RESISTANCE-JUNCTION TO CASE, $\theta_{\text{JC}}$	8.4 °C/W
THERMAL RESISTANCE-JUNCTION TO $f A$ MBIENT, $f  heta_{JA}$	86.1 °C/W

Junction Temperature Calculation :  $T_J = T_A + (P_D \times \theta_{JA})$ .

The  $\theta_{JA}$  numbers are guidelines for the thermal performance of the device/pc-board system. The 4 layers PCB is constructed based on JESD 51-7 specification and via based on JESD 51-5. All of the above assume no ambient airflow.

FUNCTIONAL PIN DESCRIPTION			
Name	Pin	Description	
RF IN	5	RF input into the power amplifier. This pin is RF-matched to 50 Ohm, and shorted to ground at DC.	
VB	1	Bias current control voltage for the first and second stage.	
VCC	6	Supply voltage for the bias reference and control circuits.	
RF OUT	3	RF output and power supply for the second stage amplifier.	
VC1	4	Power supply for the first stage amplifier.	
DET	2	DETECTOR output.	