

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



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PRODUCTION DATA SHEET

DESCRIPTION

The LX5561 is a low noise amplifier HEMT (E-pHEMT) process.

voltage supply of 3.3V, with noise resistor-network bias circuit. figure of 1.5dB while maintaining of up to +6.5dBm.

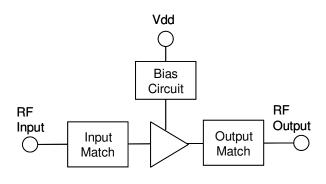
The LNA is implemented with bias (LNA) for WLAN applications in the circuit and input/output matching circuit 2.4-2.5 GHz frequency range. This on chip, resulting in simple external LNA is manufactured with an InGaAs circuit on board. In addition, the on-Enhancement mode pseudomorphic chip bias circuit provides stable performance of gain, NF and current for It operates with a single positive voltage variation compared to a general

The LX5561 is available in a 12-pin input third order intercept point(IIP3) 2mm x 2mm micro-lead package (MLPQ-12L).

KEY FEATURES

- 0.5μm InGaAs E-mode pHEMT
- 2.4 2.5GHz Operation
- Single 3.3V Supply
- Gain ~ 13.0dB
- Noise Figure ~ 1.5dB
- Input IP3 ~ +6.5dBm
- Input P1dB ~ +2.5dBm
- On-Chip Bias Circuit
- On-Chip Input/Output Match
- 2mm x 2mm MLPQ-12L
- Low Profile 0.5mm

BLOCK DIAGRAM



IMPORTANT: For the most current data, consult MICROSEMI's website: http://www.microsemi.com

APPLICATIONS

- Wireless LAN 802.11b/g
- WiMax

PRODUCT HIGHLIGHT



PACKAGE ORDER INFO

Plastic MLPQ 12 pin

RoHS Compliant / Pb-free

LX5561LL

Note: Available in Tape & Reel. Append the letters "TR" to the part number. (i.e. LX5561LL-TR)



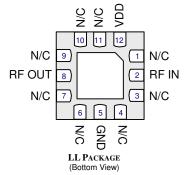
PRODUCTION DATA SHEET

ABSOLUTE MAXIMUM RATINGS

DC Supply Voltage, RF Off	4 V
Drain Current	
Total Power Dissipation	0.15 W
RF Input Power	
Operation Ambient Temperature	
Storage Temperature Range	65°C to 150°C
Package Peak Temp. for Solder Reflow (40 seconds maximum exposu	
	,

Note: Exceeding these ratings could cause damage to the device. All voltages are with respect to Ground. Currents are positive into, negative out of specified terminal.

PACKAGE PIN OUT



RoHS / Pb-free NiPdAu Finish

•	FUNCTIONAL PIN DESCRIPTION				
	Name	Pin #	Description		
	RF IN	2	RF input for the low noise amplifier. This pin is AC-coupled and does not require a DC-blocking capacitor.		
F	RF OUT	8	RF output for the low noise amplifier. This pin is AC-coupled and does not require a DC-blocking capacitor.		
	VDD	12	Supply voltage.		
	GND	5	Ground.		
	N/C	1,3,4,6,7,9, 10,11, Center Metal	Not Connected. They can be treated either as open pins or connected to ground.		



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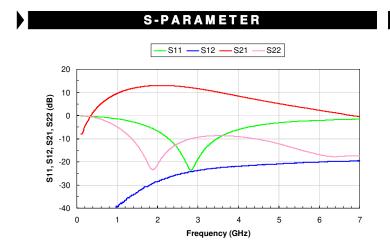
ELECTRICAL CHARACTERISTICS

Test conditions: $V_{DD} = 3.3V$, $I_{DD} = 10.5$ mA, $T_A = +25$ °C (Room Temperature)

Parameter	Symbol	Test Conditions	LX5561			Units
Faranietei	Symbol	Joi Test Conditions		Тур	Max	Ullits
Application Frequency Range	f		2.4		2.5	GHz
Small-Signal Gain	S21			13.0		dB
Noise Figure	NF			1.5	1.8	dB
Input 3 rd Order Intercept Point	IIP3	Freq. 1 = 2.412GHz, Freq. 2 = 2.432GHz		6.5		dBm
Input P1dB	IP1dB	Freq. = 2.45GHz		2.5		dBm
Input Return Loss	S11			12		dB
Output Return Loss	S22			12		dB
Supply Voltage	V_{DD}			3.3		V
Supply Current	I _{DD}			10.5		mA

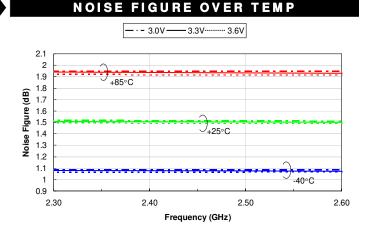


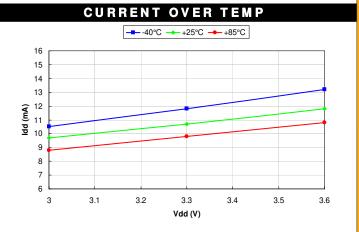
PRODUCTION DATA SHEET

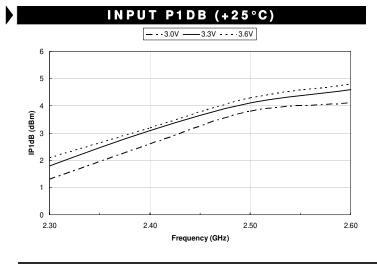


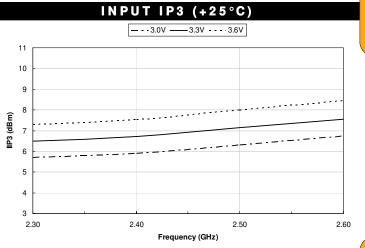
Typical S-Parameter Data at Room Temperature (Vdd = 3.3V, Idd = 10.5mA at Room Temperature)

GAIN OVER TEMP -·- 3.0V 3.3V ·· ..3.6V 15 14 -40°C S21 (dB) 12 11 10 2.0 2.1 2.2 2.3 2.5 2.6 2.8 2.9 3.0 Frequency (GHz)





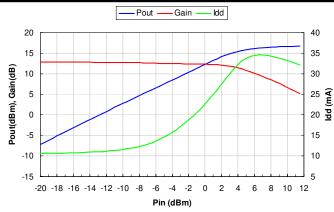






PRODUCTION DATA SHEET

POWER SWEEP @ 2.45GHZ

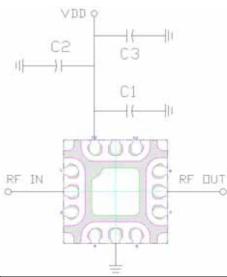


(Vdd=3.3V, Idq=10.5mA at Room Temperature)



PRODUCTION DATA SHEET

APPLICATION SCHEMATIC



BOM LIST				
Reference Designator	Part Description	Case		
C1	Capacitor, 1 nF	0402		
C2	Capacitor,1 μF	0603		
C3	Capacitor,10 μF	0805		

Notes

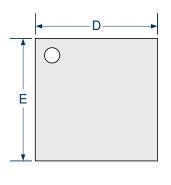
- It is recommended to place C1 at 20-50mil from MLP package outline.
- C2 and C3 are used for standalone evaluation board test only. They are not needed in final applications.

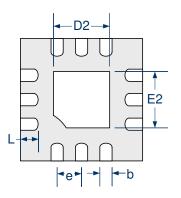


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PACKAGE DIMENSIONS

12-Pin MLPQ Plastic (2x2mm)



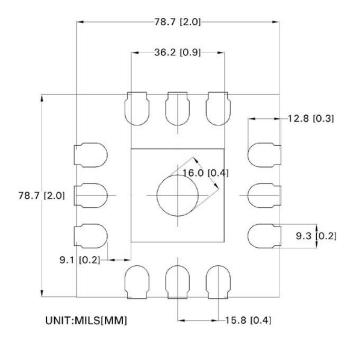




	MILLIMETERS		INCHES		
Dim	MIN	MAX	MIN	MAX	
Α	0.40	0.50	0.016	0.020	
A1	0.00	0.05	0.000	0.002	
A3	0.15 REF		0.006 REF		
b	0.15	0.25	0.006	0.010	
D	2.00 BSC		0.079 BSC		
D2	0.77	1.02	0.030	0.040	
Е	2.00 BSC		0.079 BSC		
E2	0.77	1.02	0.030	0.040	
е	0.40 BSC		0.016 BSC		
L	0.19	0.39	0.007	0.015	

Note:

 Dimensions do not include mold flash or protrusions; these shall not exceed 0.155mm(.006") on any side. Lead dimension shall not include solder coverage.



Recommended Land Pattern



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NOTES

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