# imall

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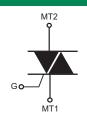
#### **LX8 Series** RoHS



#### **Main Features**

Symbol	Value	Unit
I <sub>T(RMS)</sub>	0.8	А
	400 to 600	V
I <sub>GT</sub>	3 to 5	mA

#### **Schematic Symbol**



#### Description

New 0.8 Amp bi-directional solid state switch series offering direct interface to microprocessor drivers in economical TO-92 and surface mount packages. The die voltage blocking junctions are glass-passivated to ensure long term reliability and parametric stability.

#### Features & Benefits

- RoHS compliant
- Blocking voltage (V<sub>DRM</sub>) capability — up to 600V
- Surae capability > 9.5Amps
- Static dv/dt > 10 Volts/ µsec
- Thru hole and surface mount packages

#### Applications

The LX8 EV Series is especially designed for low current applications such as heating controls in hair care products, as well as replacement of mechanical switch contacts where long life is required.

Symbol	Parameter	Parameter					
1	DMC on state surrent (full sine ways)	TO-92	$T_c = 50^{\circ}C$	0.8A	А		
I <sub>T(RMS)</sub>	RMS on-state current (full sine wave)	SOT-223	$T_{L} = 90^{\circ}C$	0.0A	A		
1	Non repetitive surge peak on-state current	TO-92	F = 50 Hz	8.0	А		
TSM	(Single cycle, $T_J$ initial = 25°C)	SOT-223	F = 60 Hz	9.5	A		
l²t	12t Value for fueing	t <sub>p</sub> = 10 ms	F = 50 Hz	0.32	A <sup>2</sup> s		
	I <sup>2</sup> t Value for fusing	t <sub>p</sub> = 8.3 ms	F = 60 Hz	0.37	AS		
di/dt	Critical rate of rise of on-state current $I_{c} = 2 \times I_{cT}$	TO-92	T <sub>1</sub> = 110°C	20	A/µs		
uyut	Circle in the of the of on state current $I_G = 2 \times I_{GT}$	SOT-223	1 <sub>J</sub> = 110 C				
I <sub>GTM</sub>	Peak gate current	1	А				
P <sub>G(AV)</sub>	Average gate power dissipation $T_J = 110^{\circ}C$			0.1	W		
T <sub>stg</sub>	Storage junction tempera	-40 to 150	°C				
	Operating junction temper	-40 to 110	°C				



Symbol	Description	Test	Quadrant	Limit	Va	lue	Unit
Symbol	Description	Conditions	Quadrant		LX803xy	LX807xy	Onic
I <sub>gt</sub>	DC Gate Trigger Current	$V_{\rm D} = 12V$	–    –      V	MAX.	3 5	5 7	mA
V <sub>gt</sub>	DC Gate Trigger Voltage	R_ = 60 Ω	ALL	MAX.	1.3	1.3	V
I <sub>H</sub>	Holding Current	Gate Open		MAX.	5	5	mA
d∨/dt	Critical Rate-of-Rise of Off-State Voltage	T <sub>J</sub> = 110°C V <sub>D</sub> = V <sub>DRM</sub> Exponential Waveform Gate Open		MIN.	10	10	V/µs
dv/dt)c	Critical Rate-of-Rise of Commutating Voltage	(di/dt)c = 0.43A/ms T <sub>J</sub> = 110°C		MIN.	1.5	1.5	V/µs
gt	Turn-On Time	PW	25mA = 15µs .2A (pk)	MAX.	2.0	2.0	μs

NOTE: x = voltage, y = package

Static Characteristics (T <sub>j</sub> = 25°C, unless otherwise specified)							
Symbol	Description	Test Conditions	Limit	Value	Unit		
V <sub>TM</sub>	Peak On-State Voltage	I <sub>TM</sub> = 1.13A (pk)	MAX	1.60	V		
1	Off State Current Deals Departitive	$V_{\rm D} = V_{\rm DRM} T_{\rm J} = 25^{\circ} {\rm C}$	MAX	5	μΑ		
I <sub>DRM</sub> Off-State Current,	Off-State Current, Peak Repetitive	$V_{\rm D} = V_{\rm DRM} T_{\rm J} = 110^{\circ} \rm C$	MAX	100	μA		

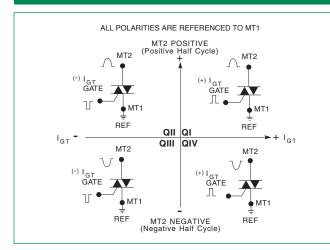
#### **Thermal Resistances**

Symbol	Description	Test Conditions		Value	Unit
D			TO-92	60	20044
R <sub>th(j-c)</sub>	Junction to case (AC)	$I_{T} = 0.8A_{(RMS)}^{1}$	SOT-223	25	°C/W
	lupation to appliant		TO-92	150	°CAN/
$R_{th(j-a)}$	Junction to ambient	$I_{T} = 0.8A_{(RMS)}^{1}$	SOT-223	60	°C/W

<sup>1</sup> 60Hz AC resistive load condition, 100% conduction.



#### **Figure 1: Definition of Quadrants**





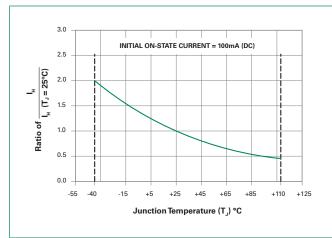
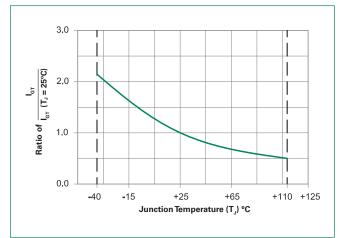
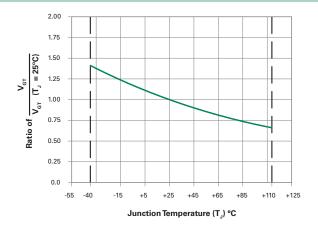


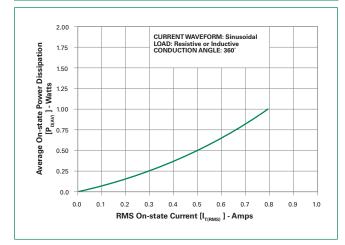
Figure 2: Normalized DC Gate Trigger Current for All Quadrants vs. Junction Temperature



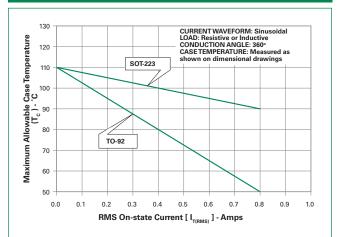
#### Figure 4: Normalized DC Gate Trigger Voltage for All Quadrants vs. Junction Temperature



#### Figure 5: Power Dissipation (Typical) vs. RMS On-State Current



#### Figure 6: Maximum Allowable Case Temperature vs. On-State Current

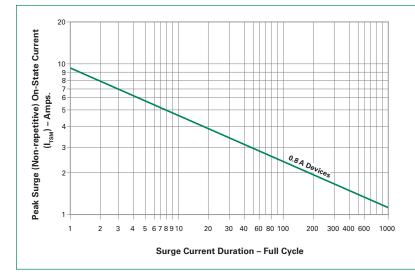


**EV 0.8A TRIACs** 

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#### Figure 7: Surge Peak On-State Current vs. Number of Cycles



Supply Frequency: 60Hz Sinusoidal Load: Resistive RMS On-State Current [I<sub>TRMS</sub>]: Max Rated Value at Specific Case Temperature

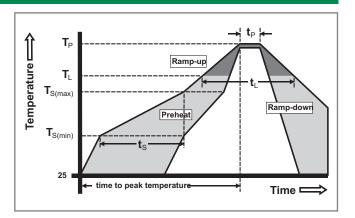
Notes:

1. Gate control may be lost during and immediately following surge current interval.

Overload may not be repeated until junction temperature has returned to steady-state rated value.

#### **Soldering Parameters**

Reflow Co	ndition	Pb – Free assembly
	-Temperature Min (T <sub>s(min)</sub> )	150°C
Pre Heat	-Temperature Max (T <sub>s(max)</sub> )	200°C
	-Time (min to max) (t <sub>s</sub> )	60 – 180 secs
Average ra (T <sub>L</sub> ) to pea	amp up rate (LiquidusTemp) k	5°C/second max
T <sub>S(max)</sub> to T <sub>L</sub> - Ramp-up Rate		5°C/second max
Reflow	-Temperature (T <sub>L</sub> ) (Liquidus)	217°C
nellow	-Time (min to max) (t <sub>s</sub> )	60 – 150 seconds
PeakTemp	erature (T <sub>P</sub> )	260 <sup>+0/-5</sup> °C
Time within 5°C of actual peak Temperature (t <sub>p</sub> )		20 – 40 seconds
Ramp-dow	vn Rate	5°C/second max
Time 25°C	to peakTemperature (T <sub>P</sub> )	8 minutes Max.
Do not exc	ceed	280°C





**Physical Specifications** 

Terminal Finish	100% Matte Tin-plated.
Body Material	UL recognized epoxy meeting flammability classification 94V-0.
Lead Material	Copper Alloy

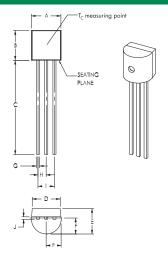
#### **Design Considerations**

Careful selection of the correct device for the application's operating parameters and environment will go a long way toward extending the operating life of the Thyristor. Good design practice should limit the maximum continuous current through the main terminals to 75% of the device rating. Other ways to ensure long life for a power discrete semiconductor are proper heat sinking and selection of voltage ratings for worst case conditions. Overheating, overvoltage (including dv/dt), and surge currents are the main killers of semiconductors. Correct mounting, soldering, and forming of the leads also help protect against component damage.

#### Reliability/Environmental Tests

Test	Specifications and Conditions
AC Blocking	MIL-STD-750, M-1040, Cond A Applied Peak AC voltage @ 110°C for 1008 hours
Temperature Cycling	MIL-STD-750, M-1051, 100 cycles; -40°C to +150°C; 15-min dwell-time
Temperature/ Humidity	EIA / JEDEC, JESD22-A101 1008 hours; 320V - DC: 85°C; 85% rel humidity
High Temp Storage	MIL-STD-750, M-1031, 1008 hours; 150°C
Low-Temp Storage	1008 hours; -40°C
Thermal Shock	MIL-STD-750, M-1056 10 cycles; 0°C to 100°C; 5-min dwell- time at each temperature; 10 sec (max) transfer time between temperature
Autoclave	EIA / JEDEC, JESD22-A102 168 hours (121°C at 2 ATMs) and 100% R/H
Resistance to Solder Heat	MIL-STD-750 Method 2031
Solderability	ANSI/J-STD-002, category 3, Test A
Lead Bend	MIL-STD-750, M-2036 Cond E

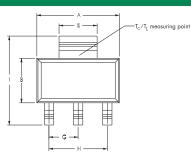
#### Dimensions – TO-92 (E Package)



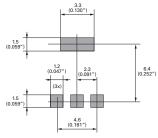
Dimensions		Inches			Millimeters		
Dimensions	Min	Тур	Max	Min	Тур	Max	
А	0.175	_	0.205	4.450	—	5.200	
В	0.170	_	0.210	4.320	—	5.330	
С	0.500	_	—	12.700	—	—	
D	0.135	0.165	—	3.430	4.190	—	
E	0.125	_	0.165	3.180	—	4.190	
F	0.080	0.095	0.105	2.040	2.400	2.660	
G	0.016	_	0.021	0.407	—	0.533	
Н	0.045	0.050	0.055	1.150	1.270	1.390	
I	0.095	0.100	0.105	2.420	2.540	2.660	
J	0.015	_	0.020	0.380	_	0.500	



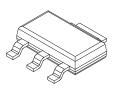
#### **Dimensions – SOT-223**

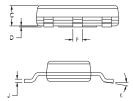


Pad Layout for SOT-223



Dimensions in Millimeters (Inches)





Dimensions	Inches			Millimeters		
Dimensions	Min	Тур	Max	Min	Тур	Max
А	0.248	0.256	0.264	6.30	6.50	6.70
В	0.130	0.138	0.146	3.30	3.50	3.70
С	—	—	0.071	—	—	1.80
D	0.001	—	0.004	0.02	—	0.10
E	0.114	0.118	0.124	2.90	3.00	3.15
F	0.024	0.027	0.034	0.60	0.70	0.85
G	—	0.090	—	—	2.30	—
Н	—	0.181	—	—	4.60	—
I	0.264	0.276	0.287	6.70	7.00	7.30
J	0.009	0.010	0.014	0.24	0.26	0.35
К	10° MAX					

#### **Product Selector**

Part Number	Voltago	Gate Sensitiv	Poekogo	
	Voltage	I – II – III	IV	Package
LX803DE	400 V	3 mA	5 mA	TO-92
LX803ME	600 V	3 mA	5 mA	TO-92
LX803DT	400 V	3 mA	5 mA	SOT-223
LX803MT	600 V	3 mA	5 mA	SOT-223
LX807DE	400 V	5 mA	7 mA	TO-92
LX807ME	600 V	5 mA	7 mA	TO-92
LX807DT	400 V	5 mA	7 mA	SOT-223
LX807MT	600 V	5 mA	7 mA	SOT-223

#### **Packing Options**

Part Number	Marking	Weight	Packing Mode	Base Quantity
LX8xxyE	LX8xxyE	0.170 g	Bulk	2500
LX8xxyEAP	LX8xxyE	0.170 g	Ammo Pack	2000
LX8xxyERP	LX8xxyE	0.170 g	Tape & Reel	2000
LX8xxyTRP	LX8xxyT	0.120 g	Tape & Reel	1000

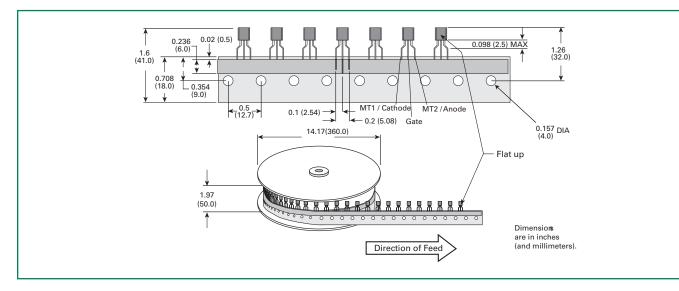
Note: xx = gate sensitivity, y = voltage



EV Series 0.8 Amp Sensitive Triacs

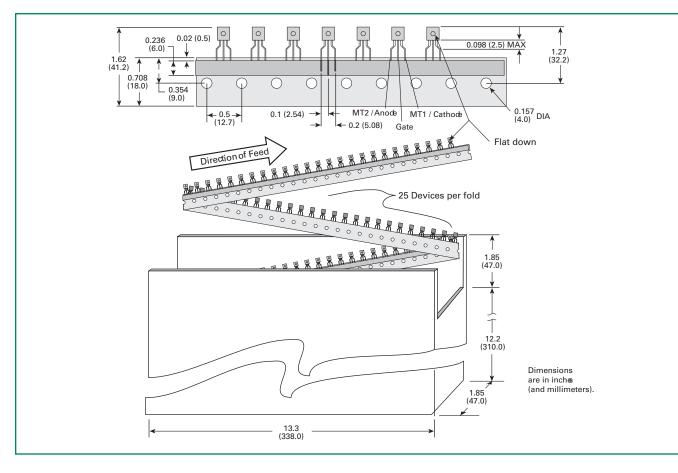
#### TO-92 (3-lead) Reel Pack (RP) Radial Leaded Specifications

#### Meets all EIA-468-B 1994 Standards



#### TO-92 (3-lead) Ammo Pack (AP) Radial Leaded Specifications

#### Meets all EIA-468-B 1994 Standards





#### SOT-223 Reel Pack (RP) Specifications

