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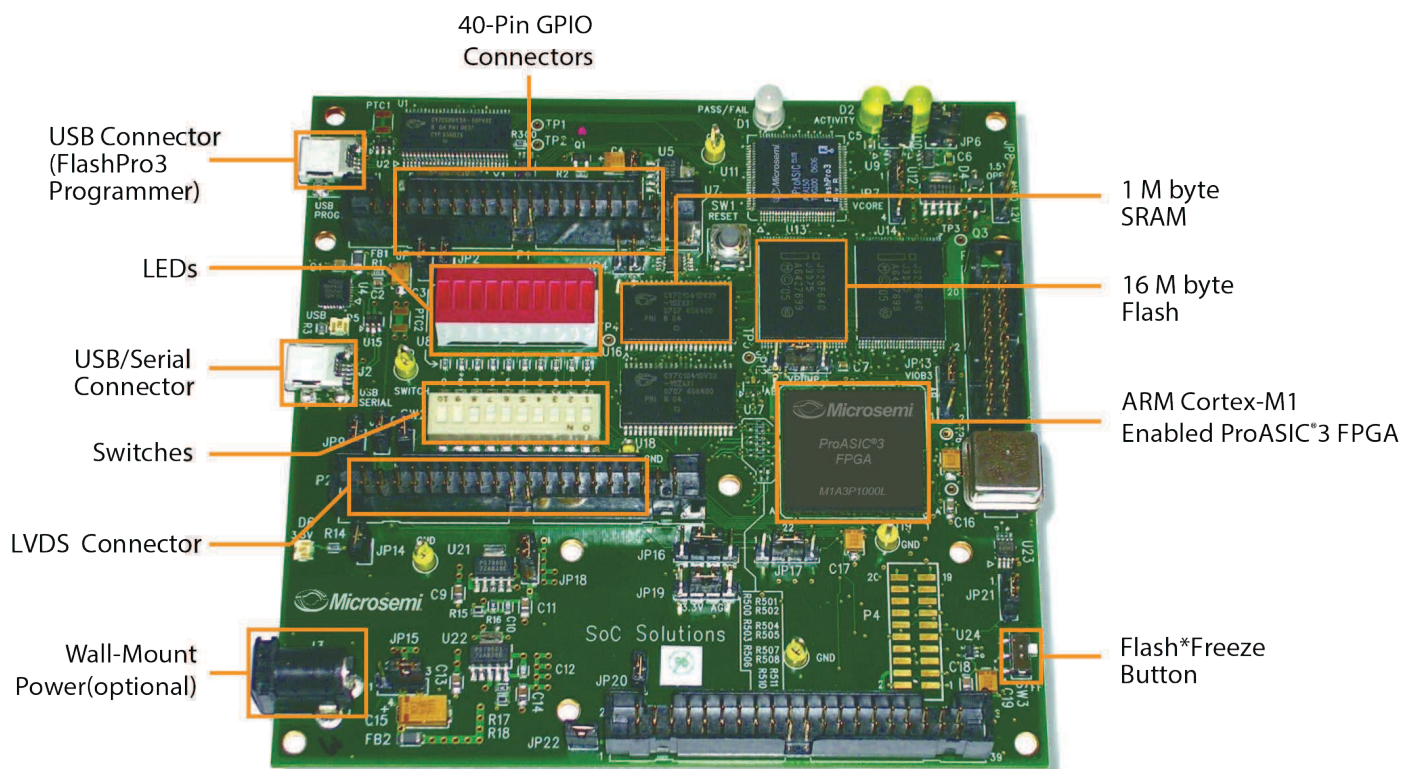
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ARM Cortex-M1-Enabled ProASIC3L Development Kit Quickstart Card

Kit Contents—M1A3PL-DEV-KIT

Quantity	Description
1	M1A3PL Development Board with M1A3PL1000-FGG484; includes built-in FlashPro3-compatible programming circuit
1	+5.0 V external power supply with international adapters
2	USB A to mini-B USB cables
1	Quickstart card



Overview

Cortex-M1-Enabled ProASIC3L Development Kit is a platform for the development and verification of embedded microprocessors. It is also a platform for product development and algorithm development. This ARM Cortex-M1-Enabled ProASIC3L Development Kit finds its applications in smart phones, global positioning systems (GPS), data management capability models (DCAM), personal data assistants (PDA), portable industrial and medical equipment, PC laptops, and PCMCIA cards.

Hardware Features

- Microsemi M1A3PL1000 ProASIC3L FPGA
- 1 MB SRAM
- 6 MB flash
- USB–RS232 converter chip
- GPIO connectors
- Ultra-low power with Flash*Freeze technology
- On-board FlashPro3 circuitry
- 20-pin Cortex-M1 JTAG connector
- Socketed crystal oscillator
- Push-button power-on reset circuit
- 10 test LEDs
- 10 test switches
- Expansion connectors

Pre-Programmed Demo Design

Press SW1 to reset. The LED on the board will perform a traffic light sequence. To follow this more clearly, go to the web page and install and run the traffic light.exe file.

Jumper and Switch Settings

Before powering up the M1 ProASIC3L Development Board for the first time, make sure the switches and jumpers are in the following factory-set positions:

- SW2: All switches (0-9) are in the ON position.
- JP1, JP2, JP3 (2–3), JP4, JP6, JP7, JP9 (2–3), JP10, JP11, JP12, JP13 (2–3), JP14, JP15, JP16 (2–3), JP17(2–3), JP18 (2–3), JP19 (2–3), JP20 (1–2), JP21 (2–3), JP22 (1–4), JP23, JP24 are installed.
- All others are not installed.

See the Documentation Resources section for more information.

Software and Licensing

Libero® SoC Design Suite offers high productivity with its comprehensive, easy-to-learn, easy-to-adopt development tools for designing with Microsemi's low power Flash FPGAs and SoC. The suite integrates industry standard Synopsys Synplify Pro® synthesis and Mentor Graphics ModelSim® simulation with best-in-class constraints management and debug capabilities.

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Documentation Resources

For more information about the ARM Cortex-M1_Enabled Pro ASIC3L Development Kit, including user's guides, tutorials, and design examples, see the documentation at www.microsemi.com/products/fpga-soc/design-resources/dev-kits/proasic3/cortex-m1-enabled-proasic3l-development-kit#documents

Support

Technical support is available online at www.microsemi.com/soc/support and by email at soc_tech@microsemi.com

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