# imall

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### Contact us

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#### Digital Attenuator 31.5 dB, 6-Bit, TTL Driver, DC-2.0 GHz

#### Features

- Attenuation: 0.5 dB Steps to 31.5 dB
- Low DC Power Consumption
- Plastic SOIC, Wide Body, SMT Package
- Integral TTL Driver
- 50 ohm Impedance
- Test Boards are Available
- Tape and Reel Packaging Available
- Lead-Free SOW-24 Package
- 100% Matte Tin Plating over Copper
- Halogen-Free "Green" Mold Compound
- 260°C Reflow Compatible
- RoHS\* Compliant Version of AT65-0107

#### Description

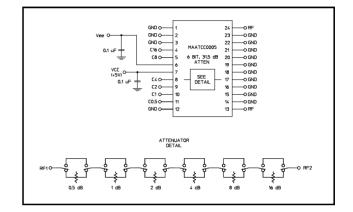
M/A-COM's MAATCC0005 is a GaAs FET 6-bit digital attenuator with a 0.5 dB minimum step size and a 31.5 dB total attenuation range. This device is in a SOIC-24 wide body, plastic surface mount package. The MAATCC0005 is ideally suited for use where accuracy, fast speed, very low power consumption and low costs are required.

#### **Ordering Information**

Part Number	Package
MAATCC0005	Bulk Packaging
MAATCC0005TR	1000 piece reel
MAATCC0005-TB	Sample Test Board

Note: Reference Application Note M513 for reel size information.

#### Schematic with Off-Chip Components



#### **Pin Configuration**

Pin No.	Function	Pin No.	Function	
1	GND	13	RF	
2	GND	14	GND	
3	GND	15	GND	
4	C16	16	GND	
5	C8	17	GND	
6	Vee	18	GND	
7	Vcc	19	GND	
8	C4	20	GND	
9	C2	21	GND	
10	C1	22	GND	
11	C0.5	23	GND	
12	GND	24	RF	

\* Restrictions on Hazardous Substances, European Union Directive 2002/95/EC.

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#### Digital Attenuator 31.5 dB, 6-Bit, TTL Driver, DC-2.0 GHz

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### Electrical Specifications: T<sub>A</sub> = 25°C

Parameter	Test Conditions	Frequency	Units	Jnits Min		Max	
Insertion Loss		DC - 1.0 GHz DC - 2.0 GHz	dB dB	_	3.1 3.6	3.6 4.2	
Attenuation Accuracy	Any Bit or Combination of Bits	DC - 2.0 GHz	dB	dB —		±(.3 +4% of atten.)	
VSWR	Full Range	DC - 2.0 GHz	- 2.0 GHz Ratio —		1.8:1	2:1	
Switching Speed	50% Cntl to 90%/10% RF 10% to 90% or 90% to 10%	_	nS nS			150 50	
1 dB Compression		50 MHz 0.5 - 2.0 GHz	dBm — Iz dBm —		+21 +29	_	
Input IP <sub>3</sub>	Two-tone inputs up to +5 dBm	50 MHz 0.5-2.0 GHz	dB dB	_	+35 +48	_	
Vcc <sup>1</sup> Vee <sup>1</sup>	_	_	V 4.75 V -8.0		5.0 -5.0	5.25 -4.75	
V <sub>IL</sub> V <sub>IH</sub>	LOW-level input voltage HIGH-level input voltage	_	— V 0 — V 2		_	0.8 5.0	
lin (Input Leakage Current)	Vin = V <sub>CC</sub> or GND	—	— uA		—	1.0	
Icc (Quiescent Supply Current)	Vcntrl = V <sub>CC</sub> or GND	_	— uA		250	400	
∆lcc (Additional Supply Current Per TTL Input Pin)	$V_{CC}$ = Max, Vcntrl = $V_{CC}$ - 2.1 V	—	mA	_	—	1.0	
IEE	VEE min to max, Vin = $V_{IL}$ or $V_{IH}$	I = V <sub>IL</sub> or V <sub>IH</sub> —		-1.0	-0.2	—	

1. Decoupling capacitors (.1  $\mu F)$  are required on Power Supply lines.

#### Absolute Maximum Ratings<sup>2,3</sup>

Parameter	Absolute Maximum		
Max. Input Power 0.05 GHz 0.5 - 2.0 GHz	+27 dBm +34 dBm		
V <sub>cc</sub>	$-0.5 V \le V_{CC} \le +7.0 V$		
V <sub>EE</sub>	$-8.5 \text{V} \le \text{V}_{\text{EE}} \le +0.5 \text{V}$		
V <sub>CC</sub> - V <sub>EE</sub>	$-0.5 \text{V} \leq \text{V}_{\text{CC}} - \text{V}_{\text{EE}} \leq 14.5 \text{V}$		
Vin <sup>4</sup>	$-0.5V \le Vin \le V_{CC} + 0.5V$		
Operating Temperature	-40°C to +85°C		
Storage Temperature	-65°C to +125°C		

2. Exceeding any one or combination of these limits may cause permanent damage to this device.

 M/A-COM does not recommend sustained operation near these survivability limits.

4. Standard CMOS TTL interface, latch-up will occur if logic signal is applied prior to power supply.

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<sup>2</sup> 

#### Digital Attenuator 31.5 dB, 6-Bit, TTL Driver, DC-2.0 GHz

#### **Handling Procedures**

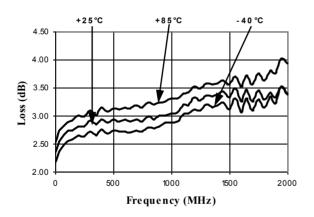
Please observe the following precautions to avoid damage:

#### **Static Sensitivity**

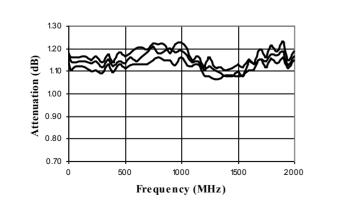
Gallium Arsenide Integrated Circuits are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these devices.

#### **Typical Performance Curves**

Loss vs. Temperature



1 dB Bit vs. Temperature



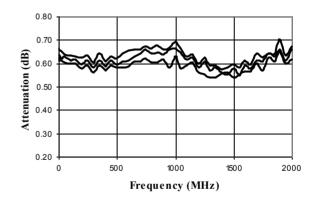
3

Truth Table (Digital Attenuator)

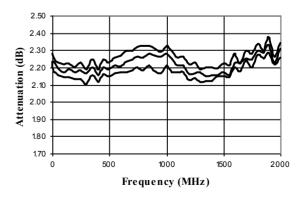
C16	C8	C4	C2	C1	C0.5	Attenuation
0	0	0	0	0	0	Loss. Reference
0	0	0	0	0	1	0.5 dB
0	0	0	0	1	0	1.0 dB
0	0	0	1	0	0	2.0 dB
0	0	1	0	0	0	4.0 dB
0	1	0	0	0	0	8.0 dB
1	0	0	0	0	0	16.0 dB
1	1	1	1	1	1	31.5 dB

0 = TTL Low; 1 = TTL High

#### 0.5 dB Bit vs. Temperature



2 dB Bit vs. Temperature



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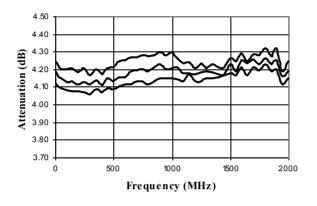
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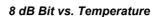
#### **Digital Attenuator** 31.5 dB, 6-Bit, TTL Driver, DC-2.0 GHz

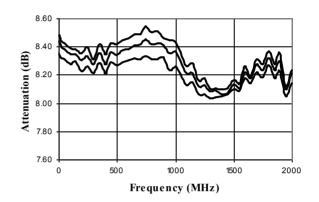
#### **Typical Performance Curves**

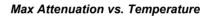
#### 4 dB Bit vs. Temperature

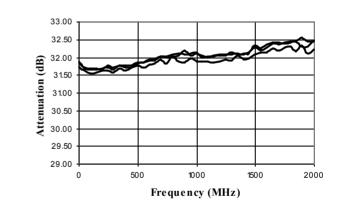


16 dB Bit vs. Temperature

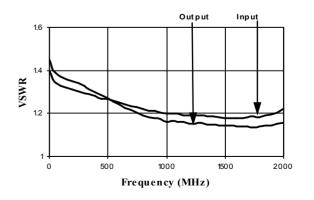






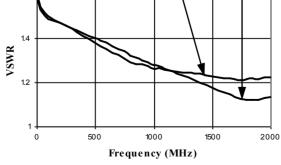


VSWR, 0.5 dB Bit



1.6

VSWR @ Insertion Loss

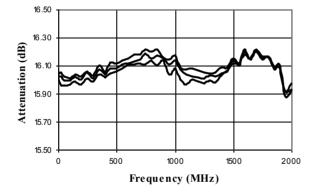


Output

Input

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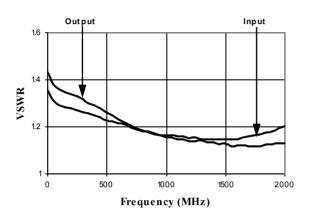
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<sup>4</sup> 

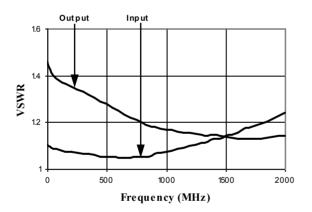
#### **Digital Attenuator** 31.5 dB, 6-Bit, TTL Driver, DC-2.0 GHz

#### **Typical Performance Curves**

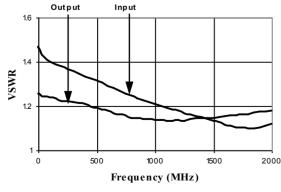
#### VSWR, 1 dB Bit



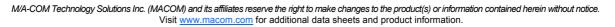
VSWR, 4 dB Bit



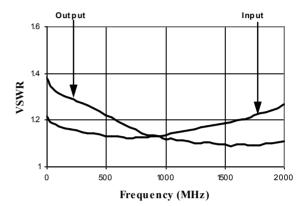
VSWR, 16 dB Bit



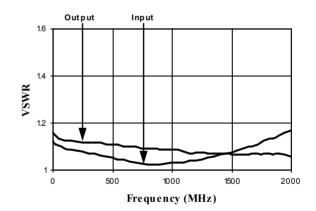
5



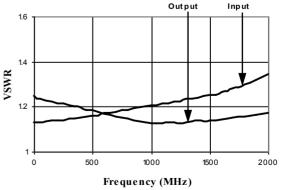
VSWR, 2 dB Bit



VSWR, 8 dB Bit



VSWR, Maximum Attenuation





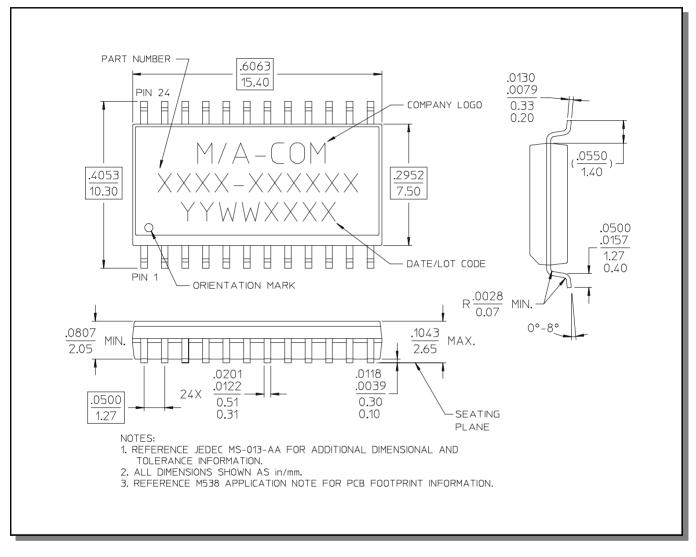
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### Lead-Free, SOW-24<sup>†</sup>



<sup>†</sup> Reference Application Note M538 for lead-free solder reflow recommendations.

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