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Frequency Doubler 16 - 24 GHz Output

Features

- 16 24 GHz Output Frequency Range
- 20 dBm Output Power
- High $1xF_{IN}$ and $3xF_{IN}$ Suppression
- High Dynamic Range

Ordering Information

Part Number

MAFC-004403-TR0500

MAFC-004403-TR1000

MAFC-004403-TR3000

MAFC-004403-001SMB

- Lead-Free 4 mm, 16-Lead QFN Package
- RoHS* Compliant and 260°C Reflow Compatible

Description

The MAFC-004403 is an active frequency doubler with an output frequency range of 16 - 24 GHz. The input power level ranges from 0 to 6 dBm, delivering a typical output power of 20 dBm. The device has excellent input and output return losses, and high $1xF_{in}$ and $3xF_{in}$ isolations.

The MAFC-004403 is ideally suited for use in LO chains in Point-to-Point radios for cellular backhaul applications. The 4mm QFN package is RoHS compliant and compatible with reflow temperatures to 260°C.

Package

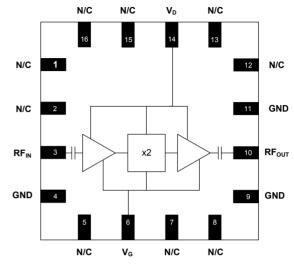
500 piece reel

1000 piece reel

3000 piece reel

Sample Test Board

Functional Block Diagram



Pin Configuration^{1,2}

Pin No.	Function	unction Description	
1	N/C	No Connection	
2	N/C	No Connection	
3	RF _{IN}	RF Input	
4	GND	Ground	
5	N/C	No Connection	
6	V _G	Gate Voltage	
7	N/C	No Connection	
8	N/C	No Connection	
9	GND	Ground	
10	RF _{OUT}	RF Output	
11	GND	Ground	
12	N/C	No Connection	
13	N/C	No Connection	
14	VD	Drain Voltage	
15	N/C	No Connection	
16	N/C	No Connection	

1. It is recommended that all No Connection pins (N/C) are connected to ground.

2. The exposed pad centered on the package bottom must be connected to RF and DC ground.

1

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*Restrictions on Hazardous Substances,

European Union Directive 2011/65/EU.

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Frequency Doubler 16 - 24 GHz Output

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Electrical Specifications: $V_D = +5 V$, $V_G = -0.7 V$, $P_{IN} = 0 dBm$, $T_A = +25^{\circ}C$

Parameter	Units	Min.	Тур.	Max.
Frequency (Input)	GHz	8	_	12
Frequency (Output)	GHz	16	_	24
Output Power (P _{OUT})	dBm	+18	+20	_
1xF _{IN} Leakage	dBc	_	30	_
3xF _{IN} Leakage	dBc	_	20	_
4xF _{IN} Leakage	dBc	_	20	_
Input Return Loss	dB	—	12	_
Output Return Loss	dB	—	12	_
Supply Current (I _D)	mA	95	140	170

Absolute Maximum Ratings ^{3,4,5}

Parameter	Absolute Maximum		
Input Power	+8 dBm		
Drain Voltage	+7 V		
Gate Voltage	-1.5 V to 0 V		
Storage Temperature	-55°C to +150°C		
Case Temperature	-40°C to +85°C		
Junction Temperature ⁶	+160 °C		

- 3. Exceeding any one or combination of these limits may cause permanent damage to this device.
- MACOM does not recommend sustained operation near these survivability limits.
- 5. Operating at nominal conditions with $T_{\rm J}$ \leq 160°C will ensure MTTF > 1 x 10^6 hours.
- 6. Junction Temperature $(T_J) = T_C + \Theta jc * ((V * I) (P_{OUT} P_{IN}))$ Typical thermal resistance $(\Theta jc) = 93^{\circ}C/W$.
 - a) For T_C = 25°C,
 - T_J = 81°C @ 5 V, 130 mA, P_{IN} = +5 dBm, P_{OUT} = 17 dBm b) For T_C = 85°C,

 T_J =141°C @ 5 V, 130 mA, P_{IN} = +5 dBm, P_{OUT} = 17 dBm

Handling Procedures

The following precautions should be observed to avoid damage:

Static Sensitivity

Gallium Arsenide Integrated Circuits are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these Class 1A devices.

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²

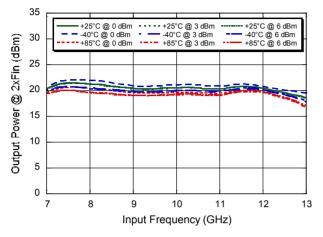


Frequency Doubler 16 - 24 GHz Output

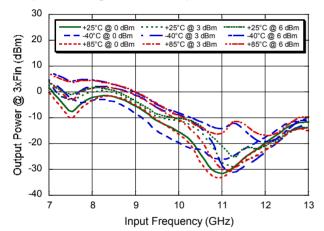
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Typical Performance Curves: V_D = +5 V, V_G = -0.7 V, Z_0 = 50 Ω

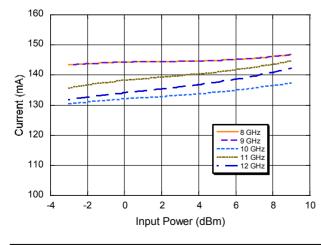
Output Power @ $2xF_{IN}$ vs. Temp., $P_{IN} = 0$, 3, 6 dBm



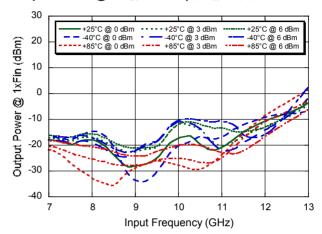
Output Power @ 3xF_{IN} vs. Temp., P_{IN} = 0, 3, 6 dBm



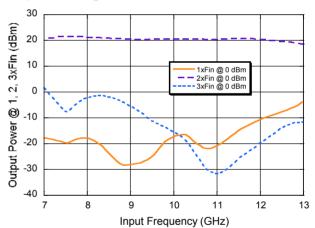




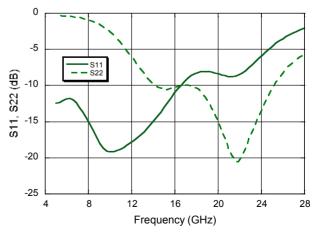
Output Power @ $1xF_{IN}$ vs. Temp., $P_{IN} = 0$, 3, 6 dBm



Output Power @ 1xF_{IN}, 2xFin & 3xF_{IN}, P_{IN} = 0 dBm







3

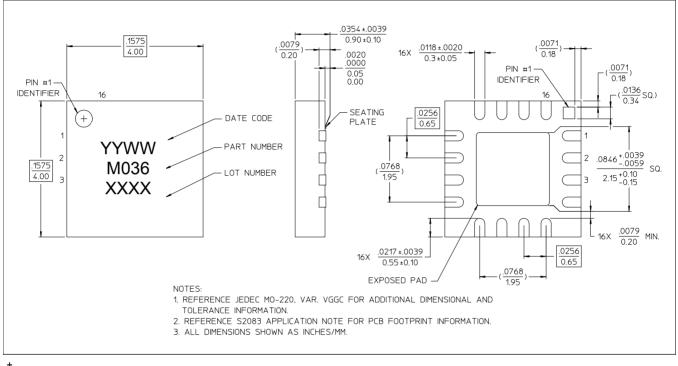
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Lead-Free 4 mm 16-Lead PQFN[†]



 Reference Application Note S2083 for lead-free solder reflow recommendations. Meets JEDEC moisture sensitivity level 1 requirements. Plating is 100% matte tin plating over copper

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Frequency Doubler 16 - 24 GHz Output



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