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## Features

- Voltage Operation as low as 1.6 V
- Insertion Loss: 0.20 dB @ 1 GHz
- Lead-Free 1 mm 6-Lead PDFN Package
- RoHS* Compliant and $260^{\circ} \mathrm{C}$ Reflow Compatible


## Description

M/A-COM's MASW-009444 is a GaAs PHEMT MMIC single pole two throw (SP2T) switch in a miniature $1 \times 1 \mathrm{~mm}$ 6-lead PDFN package. The MASW-009444 is ideally suited for applications where low control voltage, low insertion loss, moderate isolation, and small size are required.

Typical applications are for filter and antenna switching in handset systems that connect separate receive functions to a common antenna, as well as other related handset and general purpose applications. This part can be used in all systems operating up to 6 GHz requiring high power at low control voltage.

The MASW-009444 is fabricated using a 0.5 micron gate length GaAs PHEMT process. The process features full passivation for performance and reliability.

## Ordering Information ${ }^{1,2}$

| Part Number | Package |
| :---: | :---: |
| MASW-009444-TR3000 | 3000 piece reel |
| MASW-009444-001SMB | Sample Board |

1. Reference Application Note M513 for reel size information.
2. All sample boards include 5 loose parts.

## Functional Diagram



## Pin Configuration

| Pin No. | Function | Description |
| :---: | :---: | :---: |
| 1 | V1 | Control 1 |
| 2 | GND | Ground |
| 3 | RF1 | RF Port 1 |
| 4 | RF2 | RF Port 2 |
| 5 | V2 | Control 2 |
| 6 | RFC | RF Common |

## Absolute Maximum Ratings ${ }^{3,4}$

| Parameter | Absolute Maximum |
| :---: | :---: |
| Max Input Power <br> $(0.5-3 \mathrm{GHz}, 2.7 \mathrm{~V}$ Control) | +27 dBm |
| Operating Voltage | +8.5 volts |
| Operating Temperature | $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ |
| Storage Temperature | $-65^{\circ} \mathrm{C}$ to $+150^{\circ} \mathrm{C}$ |

3. Exceeding any one or combination of these limits may cause permanent damage to this device.
4. $\mathrm{M} / \mathrm{A}-\mathrm{COM}$ does not recommend sustained operation near these survivability limits.
[^0]Electrical Specifications: ${ }^{5} \mathrm{~T}_{\mathrm{A}}=25^{\circ} \mathrm{C}, \mathrm{V}_{\mathrm{C}}=1.8 \mathrm{~V}, \mathrm{Z}_{0}=50 \Omega$

| Parameter | Test Conditions | Units | Min. | Typ. | Max. |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Insertion Loss | $\begin{gathered} \mathrm{DC}-1 \mathrm{GHz} \\ 1-2 \mathrm{GHz} \\ 2-3 \mathrm{GHz} \end{gathered}$ | dB | - | $\begin{aligned} & 0.2 \\ & 0.3 \\ & 0.4 \end{aligned}$ | $\begin{aligned} & .45 \\ & - \end{aligned}$ |
| Isolation | $\begin{gathered} \mathrm{DC}-1 \mathrm{GHz} \\ 1-2 \mathrm{GHz} \\ 2-3 \mathrm{GHz} \end{gathered}$ | dB | $\begin{aligned} & 22 \\ & - \end{aligned}$ | $\begin{aligned} & 28 \\ & 23 \\ & 19 \end{aligned}$ | - |
| VSWR | $\begin{gathered} \mathrm{DC}-2.5 \mathrm{GHz} \\ 2.5-3 \mathrm{GHz} \end{gathered}$ | dB | - | $\begin{aligned} & <1.3 \\ & <1.3 \end{aligned}$ | - |
| IP3 | $\begin{gathered} \text { Two Tone }+10 \mathrm{dBm}, 5 \mathrm{MHz} \text { Spacing, >50 MHz } \\ \text { PiN }_{\text {IN }}=0 \mathrm{dBm} \mathrm{~V}_{\mathrm{C}}=0 / 2.5 \mathrm{~V} \end{gathered}$ | dBm | - | 47 | - |
| P1dB | $\begin{aligned} & \mathrm{V}_{\mathrm{C}}=1.8 \mathrm{~V} \\ & \mathrm{~V}_{\mathrm{C}}=2.7 \mathrm{~V} \end{aligned}$ | dBm | - | $\begin{aligned} & 15 \\ & 24 \end{aligned}$ | - |
| Trise, Tfall | 10\% to $90 \%$ RF, $90 \%$ to $10 \%$ RF | ns | - | 10 | - |
| Ton, Toff | 50\% control to 90\% RF, and 50\% control to 10\% RF | ns | - | 15 | - |
| Transients | In Band | mV | - | 30 | - |
| Control Current | $\|\mathrm{VC}\|=1.8 \mathrm{~V}$ | $\mu \mathrm{A}$ | - | 5 | 10 |

5. Insertion Loss can be optimized by varying the DC Blocking Capacitor value, i.e. 1000 pF for $100-500 \mathrm{MHz}, 39 \mathrm{pF}$ for $0.5-3.0 \mathrm{GHz}$

## Truth Table ${ }^{6}$

| V1 | V2 | RFC - RF1 | RFC - RF2 |
| :---: | :---: | :---: | :---: |
| 0 V | +1.8 to 5 V | On | Off |
| +1.8 to 5 V | 0 V | Off | On |

6. External DC blocking capacitors are required on all RF ports

## Handling Procedures

Please observe the following precautions to avoid damage:

## Static Sensitivity

Gallium Arsenide Integrated Circuits are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these devices.

## Typical Performance Curves



Input Return Loss


Isolation


Output Return Loss


Input P1dB vs. Control Voltage


## Lead-Free 1 mm 6-Lead PDFN ${ }^{\dagger}$



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[^0]:    * Restrictions on Hazardous Substances, European Union Directive 2002/95/EC.

[^1]:    ${ }^{\dagger}$ Reference Application Note S2083 for lead-free solder reflow recommendations.
    Meets JEDEC moisture sensitivity level 1 requirements.

