# imall

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# GaAs Broadband DPDT Diversity Switch 0.5 - 3.0 GHz

#### Features

- Ideal for 802.11 b/g Applications
- Broadband Performance: 0.5 3.0 GHz
- Low Insertion Loss: 0.6 dB @ 2.4 GHz
- High Isolation: 25 dB @ 2.4 GHz
- Fast Switching Speed: 0.5 µm GaAs PHEMT Process
- High P1dB: 34 dBm @ 3 V
- Lead-Free 3 mm 12-Lead PQFN Package
- 100% Matte Tin Plating over Copper
- Halogen-Free "Green" Mold Compound
- RoHS\* Compliant and 260°C Reflow Compatible

#### Description

M/A-COM's MASWSS0130 is a broadband GaAs PHEMT MMIC DPDT diversity switch in a low cost, lead-free 3 mm 12-lead PQFN plastic package. The MASWSS0130 is ideally suited for applications where very small size and low cost are required.

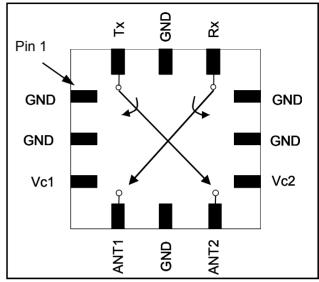
Typical applications are for WLAN IEEE 802.11b/g systems that employ two antennas for transmit and receive diversity. Designed for high power, this DPDT switch is optimized for high linearity at 2.4 GHz.

The MASWSS0130 is fabricated using a 0.5 micron gate length GaAs PHEMT process. The process features full passivation for performance and reliability.

Part Number	Package
MASWSS0130	Bulk Packaging
MASWSS0130TR	7 inch, 1000 piece reel
MASWSS0130TR-3000	13 inch, 3000 piece reel
MASWSS0130SMB	Sample Test Board (Includes 5 Samples)

1. Reference Application Note M513 for reel size information.

## Functional Schematic



### **Pin Configuration**

PIN No.	PIN Name	Description
1	GND	Ground
2	GND	Ground
3	V <sub>c</sub> 1	Control 1
4	ANT1	Antenna Port 1
5	GND	Ground
6	ANT2	Antenna Port 2
7	V <sub>c</sub> 2	Control 2
8	GND	Ground
9	GND	Ground
10	Rx	Receive Port
11	GND	Ground
12	Tx	Transmit Port

\* Restrictions on Hazardous Substances, European Union Directive 2002/95/EC.

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Ordering Information<sup>1</sup>

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### **GaAs Broadband DPDT Diversity Switch** 0.5 - 3.0 GHz



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### Electrical Specifications: $T_A = 25^{\circ}C$ , $Z_0 = 50 \Omega$ , $V_C = 0 V/3 V$ , 8 pF Capacitor <sup>2,3</sup>

Parameter	Test Conditions	Units	Min.	Тур.	Max.
Insertion Loss <sup>3</sup>	2.4 GHz	dB	_	0.6	0.9
Isolation (on/off or off/on)	2.4 GHz	dB	20	25	_
Return Loss	2.4 GHz	dB		23	—
IP2	Two Tone, +15 dBm per tone, 5 MHz Spacing, 2.4 GHz	dBm	_	94	_
IIP3	Two Tone, +15 dBm per tone, 5 MHz Spacing, 2.4 GHz	dBm		55	_
Input P1dB	2.4 GHz	dBm	—	34	—
Input P0.1dB	2.4 GHz	dBm	_	30	_
2 <sup>nd</sup> Harmonic	2.4 GHz, P <sub>IN</sub> = 20 dBm	dBm	_	-80	_
3 <sup>rd</sup> Harmonic	2.4 GHz, P <sub>IN</sub> = 20 dBm	dBm	_	-95	_
Trise, Tfall	10% to 90% RF 90% to 10% RF	nS nS	_	34 35	_
Ton, Toff	50% control to 90% RF 50% control to 10% RF	nS	_	48 54	_
Transients		mV	—	7	—
Control Current	Vc  = 3 V	μA	_	5	25

2. External DC blocking capacitors are required on all RF ports.

3. Insertion loss can be optimized by varying the DC blocking capacitor value.

### Absolute Maximum Ratings <sup>4,5</sup>

Parameter	Absolute Maximum
Input Power @ 3 V Control	+35 dBm
Input Power @ 5 V Control	+35 dBm
Operating Voltage	+8.5 volts
Operating Temperature	-40°C to +85°C
Storage Temperature	-65 <sup>°</sup> C to +150 <sup>°</sup> C

4. Exceeding any one or combination of these limits may cause permanent damage to this device.

5. M/A-COM does not recommend sustained operation near these survivability limits.

#### Qualification

Qualified to M/A-COM specification REL-201, Process Flow -2.

#### Handling Procedures

Please observe the following precautions to avoid damage:

#### Static Sensitivity

Gallium Arsenide Integrated Circuits are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these devices.

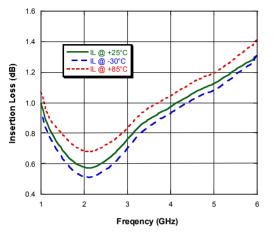
2

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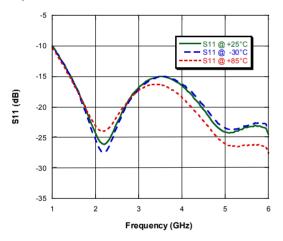
## GaAs Broadband DPDT Diversity Switch 0.5 - 3.0 GHz

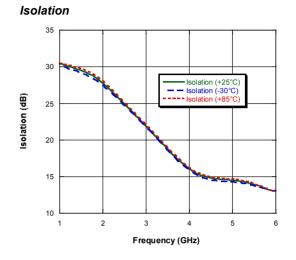
## **Typical Performance Curves**

Insertion Loss

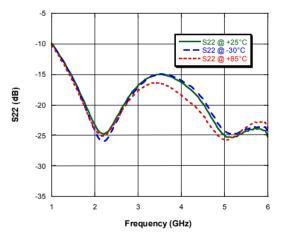


Input Return Loss





**Output Return Loss** 



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#### 0 0

Control

V<sub>c</sub>1

1

0

1

Qty

4

Truth Table <sup>6</sup>

6. 1 = +2.9 to +5V, 0 = 0 + 0.2V

Control

V<sub>c</sub>2

0

1

1

### Lead-Free 3 mm 12-Lead PQFN<sup>†</sup>

.1181 3.00

2

**ANT 1-**

Rx

On

Off

Off

Off



## **Evaluation Board**

Description

Capacitor, 8 pF, 0402, SMT, 5% (C1 - C4)

**ANT 2-**

Tx

On

Off

Off

Off

**ANT 2-**

Rx

Off

On

Off

Off

 $(\frac{.0079}{0.20})$ 

.0354 ±.0039 0.90 ±0.10

.0020 .0000

0.05

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SEATING PLATE

.0091+.0028

0.23+0.07

12X

.0197 0.50

1

(<u>.0394</u>)

Ţ

12X .0217 ± 0039

0.55 ±0.10

ANT 1

Tx

Off

On

Off

Off

.<u>1181</u> 3.00

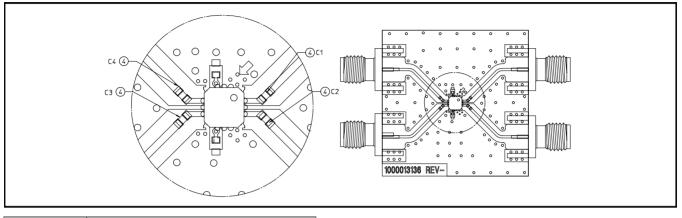
12

- PIN ±1

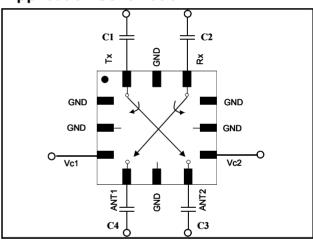
IDENTIFIER

WW

ХХХ



A		Cohomotic	
App	lication	Schematic	,



PIN #1

IDENTIFIER

.0197 0.50

.0453 +.0039

1.15 +0.10

12X .0079 MIN

SQ

### (<u>.0394</u> 1.00 EXPOSED PAD NOTES: 1. REFERENCE JEDEC MO-220, VAR VEED-1 FOR ADDITIONAL DIMENSIONAL AND TOLERANCE INFORMATION. 2. REFERENCE S2083 APPLICATION NOTE FOR PCB FOOTPRINT INFORMATION 3. ALL DIMENSIONS SHOWN AS INCHES/MM.

DATE CODE

LOT NUMBER

PART NUMBER

† Reference Application Note M538 for lead-free solder reflow recommendations.

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GaAs Broadband DPDT Diversity Switch 0.5 - 3.0 GHz



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