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**+3.3V, 622Mbps SDH/SONET Laser Driver  
with Automatic Power Control****General Description**

The MAX3667 is a complete, +3.3V laser driver with automatic power control (APC), designed for SDH/SONET applications up to 622Mbps. It accepts differential PECL inputs, provides single-ended bias and modulation currents, and operates over a -40°C to +85°C temperature range.

A temperature-stabilized reference voltage simplifies laser current programming. It allows external programming of the modulation current between 5mA<sub>P-P</sub> and 60mA<sub>P-P</sub>, and of the bias current between 5mA and 90mA.

The APC function, which incorporates a monitor photodiode, an external resistor, and two external capacitors, maintains constant laser output power. Two current monitors provide high-speed signals that are directly proportional to the bias and modulation currents. Additional features include disable/enable control and a slow-start feature with a minimum turn-on time of 50ns. The MAX3667 is available in die form and in a 32-pin TQFP package.

**Applications**

622Mbps SDH/SONET Access Nodes  
Laser Driver Transmitters  
Section Repeaters

**Features**

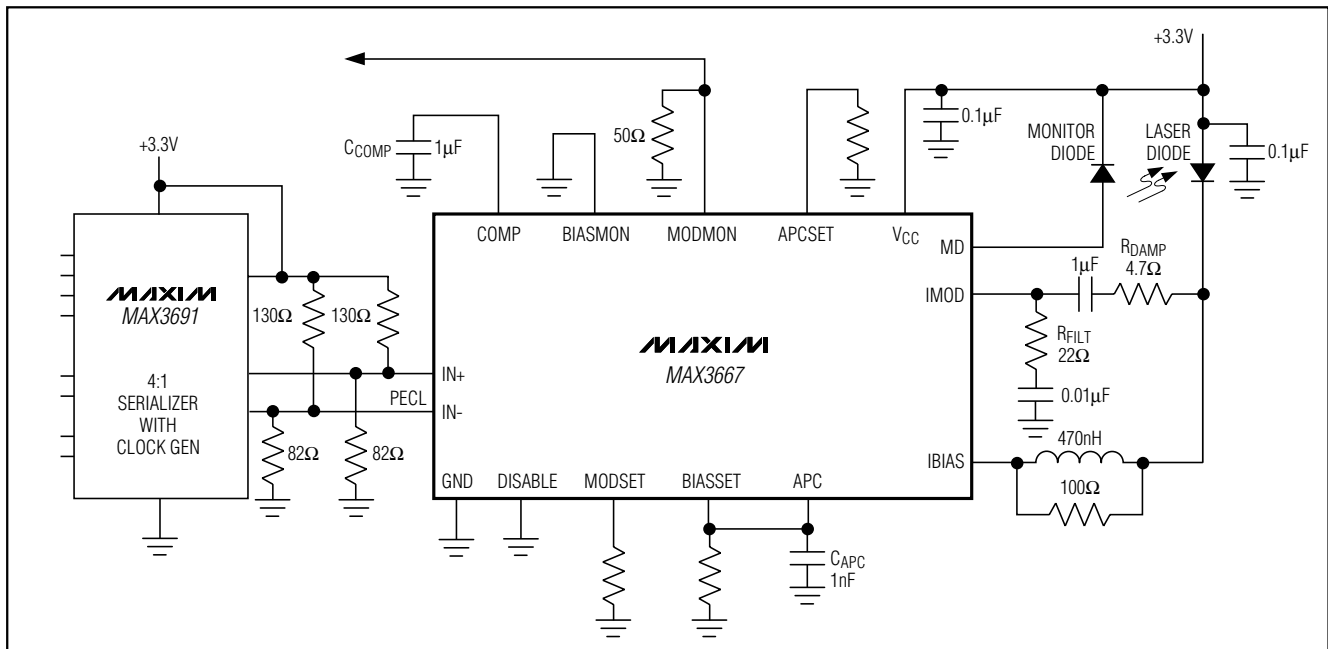
- ◆ Single +3.3V or +5.0V Operation
- ◆ Automatic Average Power Control
- ◆ Bias Current and Modulation Current Monitor Outputs
- ◆ TTL-Compatible Disable Input
- ◆ Temperature-Compensated Reference
- ◆ PECL-Compatible Data Inputs

**Ordering Information**

PART	TEMP RANGE	PIN-PACKAGE
MAX3667ECJ	-40°C to +85°C	32 TQFP
MAX3667E/D	-40°C to +85°C	Dice*

\*Dice are designed to operate from -40°C to +85°C but are tested and guaranteed only at  $T_j = +25^\circ\text{C}$ .

Pin Configuration appears at end of data sheet.

**Typical Operating Circuit**

# +3.3V, 622Mbps SDH/SONET Laser Driver with Automatic Power Control

## ABSOLUTE MAXIMUM RATINGS

Supply Voltage, $V_{CC}$ .....	-0.5V to +7.0V	Continuous Power Dissipation ( $T_A = +85^\circ\text{C}$ )	
Current into IBIAS.....	-50mA to 350mA	TQFP (derate 11.1mW/ $^\circ\text{C}$ above +85 $^\circ\text{C}$ ).....	721mW
Current into IMOD.....	-50mA to 200mA	Operating Temperature Range .....	-40 $^\circ\text{C}$ to +85 $^\circ\text{C}$
Current into MD .....	$\pm 7\text{mA}$	Operating Junction Temperature Range (die) ..	-55 $^\circ\text{C}$ to +175 $^\circ\text{C}$
Voltage at APC, MODMON, BIASMON, COMP.....	-0.5V to ( $V_{CC} + 0.5\text{V}$ )	Processing Temperature (die) .....	+400 $^\circ\text{C}$
Voltage at IN+, IN-, DISABLE, MODSET, BISASSET, APCSET, PULLUP.....	-0.5V to ( $V_{CC} + 0.5\text{V}$ )	Storage Temperature Range .....	-65 $^\circ\text{C}$ to +160 $^\circ\text{C}$
		Lead Temperature (soldering, 10s) .....	+300 $^\circ\text{C}$

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

## DC ELECTRICAL CHARACTERISTICS

( $V_{CC} = +3.3\text{V} \pm 5\%$ ,  $T_A = -40^\circ\text{C}$  to +85 $^\circ\text{C}$ , unless otherwise noted.) (Notes 1, 2)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Supply Current (Note 3)	$I_{CC}$	Closed loop (Note 4)		112	133	mA
Bias Current Range	$I_{BIAS}$	(Note 5)	5		90	mA
Bias Off Current	$I_{BIAS}$	Disable = high		5	250	$\mu\text{A}$
Modulation Off Current	$I_{BIAS}$	Disable = high		20	250	$\mu\text{A}$
Internal Pullup Resistor (Note 6)	$R_{PULLUP}$		26	31	35	$\Omega$
Reference Voltage (Note 7)	$V_{REF}$	Disable = high or low	0.91	1.01	1.11	V
Bias Current Stability		$R_{BIASSET} = 2\text{k}\Omega$ , open loop (Note 8)		500		ppm/ $^\circ\text{C}$
		$R_{BIASSET} = 33.2\text{k}\Omega$ , open loop (Note 8)		1000		
		$R_{BIASSET} = 2\text{k}\Omega$ , closed loop (Notes 4, 9)			480	
Modulation Current Stability		$R_{MODSET} = 2\text{k}\Omega$ , open loop (Note 8)		1100		ppm/ $^\circ\text{C}$
		$R_{MODSET} = 33.2\text{k}\Omega$ , open loop (Note 8)		1100		
BIASMON to IBIAS Gain	$A_I$	$R_{BIASSET} = 2\text{k}\Omega$	30	38	46	A/A
MODMON to I <sub>QMOD</sub> Gain	$A_I$	$R_{MODSET} = 2\text{k}\Omega$ (Note 10)	26	33	40	A/A
IBIASSET to IBIAS Gain	$A_I$	$R_{BIASSET} = 2\text{k}\Omega$	145	170	200	A/A
		$R_{BIASSET} = 33.2\text{k}\Omega$	128	160	195	
I <sub>MODSET</sub> to I <sub>QMOD</sub> Gain	$A_I$	$R_{MODSET} = 2\text{k}\Omega$ (Note 10)	152	190	230	A/A
		$R_{MODSET} = 33.2\text{k}\Omega$ (Note 10)	152	190	230	
I <sub>APCSET</sub> to IBIAS Gain	$A_I$	$R_{APCSET} = 2\text{k}\Omega$	135	170	205	A/A
		$R_{APCSET} = 33.2\text{k}\Omega$	164	205	250	
PECL Input High Voltage	$V_{IH}$		2.14			V
PECL Input Low Voltage	$V_{IL}$				1.82	V
PECL Input High Current	$I_{IH}$	$V_{IN} = 2.14\text{V}$		4.5	10	$\mu\text{A}$
PECL Input Low Current	$I_{IH}$	$V_{IN} = 1.82\text{V}$		2	10	$\mu\text{A}$
TTL Disable High Voltage	$V_{DIH}$		2.0			V
TTL Disable Low Voltage	$V_{DIL}$				0.8	V
TTL Disable High Current	$I_{DIH}$			1		$\mu\text{A}$
TTL Disable Low Current	$I_{DIL}$			4		$\mu\text{A}$

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## DC ELECTRICAL CHARACTERISTICS (continued)

(V<sub>CC</sub> = +5.0V ±5%, T<sub>A</sub> = -40°C to +85°C, unless otherwise noted.) (Notes 1, 2)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Supply Current (Note 3)	I <sub>CC</sub>	Closed loop (Note 4)		134	160	mA
Bias Off Current		Disable = high		2	250	μA
Modulation Off Current		Disable = high		32	250	μA
BIASMON to IBIAS Gain	A <sub>I</sub>	R <sub>BIASSET</sub> = 2kΩ		38		A/A
MODMON to IQMOD Gain	A <sub>I</sub>	R <sub>MODSET</sub> = 2kΩ (Note 10)	26	33	40	A/A
I <sub>BIASSET</sub> to IBIAS Gain	A <sub>I</sub>	R <sub>BIASSET</sub> = 2kΩ	145	180	220	A/A
		R <sub>BIASSET</sub> = 33.2kΩ	143	180	215	
I <sub>MODSET</sub> to IQMOD Gain	A <sub>I</sub>	R <sub>MODSET</sub> = 2kΩ (Note 10)	168	240	315	A/A
		R <sub>MODSET</sub> = 33.2kΩ (Note 10)	188	230	285	
I <sub>APCSET</sub> to IBIAS Gain	A <sub>I</sub>	R <sub>APCSET</sub> = 2kΩ	132	166	200	A/A
		R <sub>APCSET</sub> = 33.2kΩ	145	182	220	
PECL Input High Voltage	V <sub>IH</sub>		3.84			V
PECL Input Low Voltage	V <sub>IL</sub>				3.52	V
PECL Input High Current	I <sub>IH</sub>	V <sub>IN</sub> = 3.84V		9		μA
PECL Input Low Current	I <sub>IH</sub>	V <sub>IN</sub> = 3.52V		8		μA

## AC ELECTRICAL CHARACTERISTICS

(V<sub>CC</sub> = +3.3V ±5%, T<sub>A</sub> = -40°C to +85°C, R<sub>LOAD</sub> = 10Ω, unless otherwise noted.) (Notes 2, 11)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Modulation Current Range	I <sub>MOD</sub>	R <sub>FILT</sub> = 22Ω, R <sub>DAMP</sub> = 0Ω (Note 12)	5		60	mA
Output Rise Time	t <sub>r</sub>	V <sub>CC</sub> = 3.3V ±5%, 20% to 80%		270	450	ps
		V <sub>CC</sub> = 5.0V ±5%, 20% to 80%		205	400	
Output Fall Time	t <sub>f</sub>	V <sub>CC</sub> = 3.3V ±5%, 20% to 80%		425	650	ps
		V <sub>CC</sub> = 5.0V ±5%, 20% to 80%		315	600	
Output Aberrations				±10		%
Pulse-Width Distortion	PWD	R <sub>FILT</sub> = 22Ω, R <sub>DAMP</sub> = 0Ω (Note 13)		70		ps

**Note 1:** Dice are tested at T<sub>A</sub> = +27°C.

**Note 2:** Minimum voltage at IBIAS = V<sub>CC</sub> - 1.6V.

**Note 3:** The sum of the currents flowing into V<sub>CC</sub> and PULLUP with R<sub>BIASSET</sub> = R<sub>MODSET</sub> = R<sub>APCSET</sub> = 2kΩ, I<sub>IN+</sub> = 1.82V, I<sub>IN-</sub> = 2.14V.

**Note 4:** APC is connected to BIASSET for closed-loop operation.

**Note 5:** Bias current range is guaranteed by the I<sub>BIASSET</sub> to IBIAS gain test.

**Note 6:** R<sub>PULL-UP</sub> is connected between I<sub>MOD</sub> and PULLUP.

**Note 7:** V<sub>REF</sub> is the voltage on BIASSET, MODSET, or APCSET with R<sub>BIASSET</sub> = R<sub>MODSET</sub> = R<sub>APCSET</sub> = 2kΩ.

**Note 8:** APC is disconnected from BIASSET for open-loop operation.

**Note 9:** Bias current stability is guaranteed by design and characterization.

**Note 10:** I<sub>QMOD</sub> is the current flowing into the collector of Q<sub>MOD</sub> (Figure 1).

**Note 11:** AC parameters are guaranteed by design and characterization.

**Note 12:** Modulation current range is guaranteed by the I<sub>MODSET</sub> to I<sub>QMOD</sub> gain test.

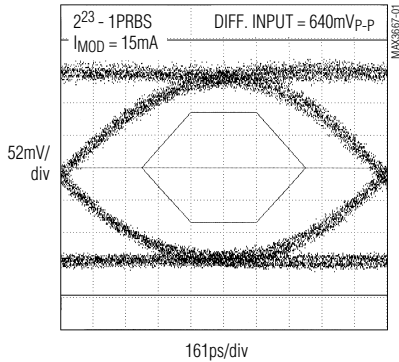
**Note 13:** Input signal is a 155Mbps 1-0 pattern. PWD = [(width of wider pulse) - (width of narrower pulse)] / 2.

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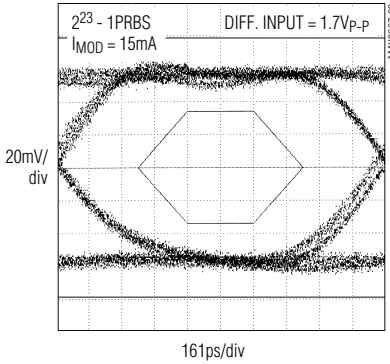
## Typical Operating Characteristics

( $T_A = +25^\circ\text{C}$ ,  $V_{CC} = +3.3\text{V}$ , unless otherwise noted.)

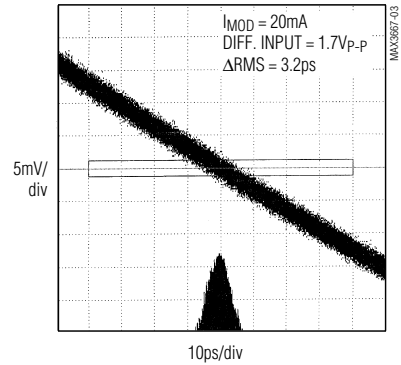
**EYE DIAGRAM**  
(622Mbps, 1300nm LASER,  
470MHz FILTER)



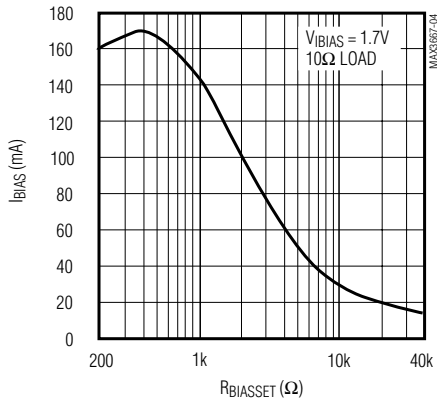
**EYE DIAGRAM**  
(622Mbps, 10Ω LOAD)



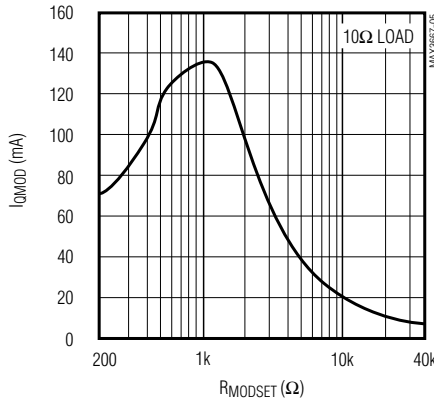
**OUTPUT JITTER**  
(622Mbps, 10Ω LOAD)



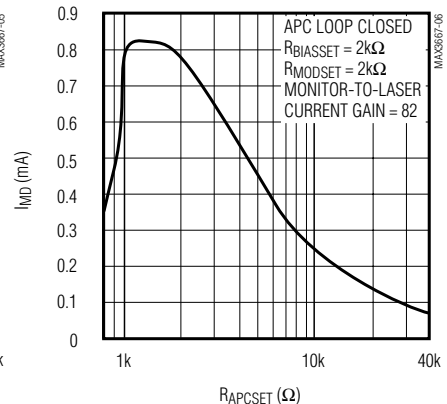
**I<sub>BIAS</sub> vs. R<sub>BIASSET</sub>**



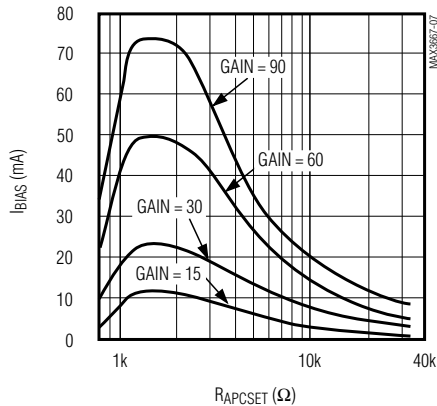
**I<sub>QMOD</sub> vs. R<sub>MODSET</sub>**



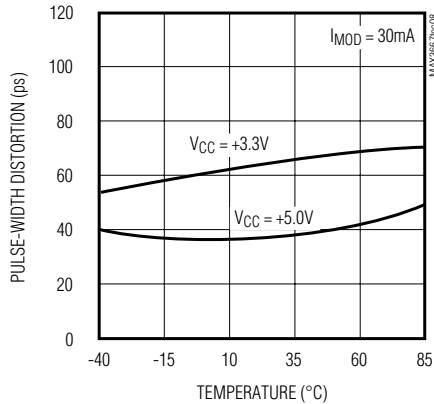
**I<sub>MD</sub> vs. R<sub>APCSET</sub>**



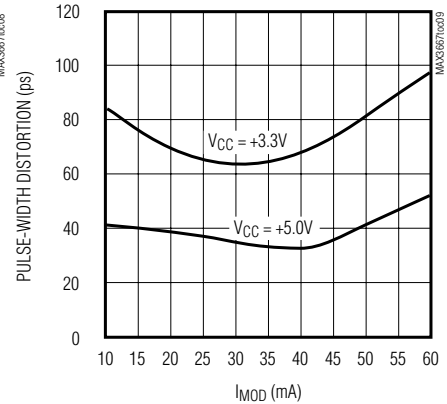
**I<sub>BIAS</sub> vs. R<sub>APCSET</sub>**  
(VARYING MONITOR-TO-LASER  
CURRENT GAIN)



**PULSE-WIDTH DISTORTION  
vs. TEMPERATURE**



**PULSE-WIDTH DISTORTION  
vs. I<sub>MOD</sub>**

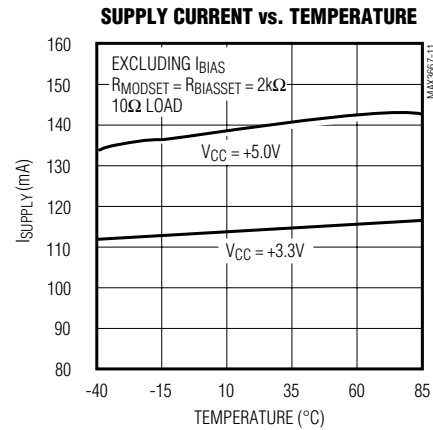
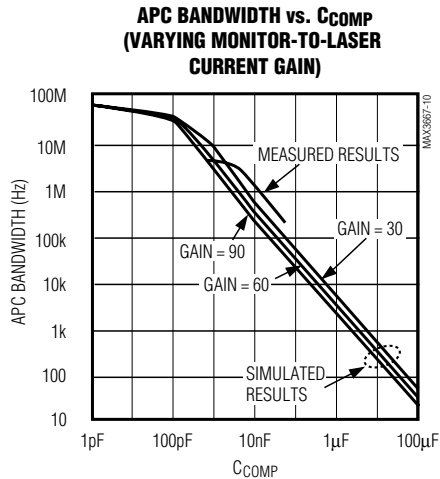


# +3.3V, 622Mbps SDH/SONET Laser Driver with Automatic Power Control

MAX3667

## Typical Operating Characteristics (continued)

( $T_A = +25^\circ\text{C}$ ,  $V_{CC} = +3.3\text{V}$ , unless otherwise noted.)



## Pin Description

PIN	NAME	FUNCTION
1, 2, 23, 24	$V_{CC}$	Supply Voltage Input
3, 6, 8, 13, 14, 15, 18, 20, 22	GND	Ground
4	IN+	Positive PECL-Compatible Input
5	IN-	Negative PECL-Compatible Input
7	DISABLE	Disable Input. High = disable, TTL-compatible input.
9, 26, 28, 31	N.C.	No Connection
10	MODSET	Adjustment for Laser-Diode Modulation Current
11	APC	Feedback Current for Closed-Loop Laser-Diode Bias Control
12	BIASSET	Open-Loop Adjustment for Laser-Diode Bias Current
16	IBIAS	Laser-Diode DC Bias Current
17	PULLUP	$V_{CC}$ Supply for Internal 31Ω Pullup Resistor
19, 21	IMOD	Laser-Diode Modulation Current
25	MD	Input for PIN Monitor Diode Current
27	APCSET	Closed-Loop Adjustment for Laser-Diode Bias Current
29	BIASMON	IBIAS Current Monitor (gain = 1/38 IBIAS). Open PNP collector; connect to ground if not used.
30	MODMON	IMOD Current Monitor (gain = 1/33 I <sub>QMOD</sub> ). Open PNP collector; connect to ground if not used.
32	COMP	External Compensation Capacitor for Closed-Loop Laser-Diode Bias Current Control Stability

# +3.3V, 622Mbps SDH/SONET Laser Driver with Automatic Power Control

## Detailed Description

Low-voltage operation of laser diodes and optical transmitters produces stringent headroom conditions for laser drivers. Fast changes in modulation current produce large inductive voltage spikes, creating device saturation problems. Therefore, for +3.3V operation, the MAX3667's modulation current should be AC-coupled to the cathode of a laser diode. The recommended DC-blocking capacitor value is  $1\mu\text{F}$ . A simplified block diagram of the modulation driver is shown in Figure 1.

The IMOD pin is internally biased through a  $31\Omega$  pullup resistor. This design decouples the headroom associated with the modulation driver from the forward voltage drop of the laser diode, allowing the circuit to tolerate greater di/dt voltage transients. The design of the MAX3667 assumes a maximum DC forward-voltage drop of 1.6V across the laser diode. Bias current is DC-coupled to the laser diode separately at the IBIAS output. In most applications, some small amount of resistance should be added in series with the DC-blocking capacitor to help damp out the aberrations created by parasitic elements.

## Automatic Power Control

The automatic power control (APC) feature allows an optical transmitter to maintain constant power, despite changes in laser efficiency due to temperature and aging. The APC loop requires the use of a PIN monitor photodiode, which generates a current proportional to the laser diode output power. A scaled version of the current flowing into the MD pin is compared to a scaled version of the current flowing out of the APCSET pin. When these currents are of equal value, the inputs of the operational transconductance amplifier (OTA) are balanced, and COMP is forced to approximately 1V.

When the average value of the monitor diode current exceeds the value established by the APCSET current, the COMP voltage is forced lower. If the average value of the monitor diode current is less than the value established by the APCSET current, the COMP node voltage is forced higher. The output of the OTA (the APC pin), when connected directly to BIASSET (closed-loop condition), is used as an error signal to adjust the bias current flowing into BIASSET. The maximum OTA output current is approximately  $\pm 250\mu\text{A}$ .

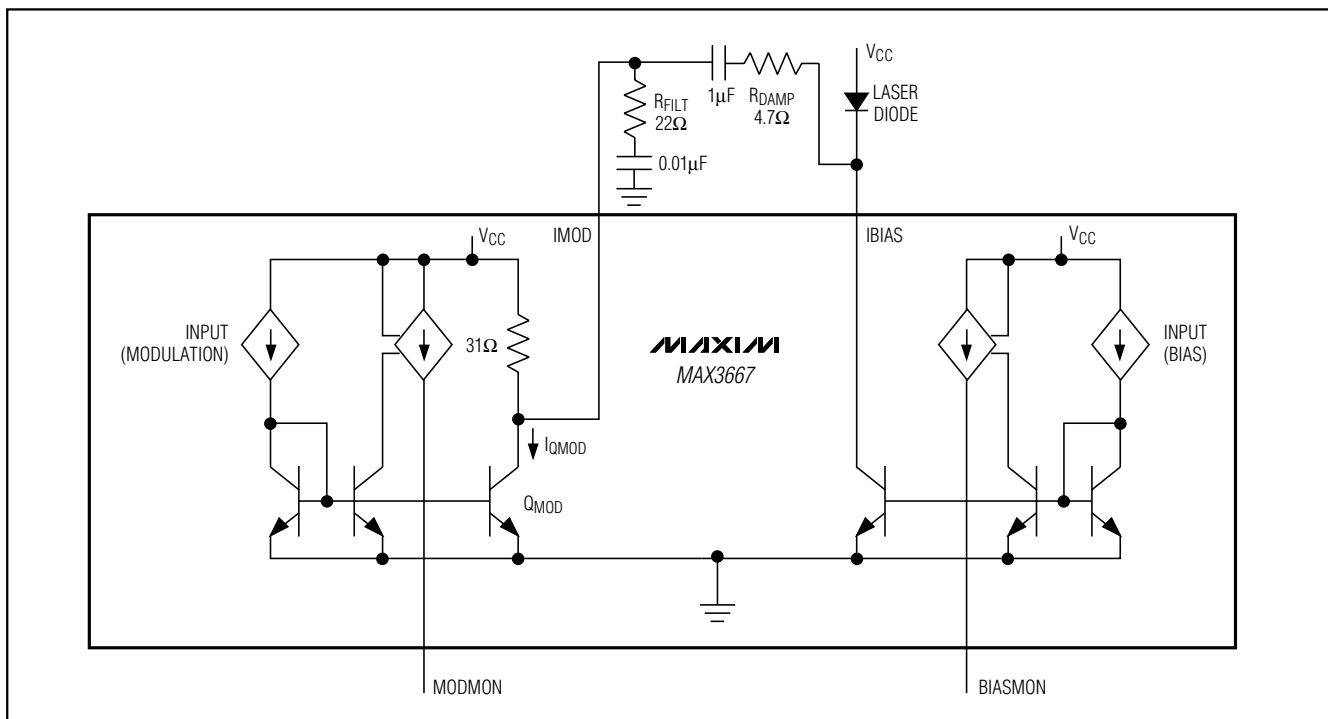


Figure 1. Simplified Modulation Driver Block Diagram

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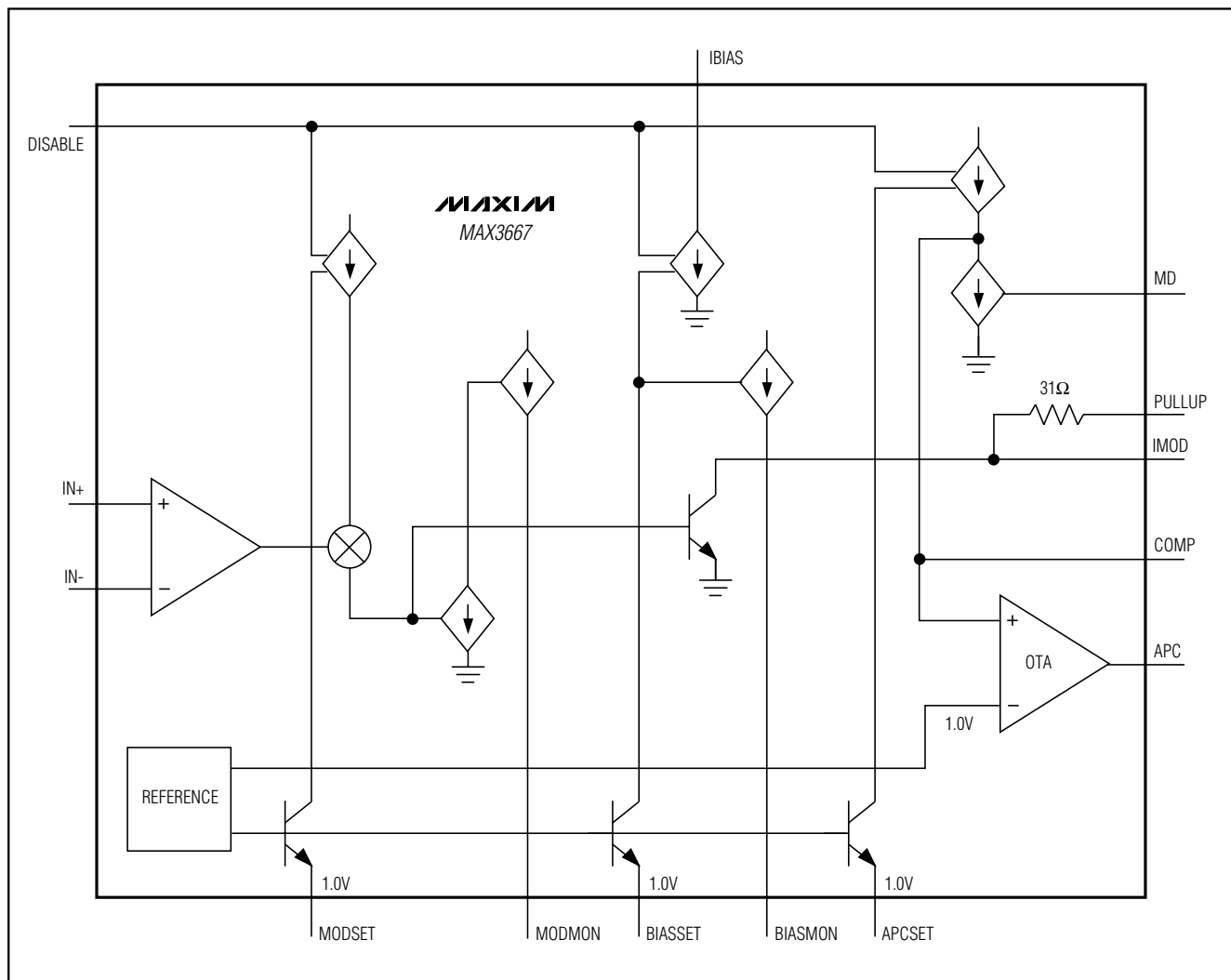


Figure 2. Block Diagram

Closed-loop operation requires the user to establish three internal currents with external resistors placed between ground and the BIASSET, MODSET, and APCSET pins. See the *Design Procedures* section for guidelines on selecting these resistor values.

### Open-Loop Operation

If desired, the MAX3667 is fully operational without the use of the APC loop. In these types of applications, the laser diode current is set solely by the external resistors connected to the BIASSET and MODSET pins. See the *Design Procedures* section for instructions on setting up the MAX3667 for open-loop operation.

### Disable Control

The MAX3667 provides a single-ended TTL-compatible disable control pin. The IBIAS, IMOD, and APCSET currents are disabled when the voltage on this pin is set high. However, the internal voltage reference and other sections of the MAX3667 remain active to ensure predictable operation and faster enable response times. The disable response time is approximately 25ns.

### Temperature Considerations

The MAX3667 contains a voltage reference that is fully temperature compensated. This reference is used throughout the circuit, as well as for programming the



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bias, modulation, and monitor diode current levels. Where necessary, the reference is adjusted by a  $V_{BE}$  voltage to cancel thermal errors created by the BIASSET, MODSET, and APCSET current mirrors. This ensures that the IBIAS and IMOD currents are nearly constant over temperature with open-loop operation. With the APC loop closed, this reference helps maintain a constant average MD current (and thus a constant average laser output power) over temperature.

### Bias and Modulation Current Monitors

The BIASMON and MODMON analog output monitors provide current levels that are directly proportional to the IBIAS and IMOD currents levels. These currents can be used in conjunction with other external circuitry to supervise the performance of the laser driver system without adding parasitics or reducing system performance. The gains associated with these pins, relative to IBIAS and IQMOD, are approximately 1/38 (for BIASMON) and approximately 1/33 (for MODMON).

In addition to a scaled copy of the modulation current, the MODMON current contains a DC offset current used internally to keep the driver transistors functioning at high speed, even with low modulation levels. This current is not precisely controlled and should be ignored when using the MODMON feature.

## Design Procedure

### Programming the Modulation Current

In addition to being a function of RMODSET, IMOD is also dependent on the values of the series damping resistor ( $R_{DAMP}$ ), the shunt compensation resistance ( $R_{FILT}$ ), and the resistance of the laser diode (Figure 1).

If  $I_{QMOD}$  represents the total current flowing into the collector of  $Q_{MOD}$ , then the modulation current into the laser diode can be represented by the following:

$$I_{MOD} = I_{QMOD} \left[ \frac{31\Omega \parallel R_{FILT}}{31\Omega \parallel R_{FILT} + (R_{DAMP} + r_{LASER})} \right]$$

$$I_{QMOD} = (A_1)(I_{MODSET})$$

$$A_1 = I_{MODSET} \text{ to } I_{MOD} \text{ Gain}$$

Assuming  $R_{FILT} = 22\Omega$ ,  $R_{DAMP} = 4.7\Omega$ , and  $r_{LASER} = 4\Omega$ , then this equation is simplified to:

$$I_{MOD} = I_{QMOD}(0.6)$$

For  $R_{DAMP} = 4.7\Omega$ ,  $R_{FILT} = 22\Omega$ , and a laser resistance of approximately  $4\Omega$ , see the IQMOD Current vs. RMODSET graph in the *Typical Operating Characteristics* and select the value of RMODSET that corresponds to the required current at +25°C.

### Programming the Bias Current (Open Loop)

When operating the MAX3667 without APC, program the bias-current output by adjusting the BIASSET resistor. To select this resistor, determine the desired bias current required at +25°C. Refer to the IBIAS Current vs. RBIASSET graph in the *Typical Operating Characteristics*, and select the value of RBIASSET that corresponds to the required current.

### Programming the Automatic Power Control (APC)

When using the MAX3667's APC feature, program the bias-current output by adjusting the APCSET resistor. To select this resistor, determine the desired monitor current to be maintained over temperature. See the MD Current vs. RAPCSET graph in the *Typical Operating Characteristics*, and select the value of RAPCSET that corresponds to the required current.

When using the APC feature, be sure to connect the APC pin directly to BIASSET (see the *Typical Operating Circuit*). In this mode, the bias-current output level is no longer controlled by the BIASSET resistor. The APCSET resistor is now controlling the output bias level. Under closed-loop conditions, RBIASSET assures that the feedback current range is properly centered. It is recommended that RBIASSET be chosen to equal RAPCSET during closed-loop operation.

### Pattern-Dependent Jitter

To reduce pattern-dependent jitter (PDJ) effects, two external compensation capacitors are required to ensure that the control loop responds slowly to changes in laser efficiency. The overall time constant of the APC loop is set by the value of these capacitors, by the transfer ratio between the laser diode current and the monitor diode current, and by the MAX3667's open-loop gain.

CCOMP must be placed between the COMP pin and ground; CAPC must be placed between the APC pin and ground (see the *Typical Operating Circuit*). For 622Mbps SDH/SONET applications, the recommended values of CCOMP and CAPC are 1 $\mu$ F and 1nF, respectively.

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Since the PDJ adjusts with changes in loop gain, it is important to choose capacitor values that are as large as is physically possible. Since each capacitor represents a different pole, for stability reasons,  $C_{APC}$  should be kept substantially smaller than  $C_{COMP}$ . It is recommended that the value of  $C_{APC}$  be set 1000 times smaller than  $C_{COMP}$ .

The time constant associated with the DC-blocking capacitor on IMOD can also have an effect on PDJ. It is important that this time constant produce minimum droop for long consecutive bit streams.

Referring to Figure 3, the droop resulting from long time periods without transitions can be represented by the following equation:

$$[100\% - \text{DROOP}] = e^{-\frac{t}{\tau}}$$

APC operation assures that the discharge level for  $\tau$  is  $P_{AVG}$ . An overall droop of 6% relative to  $P_{P-P}$  equates to a 12% droop relative to  $P_{AVG}$ . To ensure a droop of less than 12% (6% relative to  $P_{P-P}$ ), this equation can be solved for  $\tau$  as follows:

$$\tau = \frac{-t}{\ln[1 - 0.12]} = 7.8t$$

If  $t_1$  equals 100 consecutive unit intervals without a transition, then the time constant associated with the DC-blocking capacitor needs to be longer than:

$$\tau_{AC} \geq R_{ACCAC} = 7.8 (100 \text{ bits}) (1.6 \text{ ns/bit}) = 1.25 \mu\text{s}$$

The estimated value of  $R_{AC}$  is:

$$R_{AC} = 31\Omega \parallel R_{FILT} \parallel (R_{DAMP} + r_{LASER})$$

Assuming  $R_{FILT} = 22\Omega$ ,  $R_{DAMP} = 4.7\Omega$ , and  $r_{LASER} = 4\Omega$ :

$$R_{AC} = 5.2\Omega$$

with  $C_{AC} = 1\mu\text{F}$ ,  $\tau_{AC} = 5.2\mu\text{s}$ .

## Operation Without APC (Open Loop)

When operating without APC, be sure to configure the MAX3667 as follows:

- 1) Disconnect APC from BIASSET.
- 2) Force a voltage of 1V to 2V at APC to prevent the OTA from saturating.
- 3) Disconnect the monitor diode.
- 4) Pull up the MD pin to  $V_{CC}$  through a  $5\text{k}\Omega$  resistor.
- 5) Pull down the COMP pin to ground through a  $30\text{k}\Omega$  resistor.

Remember that the bias-current output is programmed by adjusting the BIASSET resistor when the APC loop is disconnected.

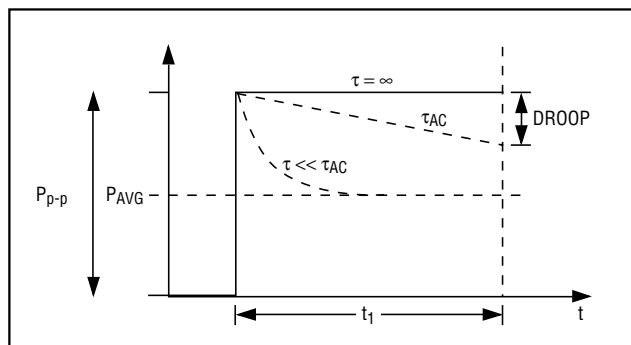


Figure 3. Droop

## Output Current Limits

The MAX3667 is equipped with output current limiting and short-circuit protection. In +3.3V operation,  $I_{BIAS}$  is limited to approximately 170mA open loop, and  $I_{QMOD}$  is limited to approximately 140mA (see *Typical Operating Characteristics*). In +5.0V operation,  $I_{BIAS}$  is limited to approximately 300mA, and  $I_{QMOD}$  is limited to approximately 140mA.

If BIASSET is shorted to ground,  $I_{BIAS}$  becomes current limited. If either APCSET or MODSET is shorted to ground, the MAX3667 output is turned off. Note that, in 5V operation, the  $I_{BIAS}$  current limit is approximately 300mA. Care should be taken if the MAX3667 is being used with a laser diode that is sensitive to this current level.

## Interface Suggestions and Laser Compensation

Adding damping resistance in series with the laser diode (typically  $3\Omega$  to  $5\Omega$ ) raises the load resistance, reduces the load frequency dependence, and improves output aberrations. A series damping resistor of  $4.7\Omega$  is suggested for the MAX3667.

Series inductance at the cathode of the laser results in high-frequency loading ( $V_L = L di/dt$ ) and increased output aberrations. Because of reduced headroom, the output performance of the transmitted eye diagram can be significantly impacted during 3.3V operation. Assuming that laser package series inductance can not be completely eliminated, a compensation network is required. With a laser diode load of approximately  $4\Omega$  and  $4\text{nH}$ , a series damping resistor of  $4.7\Omega$ , and a coupling capacitor of  $0.1\mu\text{F}$ , a shunt R-C compensation network of  $22\Omega$  and  $0.01\mu\text{F}$  is recommended (see *Typical Operating Circuit*). These values may need to be adjusted depending on the style of laser used. Note that it is important to place the compensation network as close to the load as possible.

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Since the IBIAS output is also connected directly to the laser cathode, any parasitic capacitance associated with this output must not be allowed to significantly load the response. To resolve this problem, place an R-L compensation network in series with the IBIAS output. The additional high-frequency impedance of this network helps maintain a high impedance at this node. The recommended values for this resistance and inductance are 100Ω and 470nH, respectively.

Optimize the laser diode performance by placing a bypass capacitor as close to the anode pin as possible. Use good high-frequency layout techniques and multilayer boards with uninterrupted ground planes.

### **Input Termination Requirements**

The MAX3667 data inputs are PECL compatible. Standard PECL levels require 50Ω terminations to  $V_{CC} - 2V$ . The MAX3667's common-mode input range is 1.5V to  $(V_{CC} - 0.75V)$  with a minimum differential input swing of 620mV<sub>P-P</sub>. The MAX3667's inputs need not be driven with standard PECL signals; as long as the common-mode voltage and differential swing is met, the device operates properly. 50Ω input termination is also not required, but is recommended for good high-frequency termination.

### **Wire Bonding**

For high-current density and reliable operation, the MAX3667 uses gold metalization. Make connections to the die with gold wire only, using ball-bonding techniques. Wedge bonding is not recommended. Die-pad size is 4 mils (100μm) square, and die thickness is 12 mils (300μm).

## **Applications Information**

### **DC-Coupled Operation and Output Current Limits**

To improve headroom conditions for the MAX3667, AC-coupling of the modulation current is required at +3.3V operation. At +5.0V operation, AC-coupling is suggested, but not required.

For AC-coupled operation, the total output current is equal to  $IBIAS + IMOD / 2$ . For DC-coupled modulation currents, the total output current is equal to  $IBIAS + IMOD$ .

### **Optimizing Performance for Low Modulation Currents**

The MAX3667's dynamic range and headroom requirements are such that, in order to meet these specifications, low-current performance is compromised. If continual operation at low modulation currents ( $\leq 20mA$ ) is the intended application, the MAX3667's high-frequency performance can be improved with an external pullup resistor. By shunting the AC current away from the laser diode, this technique reduces the output swing without reducing the operating current of the output transistor. Maintaining a higher modulation operating current level preserves the high-frequency performance of the output device. A suggested starting point for the external pullup resistor value is 100Ω.

### **Modulation Currents Greater than 60mA**

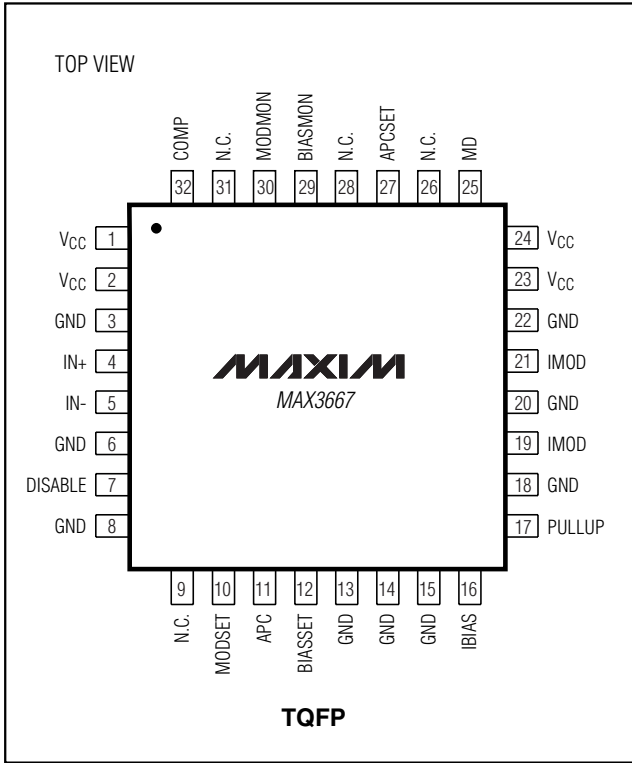
At +5.0V operation, the headroom conditions for the MAX3667 are improved significantly. In this mode, it is possible to achieve modulation currents greater than 60mA by floating PULLUP and driving the laser diode directly (DC-coupled IMOD).

### **Laser Safety and IEC 825**

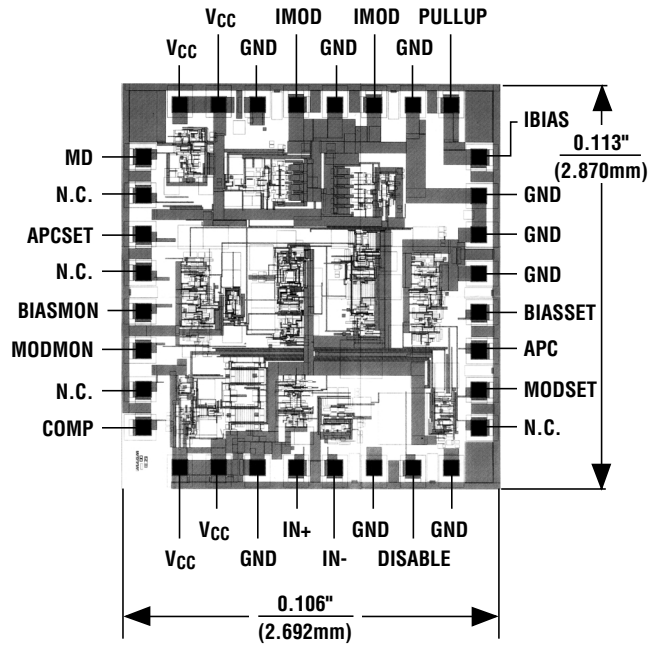
Using the MAX3667 laser driver alone does not ensure that a transmitter design is compliant with IEC 825. The entire transmitter circuit and component selections must be considered. Each customer must determine the level of fault tolerance required by their application, recognizing that Maxim products are not designed or authorized for use as components in systems intended for surgical implant into the body, for applications intended to support or sustain life, or for any other application where the failure of a Maxim product could create a situation where personal injury or death may occur.

# +3.3V, 622Mbps SDH/SONET Laser Driver with Automatic Power Control

## Pin Configuration



## Chip Topography



**MAX3667**

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## **Package Information**

(The package drawing(s) in this data sheet may not reflect the most current specifications. For the latest package outline information, go to [www.maxim-ic.com/packages](http://www.maxim-ic.com/packages).)

JEDEC VARIATION				
	BC		BE	
	32 LEAD		48 LEAD	
	MIN.	MAX.	MIN.	MAX.
A	---	1.60	---	1.60
A1	0.05	0.15	0.05	0.15
A2	1.35	1.45	1.35	1.45
D	8.90	9.10	8.90	9.10
D1	7.00	BSC.	7.00	BSC.
E	8.90	9.10	8.90	9.10
E1	7.00	BSC.	7.00	BSC.
e	0.8	BSC.	0.5	BSC.
L	0.45	0.75	0.45	0.75
b	0.30	0.45	0.17	0.27
c	0.09	0.20	0.09	0.20
α	0°	7°	0°	7°

**NOTES:**

1. ALL DIMENSIONING AND TOLERANCING CONFORM TO ANSI Y14.5-1982.
2. CONTROLLING DIMENSION: MILLIMETER.
3. THIS OUTLINE CONFORMS TO JEDEC PUBLICATION 95 REGISTRATION MO-136, VARIATIONS BC AND BE.
4. LEADS SHALL BE COPLANAR WITHIN .004 INCH.

<b>MAXIM</b>		
<small>PROPRIETARY INFORMATION</small>		
TITLE: PACKAGE OUTLINE, 32/48L, 7x7x1.4 MM TQFP		
APPROVAL	DOCUMENT CONTROL NO.	REV
	21-0054	D 1/1

32L/48L, TQFP-EP5

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