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Accuracy

Accuracy

Outputs

Circuitry

Wide Reference Current Dynamic Range

Guaranteed 250nA to 2.5mA with 5% Monitor

Extended 10nA to 10mA with 10% Monitor

Current (MAX4007) or Voltage (MAX4008) Monitor

Reference Current-Limit Protection (20mA, typ)

Voltage Clamp Protects Subsequent Output

♦ +2.7V to +76V Wide Voltage Range Operation

General Description

The MAX4007/MAX4008 precision, high-side, high-voltage current monitors are specifically designed for monitoring photodiode current in fiber applications. They offer a connection point for the reference current and a monitor output that produces a signal proportional to the reference current. The monitor output of the MAX4007 is a current proportional to the reference current. The monitor output of the MAX4008 is a voltage proportional to the reference current. The current monitors have six decades of dynamic range and monitor reference currents of 250nA to 2.5mA with better than 5% accuracy. The photodiode current can be monitored from 10nA to 10mA with reduced accuracy.

The MAX4007/MAX4008 accept a supply voltage of +2.7V to +76V, suitable for APD or PIN photodiode applications. Internal current limiting (20mA, typ) protects the devices against short circuit to ground. A clamp diode protects the monitor output from overvoltage. Additionally, these devices feature thermal shutdown if the die temperature reaches +150°C.

The MAX4007/MAX4008 are available in tiny, spacesaving 6-pin SOT23 packages, and operate over the extended temperature range of -40°C to +85°C.

_ Applications

Selector Guide

Photodiode Current-Monitoring Systems

Portable Instrumentation

Medical Instrumentation

Laboratory Instrumentation

Consumer Electronics

Current-to-Voltage Conversion

Level Translation

PARTPIN-
PACKAGEINTERNAL
RESISTORTYPICAL
ACCURACY (%)MAX4007EUT-T6 SOT23-6None5MAX4008EUT-T6 SOT23-610kΩ1

Typical Operating Circuit appears at end of data sheet.

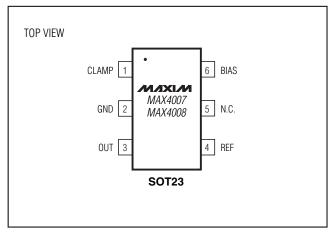
6-Pin SOT23 Packages

Ordering Information

PART	PIN-PACKAGE	TOP MARK	PKG CODE	
MAX4007EUT-T	6 SOT23-6	ABNM	U6S-3	
MAX4008EUT-T	6 SOT23-6	ABNO	U6S-3	

Note: All devices are specified over the -40°C to +85°C operating temperature range.

Pin Configuration



Maxim Integrated Products 1

For pricing, delivery, and ordering information, please contact Maxim/Dallas Direct! at 1-888-629-4642, or visit Maxim's website at www.maxim-ic.com.

Features

ABSOLUTE MAXIMUM RATINGS

CLAMP to GND	0.3V to +80V
BIAS, REF to GND	-0.3V to +80V
OUT to GND	0.3V to (V _{CLAMP} + 0.6V)
Short Circuit, REF to GND	Continuous
Current into Any Pin	±30mA
Continuous Power Dissipation ($T_A = +$	
6-Pin SOT23 (derate 8.7mW/°C abo	ove +70°C)696mW

Operating Temperature Range	40°C to +85°C
Junction Temperature	+150°C
Storage Temperature Range	65°C to +150°C
Lead Temperature (soldering, 10s)	

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS

 $(V_{BIAS} = 40V, GND = 0V, REF = open, V_{OUT} = 0V (MAX4007), CLAMP = open, T_A = -40^{\circ}C to +85^{\circ}C.$ Typical values are at T_A = +25^{\circ}C, unless otherwise noted.) (Note 1)

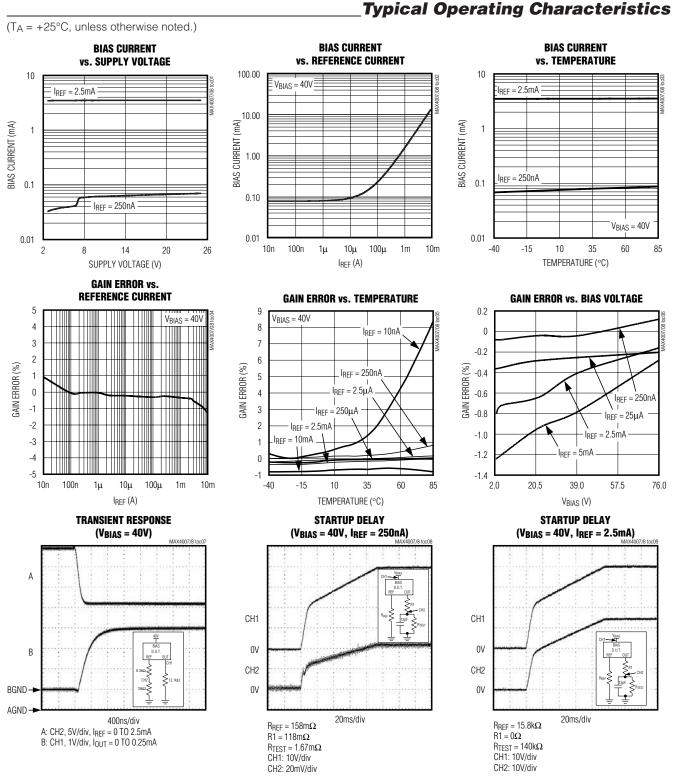
PARAMETER	SYMBOL	CONDITI	MIN	ТҮР	MAX	UNITS		
Bias Voltage Range	VBIAS	Inferred from power-supply rejection test		2.7		76	V	
Bias Quiescent Current	1214.0	I _{REF} = 250nA			90	200	μA	
Dias Quiescent Current	IBIAS	$I_{REF} = 2.5 mA$			3.6	4	mA	
Reference Voltage	VREF	$I_{REF} = 2.5 mA$		V _{BIAS} - 1.1	V _{BIAS} - 0.8		V	
Thermal Shutdown Engage Temperature	T _{SHDN}	REF shorted to GND, junction temperature rising		150			°C	
Trip-Point Temperature Hysteresis	T _{HYS}	REF shorted to GND, junction temperature falling		5			°C	
Input Current Limit	ILIM	V _{REF} = V _{BIAS} - 5V			20		mA	
	f 0.111-t-1	f _ 0 1Hz to 10Hz	$I_{REF} = 250 nA$		0.3			
MAX4007	in our	f = 0.1Hz to $10Hz$	$I_{REF} = 2.5 mA$		63		pA _{RMS}	
Output Current Noise	İNOUT	f = 0.1Hz to $10kHz$	$I_{REF} = 250 nA$		9.2			
			$I_{REF} = 2.5 mA$		1240			
		$f = 0.1Hz \text{ to } 10Hz \qquad \frac{I_{REF} = 250nA}{I_{REF} = 2.5mA}$		0.041				
MAX4008	eNOUT		$I_{REF} = 2.5 mA$		0.63		μV _{RMS}	
Output Voltage Noise		f = 0.1Hz to $10kHz$	$I_{REF} = 250 nA$		1.3			
			$I_{REF} = 2.5 mA$		12.5			
			MAX4007	$I_{REF} = 250nA$		50		GΩ
Output Resistance	Rout	101/ 001	$I_{REF} = 2.5 mA$	5		MΩ		
		MAX4008			10		kΩ	
Output Leakage		REF = open			1		рА	
Output Voltage Range	Vout	$\label{eq:bias} \begin{split} V_{BIAS} &= 2.7V \text{ to } 76V, \ I_{REF} = 0 \text{ to } 1\text{mA}, \\ MAX4007: \ \Delta I_{OUT}/I_{OUT} = \pm 1\%, \\ MAX4008: \ \Delta V_{OUT}/I_{OUT} = \pm 1\% \end{split}$		0 to V _{BIAS} - 1.5	0 to V _{BIAS} - 0.85		V	
Output Clamp Voltage	Vout - Vclamp				0.6		V	

ELECTRICAL CHARACTERISTICS (continued)

 $(V_{BIAS} = 40V, GND = 0V, REF = open, V_{OUT} = 0V (MAX4007), CLAMP = open, T_A = -40^{\circ}C \text{ to } +85^{\circ}C.$ Typical values are at T_A = +25°C, unless otherwise noted.) (Note 1)

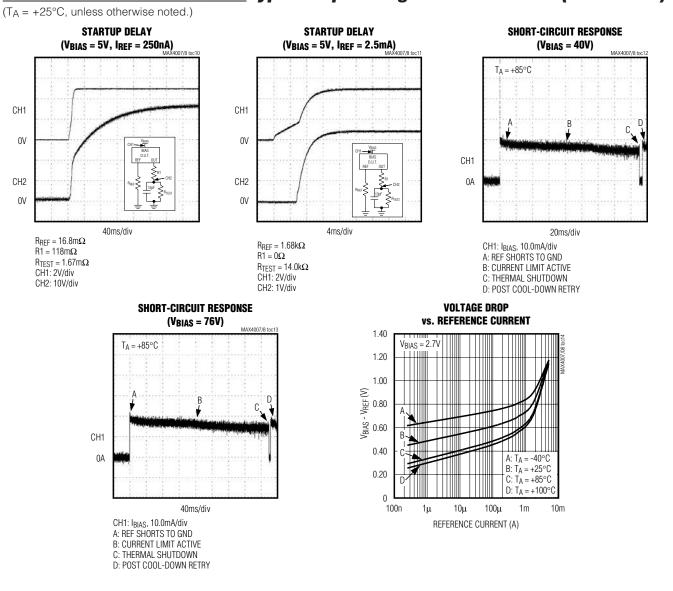
PARAMETER	SYMBOL	CONDITIONS		MIN	ТҮР	МАХ	UNITS	
Output Clamp Leakage		$V_{CLAMP} = 0$ to 76V			1		рА	
		I _{REF} = 250nA		0.09	0.0999	0.11	mA/mA	
MAX4007 Current Gain	IOUT/IREF	I _{REF} = 2.5mA		0.094	0.0998	0.106		
		I _{REF} = 10mA, V _{BIAS} = 12V			0.0997			
		I _{REF} = 250nA		0.9	0.992	1.1		
MAX4008 Transimpedance Gain	VOUT/IREF	I _{REF} = 2.5mA		0.95	0.998	1.05	V/mA	
		I _{REF} = 10mA, V _{BIAS} = 12V			1.00			
Power-Supply Rejection Ratio (PSRR)	$(\Delta I_{OUT}/I_{OUT})$	MAX4007:	$I_{REF} = 250 nA$		50 1000			
	$/\Delta V_{BIAS}$	$V_{BIAS} = 2.7V$ to 76V	I _{REF} = 1mA		50	1000	10 10 100 Å (
	$(\Delta V_{OUT}/V_{OUT})$ $/\Delta V_{BIAS}$	MAX4008: V _{BIAS} = 2.7V to 76V	I _{REF} = 1mA		50	1000	- ppm/V	
Power-Up Settling Time	C	I_{OUT} settles within 0.1%, $C_{IN} = 10$ nF			7.5		ms	
	ts	between REF and GND	I _{REF} = 2.5mA		90		μs	

Note 1: All devices are 100% tested at room temperature ($T_A = +25^{\circ}C$). All temperature limits are guaranteed by design.



MAX4007/MAX4008

Typical Operating Characteristics (continued)

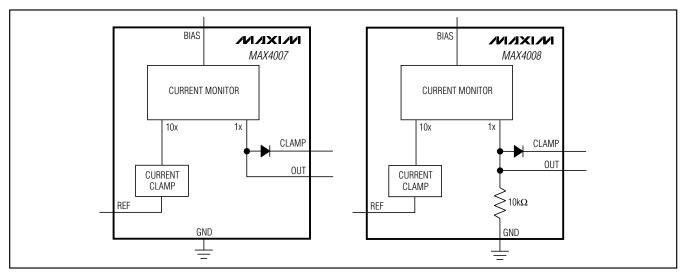


MAX4007/MAX4008

_Pin Description

F	PIN		FUNCTION	
MAX4007	MAX4008	NAME	FUNCTION	
1	1	CLAMP	Clamp Voltage Input. External potential used for voltage clamping of V_{OUT} .	
2	2	GND	Ground	
3	—	OUT	Current-Monitor Output. OUT sources a current of 1/10th IREF.	
4	4	REF	Reference Current Output. REF provides the source current to the cathode of the photodiode.	
5	5	N.C.	No Connection. Not internally connected.	
6	6	BIAS	Bias Voltage Input. Bias voltage for photodiode.	
_	3	OUT	Current-Monitor Output. OUT presents a voltage proportional to I_{REF} at 1V/mA. An internal 10k Ω resistor connects OUT to GND (see <i>Functional Diagram</i>).	

Functional Diagrams



Detailed Description

The MAX4007/MAX4008 are versatile current monitors intended for monitoring DC photodiode current in fiber applications (see *Functional Diagram*). The MAX4007 output is a current that is exactly one-tenth the reference current. The MAX4008 outputs a voltage that is proportional to the reference current with a transimpedance gain of 1V/mA achieved by a factory-trimmed, internal 10k Ω resistor.

Both current devices have six decades of dynamic range and monitor reference current ranging from 250nA to 2.5mA, the nominal operating range, with better than 5% accuracy across the entire reference current, bias voltage, and temperature ranges. The corresponding monitor outputs produce 25nA to 0.25mA (MAX4007) and 0.25mV to 2.5V (MAX4008). When the reference currents are extended to the broader range of 10nA to 10mA, an accuracy of less than 10% is maintained. Internal current limiting (20mA,



typ) protects the device against short-circuit-to-ground conditions, and a thermal shutdown feature reduces both the reference current and the monitor current to zero if the die temperature reaches +150°C.

The MAX4007/MAX4008 accept a supply voltage of +2.7V to +76V, suitable for APD photodiode applications. A clamping diode, shown in the *Functional Diagram*, is provided to protect subsequent output circuitry from an overvoltage condition.

_Applications Information

Clamping the Monitor Output Voltage

CLAMP provides a means for diode clamping the voltage at OUT; thus, V_{OUT} is limited to V_{CLAMP} + 0.6V. CLAMP can be connected to either an external supply, to BIAS, or may be left floating if voltage clamping is not required.

Using APD or PIN Photodiodes in Fiber Applications

When using the MAX4007/MAX4008 to monitor APD or PIN photodiode currents in fiber applications, several issues must be addressed. In applications where the photodiode must be fully depleted, keep track of voltages budgeted for each component with respect to the available supply voltage(s). The current monitors require as much as 1.1V between BIAS and REF, which must be considered part of the overall voltage budget.

Additional voltage margin can be created if a negative supply is used in place of a ground connection, as long as the overall voltage drop experienced by the MAX4007/MAX4008 is less than or equal to 76V. For this type of application, the MAX4007 is suggested so the output can be referenced to "true" ground and not the negative supply. The MAX4007's output current can be referenced as desired with either a resistor to ground or a transimpedance amplifier. Take care to ensure that output voltage excursions do not interfere with the required margin between BIAS and OUT. In many fiber applications, OUT is connected directly to an ADC that operates from a supply voltage that is less than the voltage at BIAS. Connecting the MAX4007/MAX4008s' clamping diode output, CLAMP, to the ADC power supply helps avoid damage to the ADC. Without this protection, voltages can develop at OUT that might destroy the ADC. This protection is less critical when OUT is connected directly to subsequent transimpedance amplifiers (linear or logarithmic) that have low-impedance, near-ground-referenced inputs. If a transimpedance amp is used on the low side of the photodiode, its voltage drop must also be considered. Leakage from the clamping diode is most often insignificant over nominal operating conditions, but grows with temperature.

To maintain low levels of wideband noise, lowpass filtering the output signal is suggested in applications where only DC measurements are required. Determining the required filtering components is straightforward, as the MAX4007 exhibits a very high output impedance (>5M Ω), while the MAX4008 exhibits an output resistance of 10k Ω .

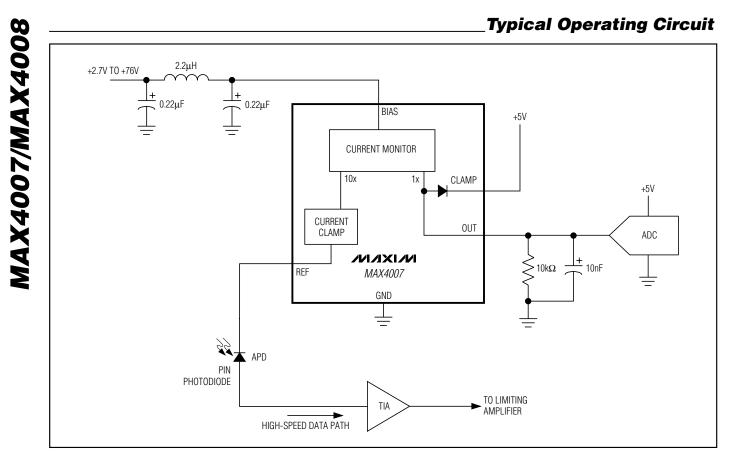
In some applications where pilot tones are used to identify specific fiber channels, higher bandwidths are desired at OUT to detect these tones. Consider the minimum and maximum currents to be detected, then consult the frequency response and noise typical operating curves. If the minimum current is too small, insufficient bandwidth could result, while too high a current could result in excessive noise across the desired bandwidth.

Bypassing and External Components

In applications where power-supply noise can interfere with DC diode measurements, additional filtering is suggested. Such noise is commonly seen when switching power supplies are used to generate the photodiode bias voltage. As shown in the *Typical Operating Circuit*, a pi filter (two 0.22µF capacitors and one 2.2µH inductor) greatly suppresses power-supply switching noise. If such a filter is already present in the bias generating circuit, only a simple bypass capacitor at the BIAS pin is suggested. The output lowpass filter, a 10k Ω resistor and a 10nF capacitor, further reduce permeating power-supply noise, as well as other wideband noise that might otherwise restrict measurements at low-signal levels. Again, reducing the bandwidth of the OUT signal can affect performance of pilot-tone systems.

To restrict high-frequency photodiode signals from affecting the current monitors and BIAS power supply, an RF choke and 10nF capacitor can be added. The capacitance presented to REF should not exceed 10nF; larger values increase startup time and could cause the thermal shutdown circuit to activate during startup.





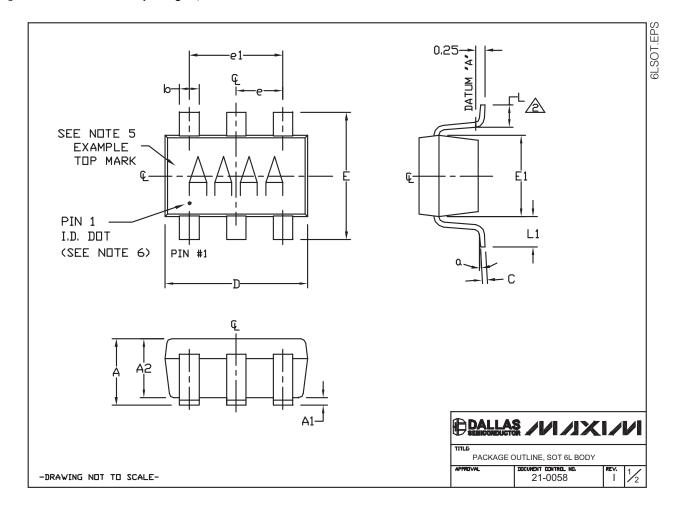
Chip Information

TRANSISTOR COUNT: 195 PROCESS: BICMOS

8

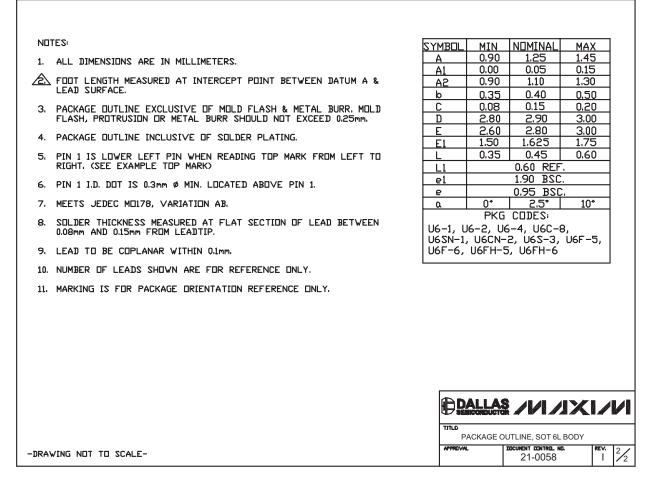
_Package Information

(The package drawing(s) in this data sheet may not reflect the most current specifications. For the latest package outline information, go to **www.maxim-ic.com/packages**.)



_Package Information (continued)

(The package drawing(s) in this data sheet may not reflect the most current specifications. For the latest package outline information go to **www.maxim-ic.com/packages**.)



Revision History

Pages changed at Rev 3: 1, 3, 10

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MAX4007/MAX4008

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