Chipsmall

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from,Europe,America and south Asia,supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts,Customers Priority,Honest Operation,and Considerate Service",our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip,ALPS,ROHM,Xilinx,Pulse,ON,Everlight and Freescale. Main products comprise IC,Modules,Potentiometer,IC Socket,Relay,Connector.Our parts cover such applications as commercial,industrial, and automotives areas.

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MAXIM

0.8Ω, Low-Voltage, Single-Supply Dual SPST Analog Switches

General Description

The MAX4741/MAX4742/MAX4743 are low on-resistance, low-voltage, dual single-pole/single-throw (SPST) analog switches that operate from a single +1.6V to +3.6V supply. These devices have fast switching speeds (t_{ON} = 24ns, t_{OFF} = 16ns max), handle rail-to-rail analog signals, and consume less than 1µW of quiescent power. The MAX4743 has break-before-make switching.

When powered from a +3V supply, the MAX4741/ MAX4742/MAX4743 feature low 0.8Ω (max) on-resistance (R_{ON}), with 0.08Ω (max) R_{ON} matching and 0.18Ω R_{ON} flatness. The digital logic input is 1.8V CMOS compatible when using a single +3V supply.

The MAX4741 has two normally open (NO) switches, the MAX4742 has two normally closed (NC) switches, and the MAX4743 has one NO switch and one NC switch. The MAX4741 is available in 8-pin µDFN (2mm x 2mm), 8-pin SOT23, and 8-pin µMAX® packages. The MAX4742/MAX4743 are available in 8-pin SOT23 and 8-pin µMAX packages.

Applications

Power Routing

Battery Powered Systems

Audio and Video Signal Routing

Low-Voltage Data-Acquisition Systems

Communications Circuits

PCMCIA Cards

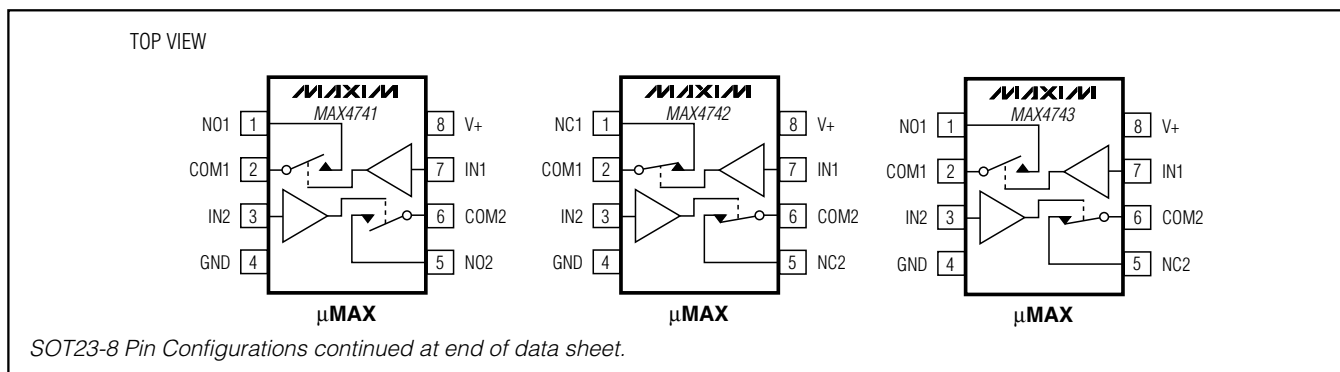Cellular Phones

Modems

Hard Drives

Features

♦ **Low R_{ON}:**
    0.8Ω max (+3V Supply)
    2.5Ω max (+1.8V Supply)

♦ **0.18Ω max R_{ON} Flatness (+3V Supply)**

♦ **+1.6V to +3.6V Single-Supply Operation**

♦ **Available in SOT23 and µMAX Packages**

♦ **High-Current Handling Capacity (150mA continuous)**

♦ **1.8V CMOS Logic Compatible (+3V Supply)**

♦ **Fast Switching: t_{ON} = 24ns, t_{OFF} = 16ns**

Ordering Information

PART	PIN-PACKAGE	TOP MARK	PACKAGE CODE
MAX4741EKA	8 SOT23-8	AAIY	K8S-3
MAX4741EUA	8 µMAX	—	U8-1
MAX4741ELA	8 µDFN	+AAV	L822-1
MAX4742EKA	8 SOT23-8	AAIZ	K8S-3
MAX4742EUA	8 µMAX	—	U8-1
MAX4743EKA	8 SOT23-8	AAJA	K8S-3
MAX4743EUA	8 µMAX	—	U8-1

Note: *All devices are specified over the -40°C to +85°C operating temperature range.*

Pin Configurations

µMAX is a registered trademark of Maxim Integrated Products, Inc.

MAXIM _____ **Maxim Integrated Products** 1

For pricing, delivery, and ordering information, please contact Maxim/Dallas Direct! at 1-888-629-4642, or visit Maxim's website at www.maxim-ic.com.

0.8Ω, Low-Voltage, Single-Supply Dual SPST Analog Switches

ABSOLUTE MAXIMUM RATINGS

Voltages Referenced to GND
V+, IN_...-0.3V to +4V
COM_, NO_, NC_ (Note 1) ....-0.3V to (V+ + 0.3V)
Continuous Current COM_ , NO_, NC_ ....±150mA
Peak Current COM_, NO_, NC_
   (pulsed at 1ms 10% duty cycle)................................±300mA
Continuous Power Dissipation (T$_A$ = +70°C)
   8-Pin SOT23 (derate 7.52mW/°C above +70°C)...........602mW
   8-Pin µMAX (derate 4.5mW/°C above +70°C)..............362mW

8-Pin µDFN (derate 4.8mW/°C above +70°C) 381mW
Operating Temperature Range ....-40°C to +85°C
Maximum Junction Temperature ....+150°C
Storage Temperature Range ....-65°C to +150°C
Lead Temperature (soldering, 10s) ....+300°C

Note 1: Signals on COM_, NO_, or NC_ exceeding V+ or GND are clamped by internal diodes. Limit forward current to
                maximum current rating.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS—Single +3V Supply

(V+ = +2.7V to +3.6V, V$_{IH}$ = +1.4V, V$_{IL}$ = +0.5V, T$_A$ = T$_{MIN}$ to T$_{MAX}$, unless otherwise specified. Typical values are at V+ = +3.0V, T$_A$ = +25°C.) (Notes 2, 3)

PARAMETER	SYMBOL	CONDITIONS	T$_A$	MIN	TYP	MAX	UNITS
ANALOG SWITCH							
Analog Signal Range	V$_{COM\_}$, V$_{NO\_}$, V$_{NC\_}$			0		V+	V
On-Resistance	R$_{ON}$	V+ = 2.7V, I$_{COM\_}$ = 100mA, V$_{NO\_}$ or V$_{NC\_}$ = 1.5V	+25°C		0.5	0.8	Ω
			T$_{MIN}$ to T$_{MAX}$			0.9	
On-Resistance Match Between Channels (Note 4)	ΔR$_{ON}$	V+ = 2.7V, I$_{COM\_}$ = 100mA, V$_{NO\_}$ or V$_{NC\_}$ = 1.5V	+25°C		0.05	0.08	Ω
			T$_{MIN}$ to T$_{MAX}$			0.09	
On-Resistance Flatness (Note 5)	R$_{FLAT(ON)}$	V+ = 2.7V, I$_{COM\_}$ = 100mA, V$_{NO\_}$ or V$_{NC\_}$ = 1V, 1.5V, 2V	+25°C		0.05	0.18	Ω
			T$_{MIN}$ to T$_{MAX}$			0.20	
NO_ or NC_ Off-Leakage Current	I$_{NO\_(OFF)}$, I$_{NC\_(OFF)}$	V+ = 3.3V, V$_{COM\_}$ = 0.3V, 3V, V$_{NO\_}$ or V$_{NC\_}$ = 3V, 0.3V	+25°C	-1		1	nA
			T$_{MIN}$ to T$_{MAX}$	-5		5	
COM_ Off-Leakage Current	I$_{COM\_(OFF)}$	V+ = 3.3V, V$_{COM\_}$ = 0.3V, 3V V$_{NO\_}$ or V$_{NC\_}$ = 3V, 0.3V or floating	+25°C	-1		1	nA
			T$_{MIN}$ to T$_{MAX}$	-5		5	
COM_ On-Leakage Current	I$_{COM\_(ON)}$	V+ = 3.3V, V$_{COM\_}$ = 3V, 0.3V; V$_{NO\_}$ or V$_{NC\_}$ = 3V, 0.3V or floating	+25°C	-2		2	nA
			T$_{MIN}$ to T$_{MAX}$	-10		10	

MAXIM

ELECTRICAL CHARACTERISTICS—Single +3V Supply (continued)

(V+ = +2.7V to +3.6V, V_{IH} = +1.4V, V_{IL} = +0.5V, T_A = T_{MIN} to T_{MAX}, unless otherwise specified. Typical values are at V+ = +3.0V, T_A = +25°C.) (Notes 2, 3)

PARAMETER	SYMBOL	CONDITIONS	T_A	MIN	TYP	MAX	UNITS
SWITCH DYNAMIC CHARACTERISTICS							
Turn-On Time	t_{ON}	$V_{NO\_}$, $V_{NC\_}$ = 1.5V, R_L = 50Ω, C_L = 35pF, Figure 1	+25°C		18	24	ns
			T_{MIN} to T_{MAX}			28	
Turn-Off Time	t_{OFF}	$V_{NO\_}$, $V_{NC\_}$ = 1.5V, R_L = 50Ω, C_L = 35pF, Figure 1	+25°C		12	16	ns
			T_{MIN} to T_{MAX}			18	
Break-Before-Make (Note 6)	t_{BBM}	$V_{NO\_}$, $V_{NC\_}$ = 1.5V, R_L = 50Ω, C_L = 35pF, Figure 1 (MAX4743)	+25°C		6		ns
			T_{MIN} to T_{MAX}	1			
Charge Injection	Q	V_{GEN} = 0, R_{GEN} = 0, C_L = 1.0nF, Figure 3	+25°C		28		pC
NO_ or NC_ Off-Capacitance	C_{OFF}	f = 1MHz, Figure 4	+25°C		32		pF
COM_ Off-Capacitance	$C_{COM\_(OFF)}$	f = 1MHz, Figure 4	+25°C		32		pF
COM_ On-Capacitance	$C_{COM\_(ON)}$	f = 1MHz, Figure 4	+25°C		44		pF
-3dB On-Channel Bandwidth	BW	Signal = 0, R_{IN} = R_{OUT} = 50Ω, C_L = 5pF, Figure 2			100		MHz
Off-Isolation (Note 7)	V_{ISO}	f = 1MHz, $V_{COM\_}$ = 1V_{RMS}, R_L = 50Ω, C_L = 5pF, Figure 2	+25°C		-55		dB
Crosstalk (Note 8)		f = 1MHz, $V_{COM\_}$ = 1V_{RMS}, R_L = 50Ω, C_L = 5pF, Figure 2	+25°C		-110		dB
Total Harmonic Distortion	THD	f = 20Hz to 20kHz, $V_{COM\_}$ = 2Vp-p, R_L = 32Ω	+25°C		0.02		%
LOGIC INPUT							
Input Logic High	V_{IH}			1.4			V
Input Logic Low	V_{IL}					0.5	V
Input Leakage Current	I_{IN}	$V_{IN\_}$ = 0 or V+		-1	0.005	1	µA
POWER SUPPLY							
Power-Supply Range	V+			1.6		3.6	V
Positive Supply Current	I+	V+ = 3.6V, $V_{IN\_}$ = 0 or V+, all channels on or off	+25°C			0.2	µA

MAX4741/MAX4742/MAX4743

ELECTRICAL CHARACTERISTICS—Single +1.8V Supply

(V+ = +1.8V, V_{IH} = +1.0V, V_{IL} = 0.4V, T_A = T_{MIN} to T_{MAX}, unless otherwise specified. Typical values are at T_A = +25°C.) (Notes 2, 3)

PARAMETER	SYMBOL	CONDITIONS	T_A	MIN	TYP	MAX	UNITS
ANALOG SWITCH							
Analog Signal Range	$V_{COM\_}$, $V_{NO\_}$, $V_{NC\_}$			0		V+	V
On-Resistance	R_{ON}	$I_{COM\_}$ = 10mA, $V_{NO\_}$ or $V_{NC\_}$ = 0.9V	+25°C		1.3	2.5	Ω
			T_{MIN} to T_{MAX}			5	
NO_ or NC_ Off-Leakage Current	$I_{NO\_(OFF)}$, $I_{NC\_(OFF)}$	$V_{COM\_}$ = 0.3V, 1.5V; $V_{NO\_}$ or $V_{NC\_}$ = 1.5V, 0.3V	+25°C	-1		1	nA
			T_{MIN} to T_{MAX}	-5		5	
COM_ Off-Leakage Current	$I_{COM\_(OFF)}$	$V_{COM\_}$ = 0.3V, 1.5V; $V_{NO\_}$ or $V_{NC\_}$ = 1.5V, 0.3V	+25°C	-1		1	nA
			T_{MIN} to T_{MAX}	-5		5	
COM_ On-Leakage Current	$I_{COM\_(ON)}$	$V_{COM\_}$ = 0.3V, 1.5V, $V_{NO\_}$ or $V_{NC\_}$ = 0.3V, 1.5V, or floating	+25°C	-2		2	nA
			T_{MIN} to T_{MAX}	-10		10	
SWITCH DYNAMIC CHARACTERISTICS							
Turn-On Time	t_{ON}	$V_{NO\_}$, $V_{NC\_}$ = 1.5V, R_L = 50Ω, C_L = 35pF, Figure 1	+25°C		25	35	ns
			T_{MIN} to T_{MAX}			40	
Turn-Off Time	t_{OFF}	$V_{NO\_}$, $V_{NC\_}$ = 1.5V, R_L = 50Ω, C_L = 35pF, Figure 1	+25°C		16	25	ns
			T_{MIN} to T_{MAX}			30	
Break-Before-Make (Note 6)	t_{BBM}	$V_{NO\_}$, $V_{NC\_}$ = 1.5V, R_L = 50Ω, C_L = 35pF, Figure 1 (MAX4743)	+25°C		10		ns
			T_{MIN} to T_{MAX}	1			
Charge Injection	Q	V_{GEN} = 0, R_{GEN} = 0, C_L = 1nF, Figure 2	+25°C		16		pC
Off-Isolation (Note 7)	V_{ISO}	f = 1MHz, $V_{NO\_}$ = $V_{NC\_}$ = 1V_{RMS}, R_L = 50Ω, C_L = 5pF, Figure 2	+25°C		-50		dB
Crosstalk (Note 8)		f = 1MHz, $V_{COM\_}$ = 1V_{RMS}, R_L = 50Ω, C_L = 5pF, Figure 2	+25°C		-110		dB

ELECTRICAL CHARACTERISTICS—Single +1.8V Supply (continued)

(V+ = +1.8V, V_{IH} = +1.0V, V_{IL} = 0.4V, T_A = T_{MIN} to T_{MAX}, unless otherwise specified. Typical values are at T_A = +25°C.) (Notes 2, 3)

PARAMETER	SYMBOL	CONDITIONS	T_A	MIN	TYP	MAX	UNITS
LOGIC INPUT							
Input Logic High	V_{IH}			1			V
Input Logic Low	V_{IL}					0.4	V
Input Leakage Current	I_{IN}	$V_{IN\_}$ = 0 or V+		-1		1	µA

Note 2: The algebraic convention, where the most negative value is a minimum and the most positive value is a maximum, is used in this data sheet.

Note 3: µDFN and SOT23 packaged parts are 100% tested at +25°C. Limits across the full temperature range are guaranteed by design and correlation. µMAX packaged parts -40°C specifications are guaranteed by design.

Note 4: ΔR_{ON} = $R_{ON(MAX)}$ - $R_{ON(MIN)}$.

Note 5: Flatness is defined as the difference between the maximum and the minimum value of on-resistance as measured over the specified analog signal ranges.
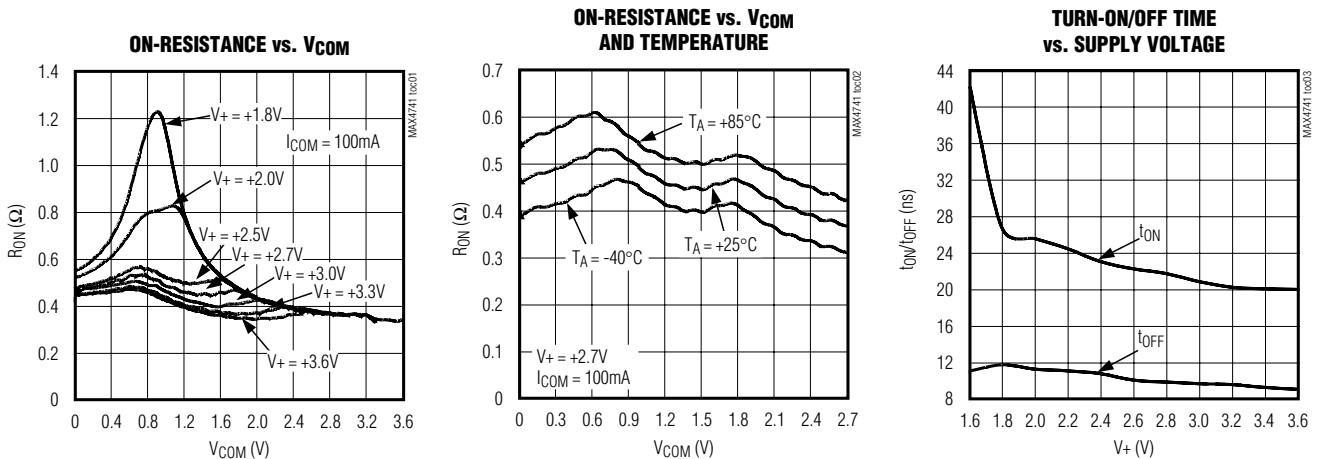
Note 6: Guaranteed by design.

Note 7: Off-Isolation = $20\log_{10}(V_{COM\_}/V_{NO\_})$, $V_{COM\_}$ = output, $V_{NO\_}$ = input to off switch.

Note 8: Between two switches.
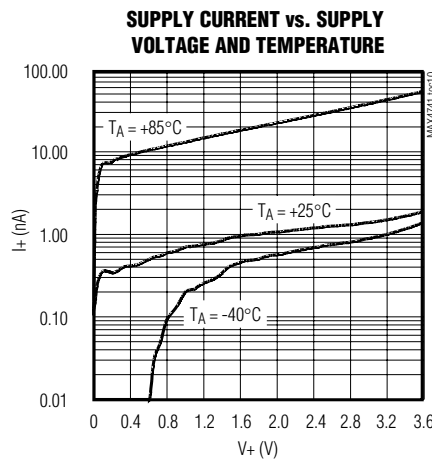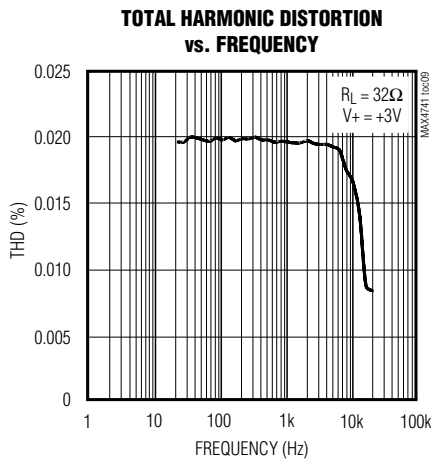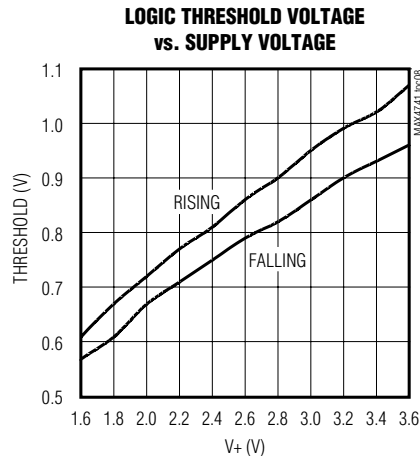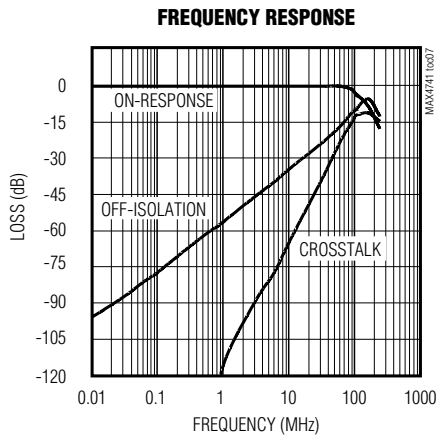
_____*Typical Operating Characteristics*

(T_A = +25°C, unless otherwise noted.)

ON-RESISTANCE vs. V_{COM}

ON-RESISTANCE vs. V_{COM} AND TEMPERATURE

TURN-ON/OFF TIME vs. SUPPLY VOLTAGE

MAX4741/MAX4742/MAX4743

_____Typical Operating Characteristics (continued)

(T$_A$ = +25°C, unless otherwise noted.)
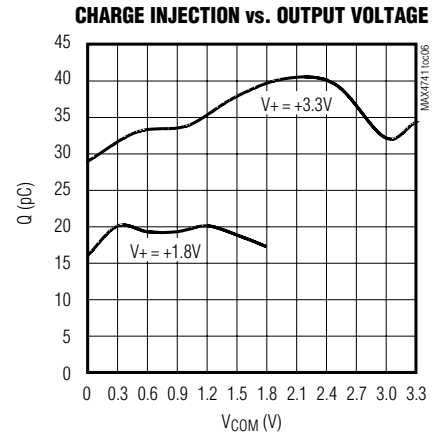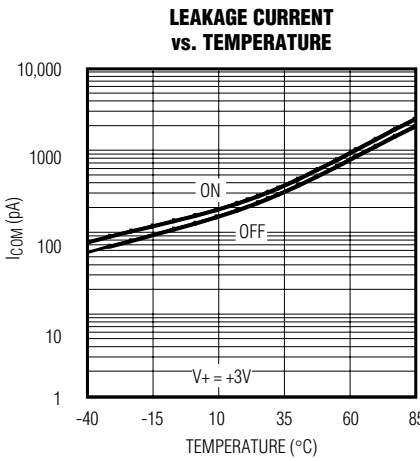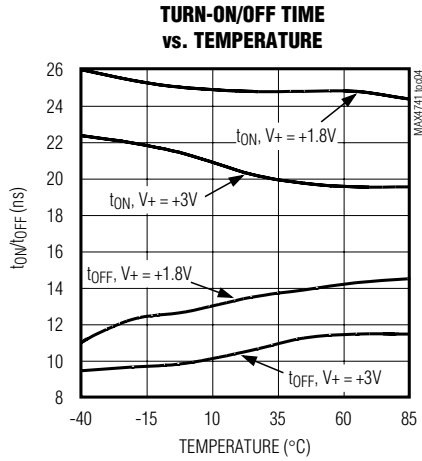
TURN-ON/OFF TIME vs. TEMPERATURE

t$_{ON}$, V+ = +1.8V
t$_{ON}$, V+ = +3V
t$_{OFF}$, V+ = +1.8V
t$_{OFF}$, V+ = +3V

LEAKAGE CURRENT vs. TEMPERATURE

ON
OFF
V+ = +3V

CHARGE INJECTION vs. OUTPUT VOLTAGE

V+ = +3.3V
V+ = +1.8V

FREQUENCY RESPONSE

ON-RESPONSE
OFF-ISOLATION
CROSSTALK

LOGIC THRESHOLD VOLTAGE vs. SUPPLY VOLTAGE

RISING
FALLING

TOTAL HARMONIC DISTORTION vs. FREQUENCY

R$_L$ = 32Ω
V+ = +3V

SUPPLY CURRENT vs. SUPPLY VOLTAGE AND TEMPERATURE

T$_A$ = +85°C
T$_A$ = +25°C
T$_A$ = -40°C

PIN						NAME	FUNCTION
MAX4741		MAX4742		MAX4743			
µMAX/µDFN	SOT23-8	µMAX	SOT23-8	µMAX	SOT23-8		
1	8	—	—	1	8	NO1	Analog Switch 1 Normally Open
—	—	1	8	—	—	NC1	Analog Switch 1 Normally Closed
2	7	2	7	2	7	COM1	Analog Switch 1 Common
3	6	3	6	3	6	IN2	Logic Control Input Switch 2
4	5	4	5	4	5	GND	Ground
5	3	—	—	—	—	NO2	Analog Switch 2 Normally Open
—	—	5	3	5	3	NC2	Analog Switch 2 Normally Closed
6	4	6	4	6	4	COM2	Analog Switch 2 Common
7	1	7	1	7	1	IN1	Logic Control Input Switch 1
8	2	8	2	8	2	V+	Positive Supply Voltage

_____Detailed Description

The MAX4741/MAX4742/MAX4743 are low 0.8Ω max (at V+ = +3V) on-resistance, low-voltage, dual analog switches that operate from a +1.6V to +3.6V single supply. CMOS switch construction allows switching analog signals that are within the supply voltage range (GND to V+).

When powered from a +3V supply, the 0.8Ω max R_{ON} allows high continuous currents to be switched in a variety of applications.

_____Applications Information

Proper power-supply sequencing is recommended for all CMOS devices. Do not exceed the absolute maximum ratings, because stresses beyond the listed ratings can cause permanent damage to the devices. Always sequence V+ on first, followed by NO_, NC_, or COM_.

Although it is not required, power-supply bypassing improves noise margin and prevents switching noise propagation from the V+ supply to other components. A 0.1µF capacitor, connected from V+ to GND, is adequate for most applications.

Logic Inputs

The MAX4741/MAX4742/MAX4743 logic inputs can be driven up to +3.6V regardless of the supply voltage. For example, with a +1.8V supply, IN_ may be driven low to GND and high to +3.6V. Driving IN_ rail-to-rail minimizes power consumption.

Analog Signal Levels

Analog signals that range over the entire supply voltage (V+ to GND) can be passed with very little change in on-resistance (see *Typical Operating Characteristics*). The switches are bidirectional, so the NO_, NC_, and COM_ pins can be used as either inputs or outputs.

Layout

High-speed switches require proper layout and design procedures for optimum performance. Reduce stray inductance and capacitance by keeping traces short and wide. Ensure that bypass capacitors are as close to the device as possible. Use large ground planes where possible.

MAX4741/MAX4742/MAX4743
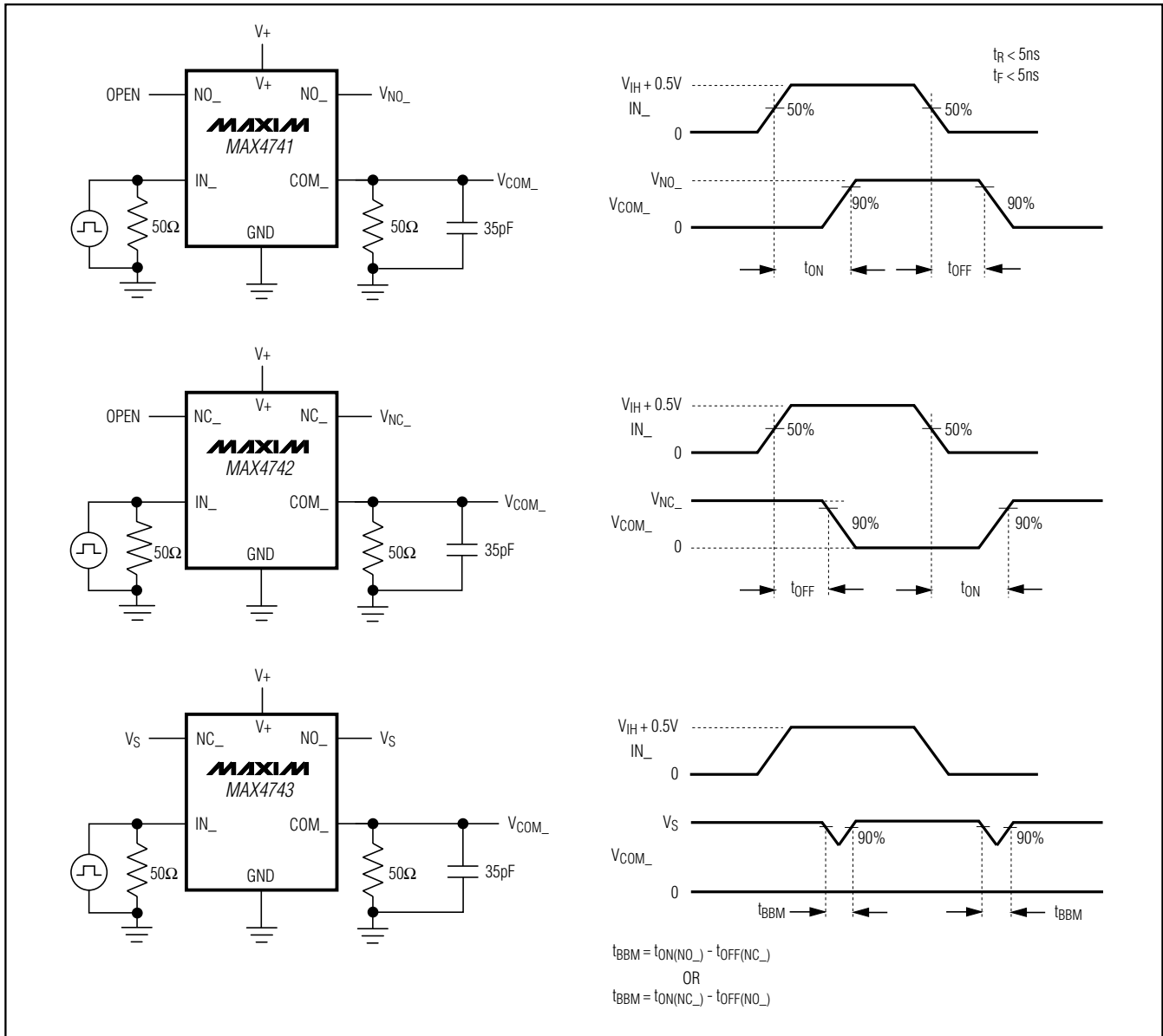
Figure 1. Switching Times

$$t_{BBM} = t_{ON(NO\_)} - t_{OFF(NC\_)}$$
$$OR$$
$$t_{BBM} = t_{ON(NC\_)} - t_{OFF(NO\_)}$$

MAXIM
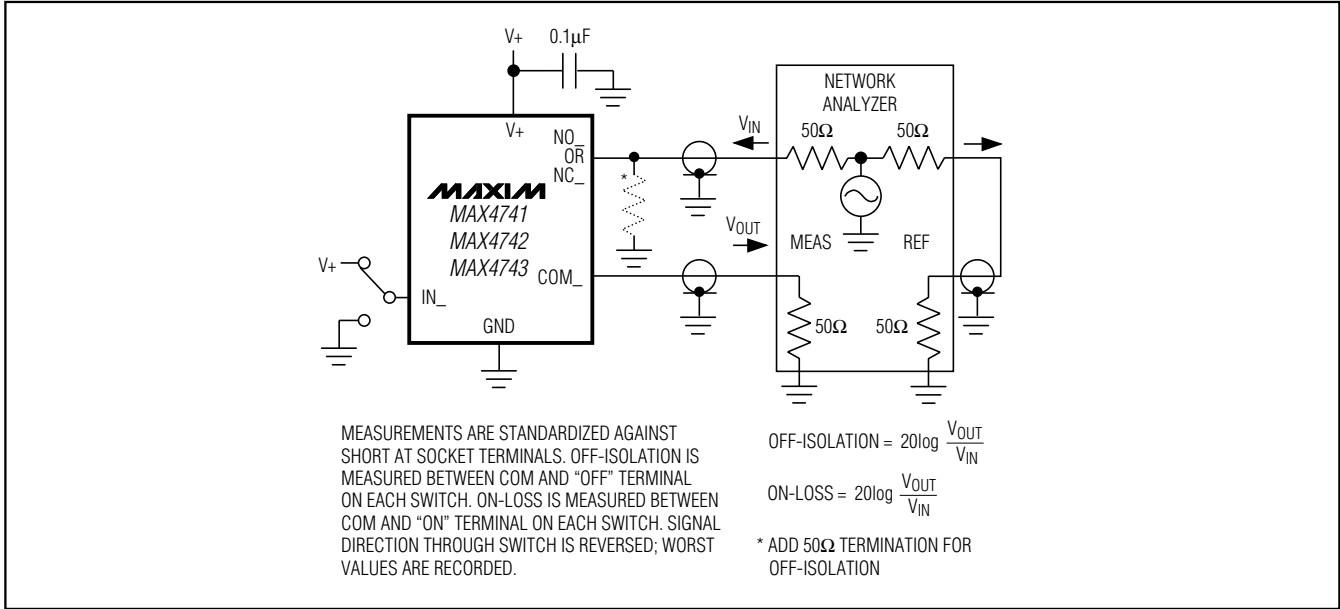
Figure 2. Off-Isolation, On-Loss, and Crosstalk

MEASUREMENTS ARE STANDARDIZED AGAINST SHORT AT SOCKET TERMINALS. OFF-ISOLATION IS MEASURED BETWEEN COM AND "OFF" TERMINAL ON EACH SWITCH. ON-LOSS IS MEASURED BETWEEN COM AND "ON" TERMINAL ON EACH SWITCH. SIGNAL DIRECTION THROUGH SWITCH IS REVERSED; WORST VALUES ARE RECORDED.

$$\text{OFF-ISOLATION} = 20 \log \frac{V_{OUT}}{V_{IN}}$$

$$\text{ON-LOSS} = 20 \log \frac{V_{OUT}}{V_{IN}}$$

* ADD 50Ω TERMINATION FOR OFF-ISOLATION

ΔV$_{OUT}$ IS THE MEASURED VOLTAGE DUE TO CHARGE TRANSFER ERROR Q WHEN THE CHANNEL TURNS OFF.

$$Q = \Delta V_{OUT} \times C_L$$

Figure 3. Charge Injection
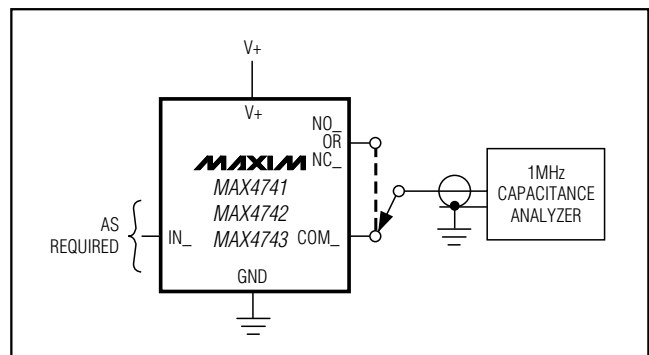
Figure 4. NO_, NC_, and COM_ Capacitance

MAX4741/MAX4742/MAX4743

TOP VIEW

SOT23-8

SOT23-8

SOT23-8

μ**DFN**

(2mm x 2mm)
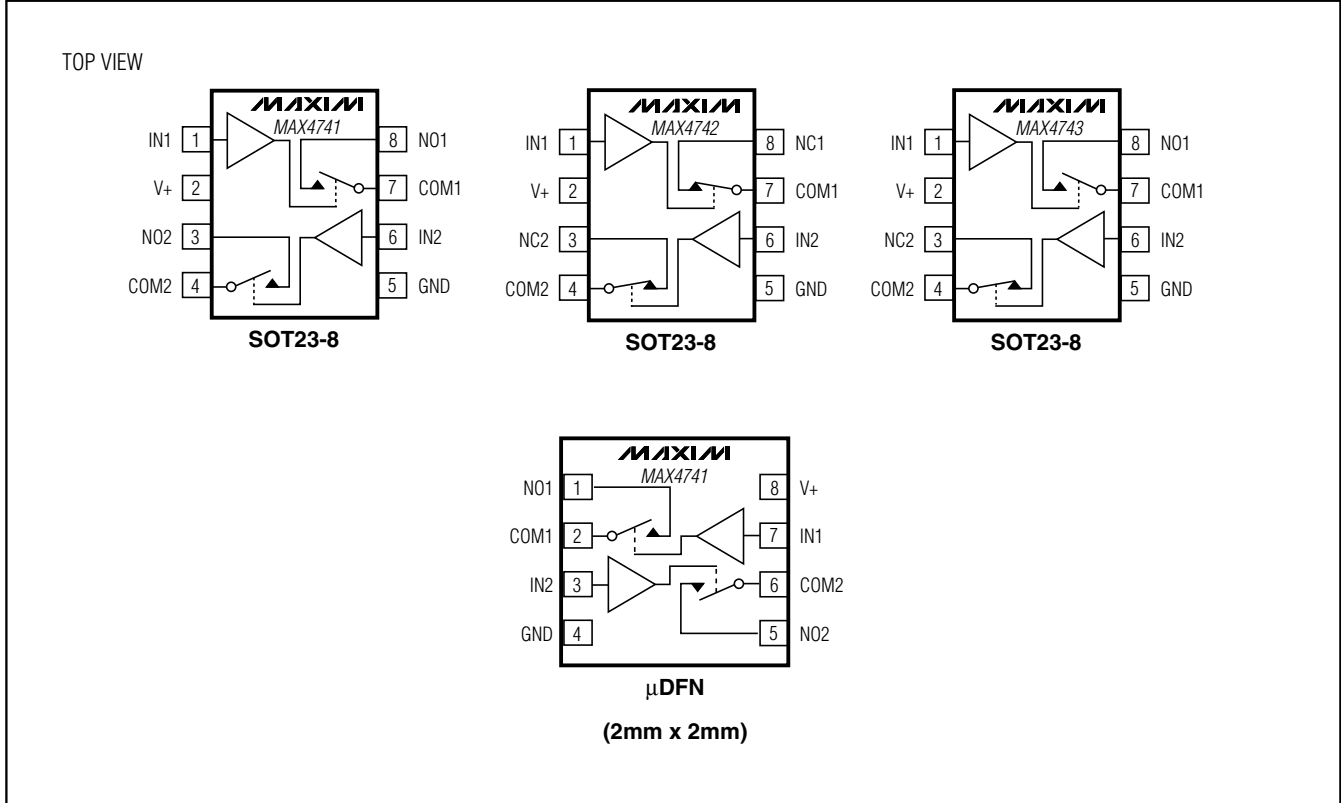
Chip Information

TRANSISTOR COUNT = 121

PROCESS = CMOS

MAXIM

Package Information

(The package drawing(s) in this data sheet may not reflect the most current specifications. For the latest package outline information, go to **www.maxim-ic.com/packages**.)

SOT23, 8L.EPS

SYMBOL	MIN	NOM	MAX
A	0.90	1.25	1.45
A1	0.00	0.06	0.15
A2	0.90	1.10	1.30
b	0.22	0.30	0.38
c	0.08	0.15	0.22
D	2.80	2.90	3.00
E	2.60	2.80	3.00
E1	1.50	1.625	1.75
L	0.30	0.45	0.60
L2		0.25 BSC.	
e		0.65 BSC.	
e1		1.95 REF.	
θ	0°	3°	8°

PKG CODES: K8-1, K8-2, K8S-3, K8F-4, K8FH-4, K8-5, K8C-6

NOTE:
1. ALL DIMENSIONS ARE IN MILLIMETERS.
2. FOOT LENGTH MEASURED FROM LEAD TIP TO UPPER RADIUS OF HEEL OF THE LEAD PARALLEL TO SEATING PLANE C.
3. PACKAGE OUTLINE EXCLUSIVE OF MOLD FLASH & METAL BURR.
4. PACKAGE OUTLINE INCLUSIVE OF SOLDER PLATING.
5. COPLANARITY 4 MILS. MAX.
6. PIN 1 I.D. DOT IS 0.3 MM Ø MIN. LOCATED ABOVE PIN 1.
7. SOLDER THICKNESS MEASURED AT FLAT SECTION OF LEAD BETWEEN 0.08mm AND 0.15mm FROM LEAD TIP.
8. MEETS JEDEC MO178 VARIATION BA.

—DRAWING NOT TO SCALE—

DALLAS SEMICONDUCTOR **MAXIM**

TITLE:
PACKAGE OUTLINE, SOT-23, 8L BODY

APPROVAL	DOCUMENT CONTROL NO.	REV.	1/1
	21-0078	F	

8LUMAXD.EPS

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	-	0.043	-	1.10
A1	0.002	0.006	0.05	0.15
A2	0.030	0.037	0.75	0.95
b	0.010	0.014	0.25	0.36
c	0.005	0.007	0.13	0.18
D	0.116	0.120	2.95	3.05
e	0.0256 BSC		0.65 BSC	
E	0.116	0.120	2.95	3.05
H	0.188	0.198	4.78	5.03
L	0.016	0.026	0.41	0.66
α	0°	6°	0°	6°
S	0.0207 BSC		0.5250 BSC	

TOP VIEW

BOTTOM VIEW

FRONT VIEW

SIDE VIEW

NOTES:
1. D&E DO NOT INCLUDE MOLD FLASH.
2. MOLD FLASH OR PROTRUSIONS NOT TO EXCEED 0.15MM (.006").
3. CONTROLLING DIMENSION: MILLIMETERS.
4. MEETS JEDEC MO-187C-AA.

DALLAS SEMICONDUCTOR **MAXIM**

PROPRIETARY INFORMATION

TITLE:
PACKAGE OUTLINE, 8L uMAX/uSOP

APPROVAL	DOCUMENT CONTROL NO.	REV.	1/1
	21-0036	J	

MAXIM _____ 11

MAX4741/MAX4742/MAX4743

MAX4741/MAX4742/MAX4743

(The package drawing(s) in this data sheet may not reflect the most current specifications. For the latest package outline information, go to **www.maxim-ic.com/packages**.)
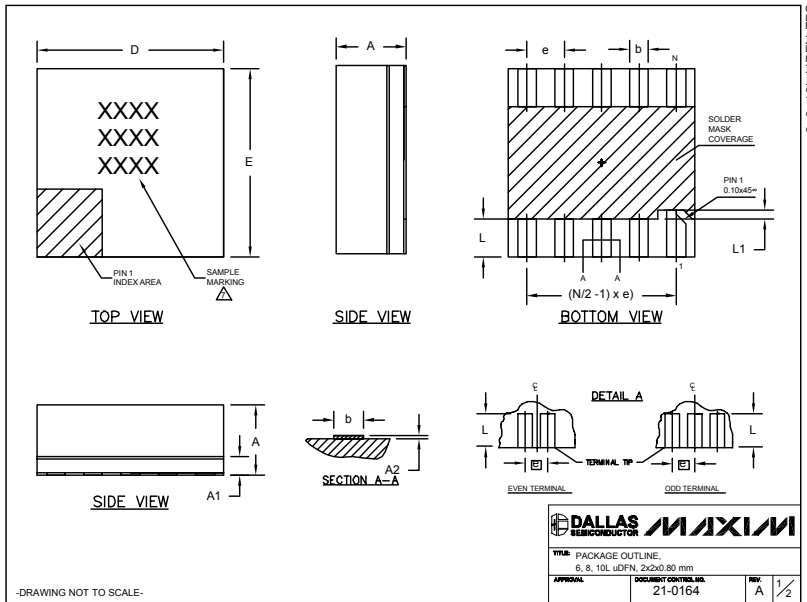

COMMON DIMENSIONS			
SYMBOL	MIN.	NOM.	MAX.
A	0.70	0.75	0.80
A1	0.15	0.20	0.25
A2	0.020	0.025	0.035
D	1.95	2.00	2.05
E	1.95	2.00	2.05
L	0.30	0.40	0.50
L1	0.10 REF.		

PACKAGE VARIATIONS				
PKG. CODE	N	e	b	(N/2 -1) x e
L622-1	6	0.65 BSC	0.30±0.05	1.30 REF.
L822-1	8	0.50 BSC	0.25±0.05	1.50 REF.
L1022-1	10	0.40 BSC	0.20±0.03	1.60 REF.

NOTES:
1. ALL DIMENSIONS ARE IN mm. ANGLES IN DEGREES.
2. COPLANARITY SHALL NOT EXCEED 0.08mm.
3. WARPAGE SHALL NOT EXCEED 0.10mm.
4. PACKAGE LENGTH/PACKAGE WIDTH ARE CONSIDERED AS SPECIAL CHARACTERISTIC(S).
5. "N" IS THE TOTAL NUMBER OF LEADS.
6. NUMBER OF LEADS SHOWN ARE FOR REFERENCE ONLY.
7. MARKING IS FOR PACKAGE ORIENTATION REFERENCE ONLY.

-DRAWING NOT TO SCALE-

DALLAS **MAXIM**
TITLE: PACKAGE OUTLINE, 6, 8, 10L uDFN, 2x2x0.80 mm
APPROVAL | DOCUMENT CONTROL NO. 21-0164 | REV. A | 2/2

_____**Revision History**

Pages changed at Rev 2: 1, 12