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General Description

The MAX6143 is a low-noise, high-precision voltage reference. The device features a proprietary temperaturecoefficient curvature-correction circuit and laser-trimmed thin-film resistors that result in a very low 3ppm/°C temperature coefficient and excellent ±0.06% initial accuracy. The MAX6143 provides a TEMP output where the output voltage is proportional to die temperature, making the device suitable for a wide variety of temperature-sensing applications. The device also provides a TRIM input, allowing fine trimming of the output voltage with a resistive-divider network. Low temperature drift and low noise make the MAX6143 ideal for use with high-resolution A/D or D/A converters.

The MAX6143 provides accurate preset +2.5V, +3.3V, +4.096V, +5.0V, and +10V reference voltages and accepts input voltages up to +40V. The device draws 340µA of supply current and sources 30mA or sinks 2mA of load current. The active-low shutdown feature (SHDN) reduces supply current to 0.01µA. The MAX6143 uses bandgap technology for low-noise performance and excellent accuracy. The MAX6143 does not require an output bypass capacitor for stability, and is stable with capacitive loads up to 100µF. Eliminating the output bypass capacitor saves valuable board area in spacecritical applications.

The MAX6143 is available in an 8-pin SO package and operates over the automotive (-40°C to +125°C) temperature range.

Applications

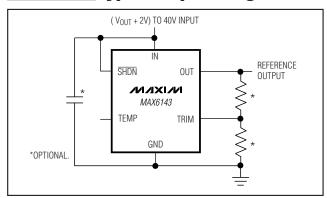
A/D Converters D/A Converters Voltage Regulators **Threshold Detectors**

Digital Voltmeters

Features

- ♦ Wide (Vout + 2V) to +40V Supply Voltage Range
- **♦** Excellent Temperature Stability: 3ppm/°C (max)
- ♦ Tight Initial Accuracy: 0.05% (max)
- ♦ Low Noise: 3.8µV_{P-P} (typ at 2.5V Output)
- ♦ Sources up to 30mA Output Current
- **♦** Low Supply Current: 450µA (max at +25°C)
- **♦** Low 0.01µA Shutdown Current
- **♦ Linear Temperature Transducer Voltage Output**
- ♦ +2.5V, +3.3V, +4.096V, +5.0V, or +10V Output **Voltages**
- ♦ Wide Operating Temperature Range: -40°C to +125°C
- ♦ No External Capacitors Required for Stability

Typical Operating Circuit



Pin Configuration appears at end of data sheet.

Ordering Information/Selector Guide

PART	TEMP RANGE	PIN- PACKAGE	OUTPUT VOLTAGE (V)	TEMPERATURE COEFFICIENT (ppm/°C) 40°C TO +125°C	INITIAL ACCURACY (%)
MAX6143AASA25	-40°C to +125°C	8 SO	2.500	3	0.06
MAX6143BASA25	-40°C to +125°C	8 SO	2.500	10	0.10
MAX6143AASA33	-40°C to +125°C	8 SO	3.300	3	0.06
MAX6143BASA33	-40°C to +125°C	8 SO	3.300	10	0.10
MAX6143AASA41	-40°C to +125°C	8 SO	4.096	3	0.06
MAX6143BASA41	-40°C to +125°C	8 SO	4.096	10	0.10
MAX6143AASA50	-40°C to +125°C	8 SO	5.000	3	0.06
MAX6143BASA50	-40°C to +125°C	8 SO	5.000	10	0.10
MAX6143AASA10	-40°C to +125°C	8 SO	10.000	3	0.05
MAX6143BASA10	-40°C to +125°C	8 SO	10.000	10	0.10

ABSOLUTE MAXIMUM RATINGS

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS—MAX6143_25 (Vout = 2.5V)

 $(V_{IN} = V_{\overline{SHDN}} = +5V, T_A = -40^{\circ}C \text{ to } +125^{\circ}C, \text{ unless otherwise noted.}$ Typical values are at $T_A = +25^{\circ}C.)$ (Note 1)

PARAMETER	SYMBOL	CON	IDITIONS	MIN	TYP	MAX	UNITS
OUTPUT							
0.4	1/	N-11 T 0500	MAX6143A_25 (0.06%)	2.4985	2.5	2.5015	V
Output Voltage	Vout	No load, $T_A = +25^{\circ}C$	MAX6143B_25 (0.1%)	2.4975	2.5	2.5025	V
Output Adjustment Range	ΔVTRIM	$R_{POT} = 10k\Omega$	•	±3	±6		%
Output-Voltage Temperature Coefficient	TCV _{OUT}	$T_A = -40^{\circ}\text{C to} + 125^{\circ}\text{C}$	MAX6143AASA		1.5	3	ppm/°C
(Note 2)		10 0 10 1 120 0	MAX6143BASA		3	10	ββ, σ
Line Regulation (Note 2)	A\/\cit / A\/\bi	1 5V < V \ \ 10V	T _A = +25°C		0.6	5	nnm/\/
Line Regulation (Note 3)	$\Delta V_{OUT} / \Delta V_{IN}$	$4.5V \le V_{IN} \le 40V$	$T_A = -40^{\circ}C \text{ to } +125^{\circ}C$		0.8	10	ppm/V
		Sourcing:	T _A = +25°C		2	10	
Load Regulation (Note 3)	ΔV _{OUT} /	$0 \le I_{OUT} \le 10mA$	$T_A = -40^{\circ}C \text{ to } +125^{\circ}C$		2	15	ppm/mA
Load Hegulation (Note 3)	Δ lout	Sinking:	$T_A = +25^{\circ}C$		50	500	
		-0.6mA ≤ I _{OUT} ≤ 0	$T_A = -40^{\circ}C \text{ to } +125^{\circ}C$		90	900	
Output Short-Circuit	1	OUT shorted to GND			60		Λ
Current	I _{SC}	OUT shorted to IN			3		mA
Temperature Hysteresis (Note 4)	ΔV _{OUT} / cycle				120		ppm
Long-Term Stability	ΔV _{OUT} / time	1000 hours at T _A = +25	5°C		50		ppm
DYNAMIC							
Naisa Valtaga		f = 0.1Hz to 10Hz			3.8		μV _{P-P}
Noise Voltage	eout	f = 10Hz to 1kHz			6.8		μV _{RMS}
Turn-On Settling Time	t _R	To Vout = 0.1% of fina	ıl value, C _{OUT} = 50pF		150		μs
INPUT							
Supply Voltage Range	VIN	Guaranteed by line reg	julation test	4.5		40.0	V
Outles agent Cumply Courses	lis.	No load, normal	T _A = +25°C		300	450	
Quiescent Supply Current	IIN		$T_A = -40^{\circ}\text{C to } + 125^{\circ}\text{C}$			600	μΑ
Shutdown Supply Current	ISHDN	No load, VSHDN < 0.8V	$T_A = -40^{\circ}\text{C to } + 125^{\circ}\text{C}$		0.01	5	μΑ

ELECTRICAL CHARACTERISTICS—MAX6143_25 (Vout = 2.5V) (continued)

 $(V_{IN} = V_{\overline{SHDN}} = +5V, T_A = -40^{\circ}C \text{ to } +125^{\circ}C, \text{ unless otherwise noted.}$ Typical values are at $T_A = +25^{\circ}C.)$ (Note 1)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
SHUTDOWN (SHDN)						
Logic-High Input Voltage	VIH		2.0			V
Logic-Low Input Voltage	V _I L				0.8	V
Logic-High Input Current	IIH				20	μΑ
Logic-Low Input Current	lıL				2	μΑ
TEMP OUTPUT						
TEMP Output Voltage	VTEMP			570		mV
TEMP Temperature Coefficient	TC _{TEMP}			1.9		mV/°C

ELECTRICAL CHARACTERISTICS—MAX6143_33 (Vout = 3.3V)

 $(V_{IN} = V_{\overline{SHDN}} = +10V, T_A = -40^{\circ}C \text{ to } +125^{\circ}C, \text{ unless otherwise noted.}$ Typical values are at $T_A = +25^{\circ}C.)$ (Note 1)

PARAMETER	SYMBOL	CON	CONDITIONS		TYP	MAX	UNITS
OUTPUT				.			
O. d d V - H	1/	N-11 T 0500	MAX6143A_33 (0.06%)	3.2980	3.3	3.3020	V
Output Voltage	Vout	No load, $T_A = +25^{\circ}C$	MAX6143B_33 (0.1%)	3.2967	3.3	3.3033	V
Output Adjustment Range	ΔVTRIM	$R_{POT} = 10k\Omega$	•	±3	±6		%
Output-Voltage Temperature Coefficient	TCV _{OUT}	$T_A = -40^{\circ}\text{C to } + 125^{\circ}\text{C}$	MAX6143AASA		1.5	3	ppm/°C
(Note 2)	10,0001	1A = -40 C to +125 C	MAX6143BASA		3	10	ррпі, С
Line Regulation (Note 3)	ΔV _{OUT} /	5.3V ≤ V _{IN} ≤ 40V	$T_A = +25$ °C		0.6	5	ppm/V
Line negulation (Note 3)	ΔVIN	3.3V ≤ V N ≤ 40V	$T_A = -40^{\circ}C \text{ to } +125^{\circ}C$		0.8	10	
		Sourcing:	$T_A = +25$ °C		2	10	
Load Regulation (Note 3)	ΔV _{OUT} / Δl _{OUT}	0 ≤ I _{OUT} ≤ 10mA	$T_A = -40^{\circ}C \text{ to } +125^{\circ}C$		2	15	ppm/ mA
Load negulation (Note 3)		Sinking: -0.6mA ≤ I _{OUT} ≤ 0	$T_A = +25$ °C		50	500	
			$T_A = -40^{\circ}C \text{ to } +125^{\circ}C$		90	900	
Output Chart Circuit Current	laa	OUT shorted to GND			60		mA
Output Short-Circuit Current	Isc	OUT shorted to IN			3		MA
Temperature Hysteresis (Note 4)	ΔV _{OUT} / cycle				120		ppm
Long-Term Stability	ΔV _{OUT} / time	1000 hours at T _A = +25	5°C		50		ppm
DYNAMIC							
Noise Voltage	00117	f = 0.1Hz to 10Hz			5		μV _{P-P}
Noise Voltage	eout	f = 10Hz to 1kHz		9.3		μVRMS	
Turn-On Settling Time	t _R	To Vout = 0.1% of fina	l value, C _{OUT} = 50pF		180		μs

ELECTRICAL CHARACTERISTICS—MAX6143_33 (Vout = 3.3V) (continued)

 $(V_{IN} = V_{\overline{SHDN}} = +10V, T_A = -40^{\circ}C \text{ to } +125^{\circ}C, \text{ unless otherwise noted.}$ Typical values are at $T_A = +25^{\circ}C.)$ (Note 1)

PARAMETER	SYMBOL	CC	ONDITIONS	MIN	TYP	MAX	UNITS
INPUT							
Supply Voltage Range	V _{IN}	Guaranteed by line r	luaranteed by line regulation test			40.0	V
Quiescent Supply Current	lu.	No load, normal	$T_A = +25$ °C		320	500	
Quiescent Supply Current	IIN	operation	$T_A = -40^{\circ}C \text{ to } +125^{\circ}C$			650	μΑ
Shutdown Supply Current	ISHDN	No load, V _{SHDN} < 0.8V	$T_A = -40^{\circ}\text{C to } + 125^{\circ}\text{C}$		0.01	5	μΑ
SHUTDOWN (SHDN)			·				
Logic-High Input Voltage	VIH			2.0			V
Logic-Low Input Voltage	V _{IL}					0.8	V
Logic-High Input Current	lιΗ					20	μΑ
Logic-Low Input Current	IIL					2	μΑ
TEMP OUTPUT							
TEMP Output Voltage	VTEMP				630		mV
TEMP Temperature Coefficient	TC _{TEMP}				2.1		mV/°C

ELECTRICAL CHARACTERISTICS—MAX6143_41 (Vout = 4.096V)

 $(V_{IN} = V_{\overline{SHDN}} = +10V, T_A = -40^{\circ}C \text{ to } +125^{\circ}C, \text{ unless otherwise noted.}$ Typical values are at $T_A = +25^{\circ}C.)$ (Note 1)

PARAMETER	SYMBOL	CON	DITIONS	MIN	TYP	MAX	UNITS
OUTPUT							
Outout Valtage	\/	No look T. 1058C	MAX6143A_41 (0.06%)	4.0935	4.096	4.0985	V
Output Voltage	Vout	No load, $T_A = +25^{\circ}C$	MAX6143B_41 (0.1%)	4.0919	4.096	4.1001]
Output Adjustment Range	ΔVTRIM	$R_{POT} = 10k\Omega$		±3	±6		%
Output-Voltage Temperature	TCV/sur	$T_A = -40^{\circ}C \text{ to } +125^{\circ}C$	MAX6143AASA		1.5	3	
Coefficient (Note 2)	TCV _{OUT}	1A = -40°C (0 + 125°C	MAX6143BASA		3	10	ppm/°C
Line Regulation (Note 3)	ΔV _{OUT} /	6.1V ≤ V _{IN} ≤ 40V	$T_A = +25^{\circ}C$		0.6	5	10 to 100 A /
	ΔVIN	0.1V ≤ V N ≤ 40V	$T_A = -40^{\circ}C \text{ to } +125^{\circ}C$		0.8	10	ppm/V
	ΔV _{OUT} / Δl _{OUT}	Sourcing: 0 ≤ I _{OUT} ≤ 10mA	T _A = +25°C		2	10	ppm/mA
Load Deculation (Nata 2)			$T_A = -40^{\circ}C \text{ to } +125^{\circ}C$		2	15	
Load Regulation (Note 3)		Sinking:	T _A = +25°C		50	500	
		-0.6mA ≤ I _{OUT} ≤ 0	$T_A = -40^{\circ}C \text{ to } +125^{\circ}C$		90	900	
Output Chart Circuit Current	laa	OUT shorted to GND			60		mA
Output Short-Circuit Current	I _{SC}	OUT shorted to IN			3		mA
Temperature Hysteresis (Note 4)	ΔV _{OUT} / cycle				120	_	ppm
Long-Term Stability	ΔV _{OUT} / time	1000 hours at T _A = +25	5°C		50		ppm

ELECTRICAL CHARACTERISTICS—MAX6143_41 (Vout = 4.096V) (continued)

 $(V_{IN} = V_{\overline{SHDN}} = +10V, T_A = -40^{\circ}C \text{ to } +125^{\circ}C, \text{ unless otherwise noted.}$ Typical values are at $T_A = +25^{\circ}C.)$ (Note 1)

PARAMETER	SYMBOL	co	ONDITIONS	MIN	TYP	MAX	UNITS
DYNAMIC	•			•			
Naisa Valtaga		f = 0.1Hz to $10Hz$			7		μV _{P-P}
Noise Voltage	eout	f = 10Hz to 1kHz			11.5		μV _{RMS}
Turn-On Settling Time	t _R	To $V_{OUT} = 0.1\%$ of fi	o V _{OUT} = 0.1% of final value, C _{OUT} = 50pF		200		μs
INPUT							
Supply Voltage Range	V _{IN}	Guaranteed by line r	egulation test	6.1		40.0	V
Quiescent Supply Current	l	No load, normal	T _A = +25°C		320	500	
	I _{IN}	operation	$T_A = -40^{\circ}\text{C to } + 125^{\circ}\text{C}$			650	μΑ
Shutdown Supply Current	ISHDN	No load, VSHDN < 0.8V	T _A =-40°C to +125°C		0.01	5	μΑ
SHUTDOWN (SHDN)	•	•		•			•
Logic-High Input Voltage	VIH			2.0			V
Logic-Low Input Voltage	VIL					0.8	V
Logic-High Input Current	IIH					20	μΑ
Logic-Low Input Current	I _{IL}					2	μΑ
TEMP OUTPUT							
TEMP Output Voltage	V _{TEMP}				630		mV
TEMP Temperature Coefficient	ТСтемр	(0.06%)			2.1		mV/°C

ELECTRICAL CHARACTERISTICS—MAX6143_50 (VOUT = 5.0V)

 $(V_{IN} = V_{\overline{SHDN}} = +15V, T_A = -40^{\circ}C \text{ to } +125^{\circ}C, \text{ unless otherwise noted.}$ Typical values are at $T_A = +25^{\circ}C.)$ (Note 1)

PARAMETER	SYMBOL	CON	DITIONS	MIN	TYP	MAX	UNITS
OUTPUT							
Output Voltage	V 0.17	No load, T _A = +25°C	MAX6143A_50 (0.06%)	4.9970	5.0	5.0030	V
- Output voltage	Vout		MAX6143B_50 (0.1%)	4.9950	5.0	5.0050	V
Output Adjustment Range	ΔVTRIM	$R_{POT} = 10k\Omega$		±3	±6		%
Output-Voltage Temperature Coefficient (Note 2)	TCVOUT	$T_A = -40^{\circ}\text{C to } + 125^{\circ}\text{C}$	MAX6143AASA		1.5	3	ppm/°C
	10,000		MAX6143BASA		3	10	
Line Degulation (Note 2)	ΔV _{OUT} /	7) /) / 40) /	$T_A = +25$ °C		0.6	5	h./
Line Regulation (Note 3)	ΔV_{IN}	7V ≤ V _{IN} ≤ 40V	$T_A = -40^{\circ}C \text{ to } +125^{\circ}C$		0.8	10	ppm/V
		Sourcing:	$T_A = +25^{\circ}C$		2	10	
Load Degulation (Nata 2)	ΔV _{OUT} /	0 ≤ I _{OUT} ≤ 10mA	$T_A = -40^{\circ}C \text{ to } +125^{\circ}C$		2	15	ppm/mA
Load Regulation (Note 3)	Δlout	Sinking: -0.6mA ≤ I _{OUT} ≤ 0	$T_A = +25^{\circ}C$		50	500	
			$T_A = -40^{\circ}C \text{ to } +125^{\circ}C$		90	900	

ELECTRICAL CHARACTERISTICS—MAX6143_50 (Vout = 5.0V) (continued)

 $(V_{IN} = V_{\overline{SHDN}} = +15V, T_A = -40^{\circ}C \text{ to } +125^{\circ}C, \text{ unless otherwise noted.}$ Typical values are at $T_A = +25^{\circ}C.)$ (Note 1)

PARAMETER	SYMBOL	co	ONDITIONS	MIN	TYP	MAX	UNITS
Outrout Chart Circuit Current	la a	OUT shorted to GND			60		A
Output Short-Circuit Current	Isc	OUT shorted to IN			3		mA
Temperature Hysteresis (Note 4)	ΔV _{OUT} / cycle				120		ppm
Long-Term Stability	ΔV _{OUT} / time	1000 hours at T _A = +	25°C		50		ppm
DYNAMIC							
Nicion Weller		f = 0.1Hz to 10Hz			9		μV _{P-P}
Noise Voltage	eout	f = 10Hz to 1kHz	10Hz to 1kHz		14.5		μV _{RMS}
Turn-On Settling Time	t _R	To $V_{OUT} = 0.1\%$ of fi		230		μs	
INPUT							
Supply Voltage Range	VIN	Guaranteed by line r	egulation test	7		40	V
Outre and Outre by Out	livi	No load, normal	$T_A = +25$ °C		320	550	
Quiescent Supply Current	IΝ	operation	$T_A = -40^{\circ}C \text{ to } +125^{\circ}C$			700	μΑ
Shutdown Supply Current	ISHDN	No load, V _{SHDN} < 0.8V	$T_A = -40^{\circ}\text{C to } + 125^{\circ}\text{C}$		0.01	5	μА
SHUTDOWN (SHDN)							
Logic-High Input Voltage	VIH			2.0			V
Logic-Low Input Voltage	VIL					0.8	V
Logic-High Input Current	l _{IH}					20	μΑ
Logic-Low Input Current	IIL					2	μΑ
TEMP OUTPUT							
TEMP Output Voltage	VTEMP				630		mV
TEMP Temperature Coefficient	TC _{TEMP}				2.1		mV/°C

ELECTRICAL CHARACTERISTICS—MAX6143_10 (Vout = 10V)

 $(V_{IN} = V_{\overline{SHDN}} = +15V, T_A = -40^{\circ}C \text{ to } +125^{\circ}C, \text{ unless otherwise noted.}$ Typical values are at $T_A = +25^{\circ}C.)$ (Note 1)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS			
OUTPUT										
Output Voltage	\/-·-	No local Tr. 10580	MAX6143A_10 (0.05%)	9.995	10.0	10.005	\/			
	Vout	No load, $T_A = +25^{\circ}C$	MAX6143B_10 (0.1%)	9.990	10.0	10.010	V			
Output Adjustment Range	ΔVTRIM	$R_{POT} = 10k\Omega$		±3	±6		%			
Output-Voltage	TCV	T. 40°C to .125°C	MAX6143AASA		1.5	3	nnm/0C			
Temperature Coefficient (Note 2)	TCV _{OUT}	$T_A = -40^{\circ}C \text{ to } +125^{\circ}C$	MAX6143BASA		3	10	ppm/°C			
Line Regulation (Note 3)	ΔV _{OUT} /	101/	T _A = +25°C		0.6	5				
	ΔVIN	$12V \le V_{\text{IN}} \le 40V$	$T_A = -40^{\circ}\text{C to } + 125^{\circ}\text{C}$		0.8	10	ppm/V			

ELECTRICAL CHARACTERISTICS—MAX6143_10 (Vout = 10V) (continued)

 $(V_{IN} = V_{\overline{SHDN}} = +15V, T_A = -40^{\circ}C \text{ to } +125^{\circ}C, \text{ unless otherwise noted.}$ Typical values are at $T_A = +25^{\circ}C.)$ (Note 1)

PARAMETER	SYMBOL	co	NDITIONS	MIN	TYP	MAX	UNITS
		Sourcing:	$T_A = +25$ °C		2	10	
Load Regulation (Note 3)	ΔV _{OUT} /	$0 \le I_{OUT} \le 10$ mA	$T_A = -40^{\circ}C \text{ to } +125^{\circ}C$		2	15	ppm/mA
Load negulation (Note 3)	Δlout	Sinking:	T _A = +25°C		50	500	ррилиа
		-0.6mA ≤ I _{OUT} ≤ 0	$T_A = -40^{\circ}C \text{ to } +125^{\circ}C$		90	900	
Output Short-Circuit	Isc	OUT shorted to GND			60		mA
Current	150	OUT shorted to IN			3		ША
Temperature Hysteresis (Note 4)	ΔV _{OUT} / cycle				120		ppm
Long-Term Stability	ΔV _{OUT} / time	1000 hours at T _A = +	00 hours at T _A = +25°C		50		ppm
DYNAMIC							
Noise Voltage	00117	f = 0.1Hz to 10Hz	= 0.1Hz to 10Hz		18		μV _{P-P}
Noise voitage	eout	f = 10Hz to $1kHz$	= 10Hz to 1kHz		29		μV _{RMS}
Turn-On Settling Time	t _R	To $V_{OUT} = 0.1\%$ of fire	To $V_{OUT} = 0.1\%$ of final value, $C_{OUT} = 50pF$		400		μs
INPUT							
Supply Voltage Range	V _{IN}	Guaranteed by line re	egulation test	12		40	V
Quiescent Supply Current	I _{IN}	No load, normal	$T_A = +25^{\circ}C$		340	550 µA	πΔ
Quicocont Supply Surrent	IIIV	operation	$T_A = -40^{\circ}C \text{ to } +125^{\circ}C$			700	μ/ (
Shutdown Supply Current	ISHDN	No load, V SHDN < 0.8V	$T_A = -40^{\circ}C \text{ to } +125^{\circ}C$		0.01	5	μΑ
SHUTDOWN (SHDN)							
Logic-High Input Voltage	VIH			2.0			V
Logic-Low Input Voltage	V _I L					0.8	V
Logic-High Input Current	lıH					20	μΑ
Logic-Low Input Current	I _{IL}					2	μΑ
TEMP OUTPUT				•			
TEMP Output Voltage	VTEMP				630		mV
TEMP Temperature Coefficient	ТСтемР				2.1		mV/°C

Note 1: All devices are 100% production tested at $T_A = +25$ °C and guaranteed by design over $T_A = T_{MIN}$ to T_{MAX} , as specified.

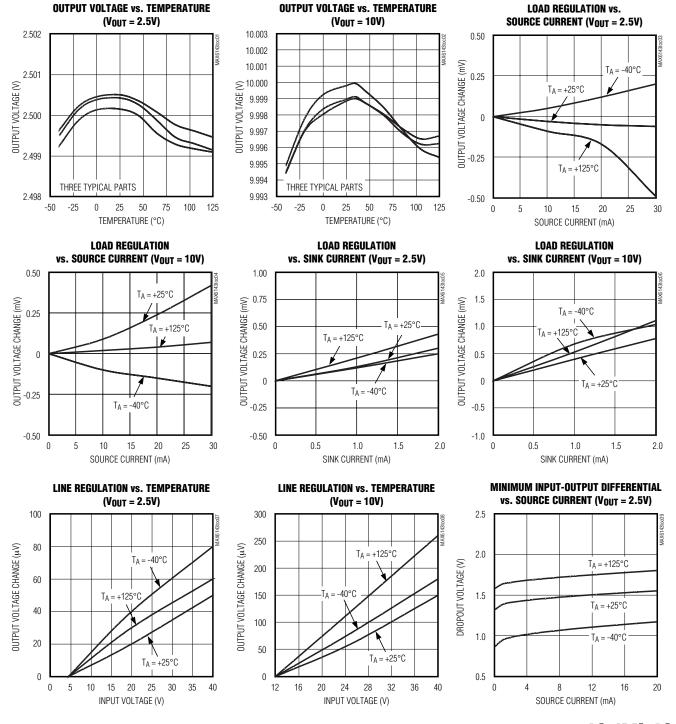
Note 2: Temperature coefficient is defined as ΔV_{OUT} divided by the temperature range.

Note 3: Line and load regulation specifications do not include the effects of self-heating.

Note 4: Thermal hysteresis is defined as the change in +25°C output voltage before and after cycling the device from T_{MAX} to T_{MIN}.

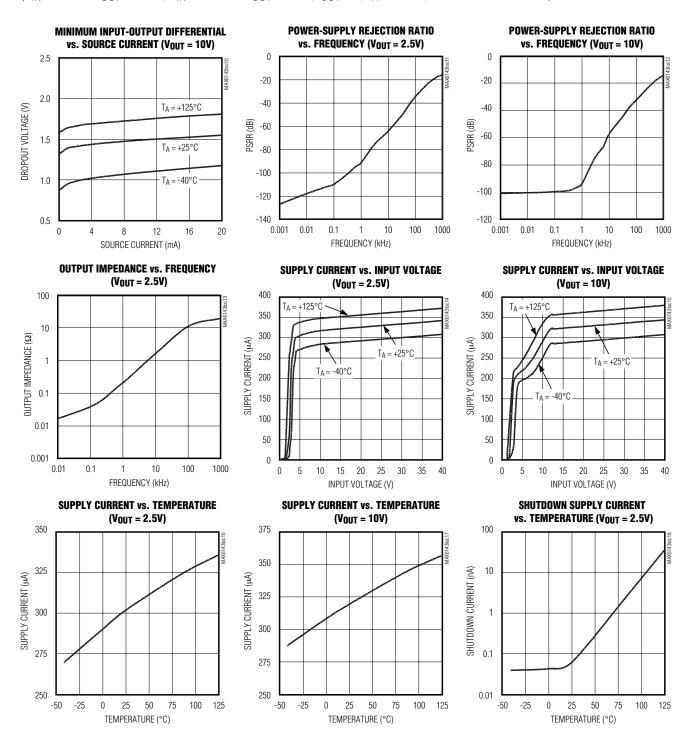
Typical Operating Characteristics

 $(V_{IN} = +5V \text{ for } V_{OUT} = +2.5V, V_{IN} = +15V \text{ for } V_{OUT} = +10V, I_{OUT} = 0, T_A = +25^{\circ}C, \text{ unless otherwise noted.})$



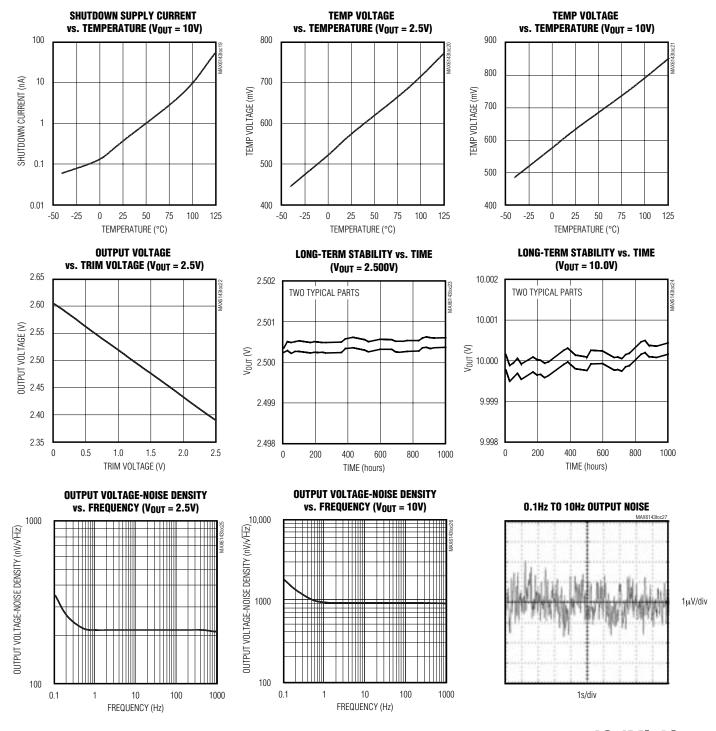
Typical Operating Characteristics (continued)

 $(V_{IN} = +5V \text{ for } V_{OUT} = +2.5V, V_{IN} = +15V \text{ for } V_{OUT} = +10V, I_{OUT} = 0, T_A = +25^{\circ}C, unless \text{ otherwise noted.})$



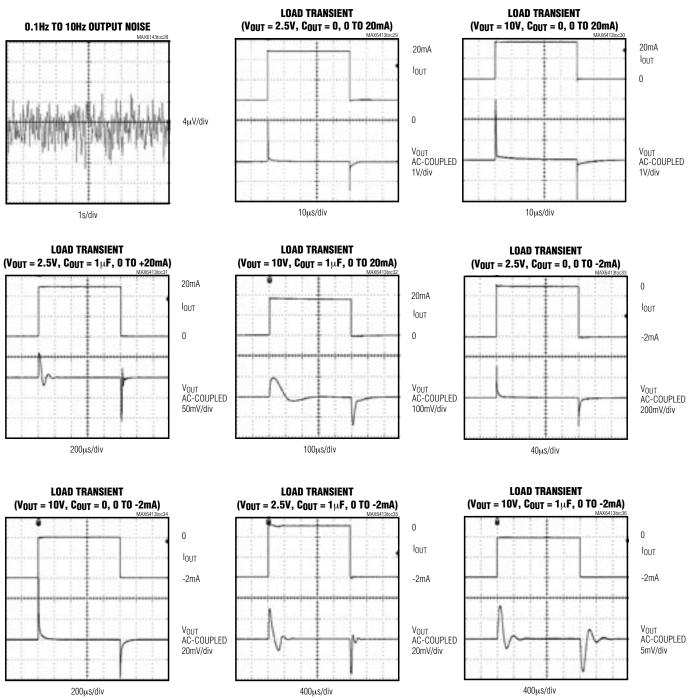
Typical Operating Characteristics (continued)

 $(V_{IN} = +5V \text{ for } V_{OUT} = +2.5V, V_{IN} = +15V \text{ for } V_{OUT} = +10V, I_{OUT} = 0, T_A = +25^{\circ}C, \text{ unless otherwise noted.})$



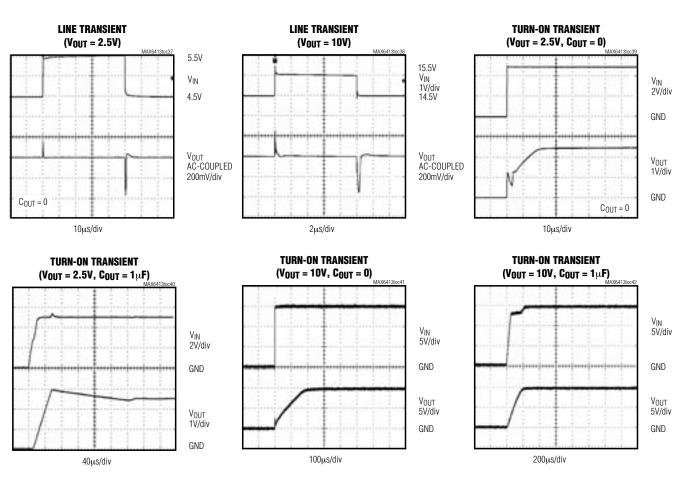
Typical Operating Characteristics (continued)

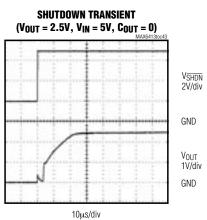
 $(V_{IN} = +5V \text{ for } V_{OUT} = +2.5V, V_{IN} = +15V \text{ for } V_{OUT} = +10V, I_{OUT} = 0, T_A = +25^{\circ}C, unless otherwise noted.)$

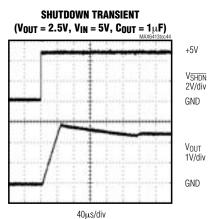


Typical Operating Characteristics (continued)

 $(V_{IN} = +5V \text{ for } V_{OUT} = +2.5V, V_{IN} = +15V \text{ for } V_{OUT} = +10V, I_{OUT} = 0, T_A = +25^{\circ}C, unless \text{ otherwise noted.})$

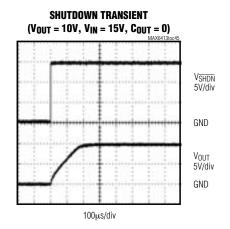


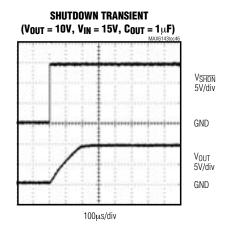




Typical Operating Characteristics (continued)

 $(V_{IN} = +5V \text{ for } V_{OUT} = +2.5V, V_{IN} = +15V \text{ for } V_{OUT} = +10V, I_{OUT} = 0, T_A = +25^{\circ}C, \text{ unless otherwise noted.})$





Pin Description

PIN	NAME	FUNCTION
1, 8	I.C.	Internally Connected. Do not connect externally.
2	IN	Positive Power-Supply Input
3	TEMP	Temperature Proportional Output Voltage. TEMP generates an output voltage proportional to the die temperature.
4	GND	Ground
5	TRIM	Output Voltage Trim. Connect TRIM to the center of a voltage-divider between OUT and GND for trimming. Leave unconnected to use the preset output voltage.
6	OUT	Output Voltage
7	SHDN	Active-Low Shutdown. Connect SHDN to IN for normal operation.

Detailed Description

The MAX6143 precision voltage reference provides accurate preset +2.5V, +3.3V, +4.096V, +5.0V, and +10V reference voltages from up to +40V input voltages. These devices feature a proprietary temperature-coefficient curvature-correction circuit and laser-trimmed thinfilm resistors that result in a very low 3ppm/°C temperature coefficient and excellent 0.05% initial accuracy. The MAX6143 draws 340µA of supply current and sources 30mA or sinks 2mA of load current.

Trimming the Output Voltage

Trim the factory-preset output voltage on the MAX6143 by placing a resistive-divider network between OUT, TRIM, and GND. Use the following formula to calculate the change in output voltage from its preset value:

 $\Delta V_{OUT} = 2 \times (V_{TRIM} - V_{TRIM} (open)) \times k$

where:

 $V_{TRIM} = 0$ to V_{OUT}

VTRIM (open) = VOUT (nominal) / 2 (typ)

 $k = \pm 6\%$ typ

For example, use a $50k\Omega$ potentiometer (such as the MAX5436) between OUT, TRIM, and GND with the potentiometer wiper connected to TRIM (see Figure 2). As the TRIM voltage changes from V_{OUT} to GND, the output voltage changes accordingly. Set R2 to $1M\Omega$ or less. Currents through resistors R1 and R2 add to the quiescent supply current.

Temp Output

The MAX6143 provides a temperature output proportional to die temperature. TEMP can be calculated from the following formula:

TEMP (V) = T
$$_J$$
 (°K) x n

where T_J = the die temperature, n = the temperature multiplier.

$$n = \frac{V_{TEMP}(at T_J = T_0)}{T_0} \approx 1.9 \text{mV/}^{\circ} \text{K}$$

 T_A = the ambient temperature.

Self-heating affects the die temperature and conversely, the TEMP output. The TEMP equation assumes the output is not loaded. If device power dissipation is negligible, then $T_J \approx T_A$.

Applications Information Bypassing/Output Capacitance

For the best line-transient performance, decouple the input with a 0.1µF ceramic capacitor as shown in the *Typical Operating Circuit*. Place the capacitor as close to IN as possible. When transient performance is less important, no capacitor is necessary.

The MAX6143 does not require an output capacitor for stability and is stable with capacitive loads up to $100\mu F$. In applications where the load or the supply can experience step changes, a larger output capacitor reduces the amount of overshoot (undershoot) and improves the circuit's transient response. Place output capacitors as close to the device as possible for best performance.

Supply Current

The MAX6143 consumes 340µA of quiescent supply current. This improved efficiency reduces power dissipation and extends battery life.

Shutdown

The active-low shutdown feature reduces supply current to $0.01\mu A$ (typ), further extending battery life. Connect \overline{SHDN} to GND to activate shutdown. Connect \overline{SHDN} to IN for normal operation.

Thermal Hysteresis

Thermal hysteresis is the change in the output voltage at $T_A = +25^{\circ}\text{C}$ before and after the device is cycled over its entire operating temperature range. Hysteresis is caused by differential package stress appearing across the bandgap core transistors. The typical thermal hysteresis value is 120ppm.

Turn-On Time

The MAX6143 typically turns on and settles to within 0.1% of the preset output voltage in 150 μ s (2.5V output). The turn-on time can increase up to 150 μ s with the device operating at a 1 μ F load.

Short-Circuited Outputs

The MAX6143 features a short-circuit-protected output. Internal circuitry limits the output current to 60mA when short circuiting the output to ground. The output current is limited to 3mA when short circuiting the output to the input.

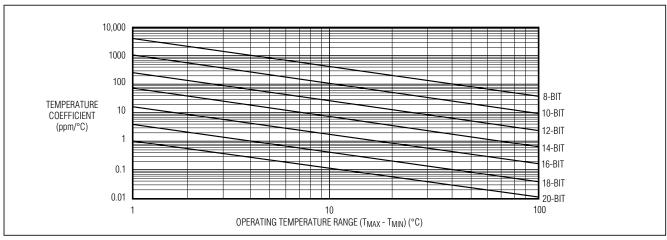


Figure 1. Temperature Coefficient vs. Operating Temperature Range for a 1 LSB Maximum Error

Temperature Coefficient vs. Operating Temperature Range for a 1 LSB Maximum Error

In a data converter application, the reference voltage of the converter must stay within a certain limit to keep the error in the data converter smaller than the resolution limit through the operating temperature range. Figure 1 shows the maximum allowable reference-voltage temperature coefficient to keep the conversion error to less than 1 LSB, as a function of the operating temperature range (T_{MAX} - T_{MIN}) with the converter resolution as a parameter. The graph assumes the reference-voltage temperature coefficient as the only parameter affecting accuracy.

In reality, the absolute static accuracy of a data converter is dependent on the combination of many parameters such as integral nonlinearity, differential nonlinearity, offset error, gain error, as well as voltage-reference changes.

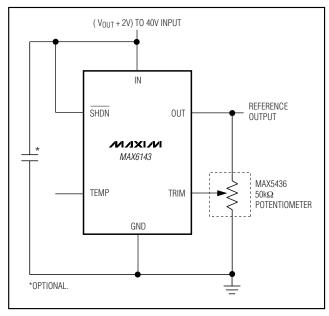


Figure 2. Applications Circuit Using the MAX5436 Potrntiometer

Pin Configuration

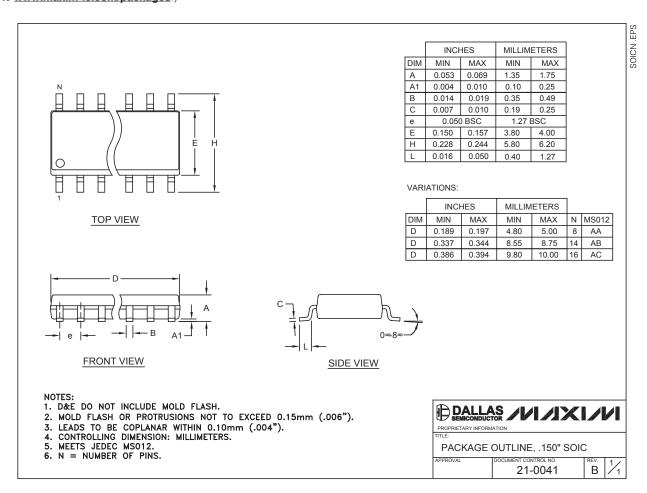
TOP VIEW I.C.* IN 2 TEMP 3 GND 4 SO *INTERNALLY CONNECTED. DO NOT CONNECT.

_Chip Information

TRANSISTOR COUNT: 429 PROCESS: BiCMOS

Package Information

(The package drawing(s) in this data sheet may not reflect the most current specifications. For the latest package outline information go to www.maxim-ic.com/packages.)



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