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### **General Description**

The MAX6220 is a low-noise, precision voltage reference with extremely low 20ppm/°C temperature coefficient over the automotive temperature range (-40°C to +125°C) and excellent ±0.1% initial accuracy. This device features buried-zener technology for low noise performance. The load-regulation specification is guaranteed for source and sink currents up to 15mA. Excellent line and load regulation and low output impedance at high frequency make the MAX6220 ideal for high-resolution data-conversion systems up to 16 bits. The MAX6220 is set for 2.5V, 4.096V, and 5.0V outputs.

### **Applications**

High-Accuracy Industrial and Process Control ATE Equipment **Precision Current Sources** 

### **Features**

- **♦** Low 20ppm/°C Temperature Coefficient over -40°C to +125°C Range
- ♦ Very Low 1.5µVp-p Noise (0.1Hz to 10Hz)
- ♦ ±0.1% Initial Accuracy
- ♦ ±15mA Output Source and Sink Current
- ♦ 8V to 40V Input Voltage Range
- Optional Noise Reduction and Voltage Trim
- **♦** Excellent Transient Response
- ♦ Low 20ppm/1000hr Long-Term Stability
- ♦ Stable for All Capacitive Loads

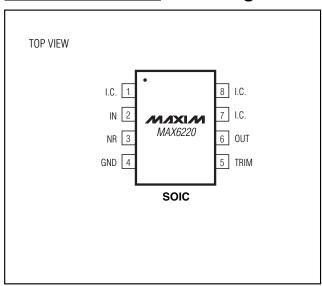
### **Ordering Information**

PART	TEMP. RANGE	PIN- PACKAGE	OUPUT VOLTAGE (V)
MAX6220ASA25	-40°C to +125°C	8 SO	2.5
MAX6220ASA41	-40°C to +125°C	8 SO	4.096
MAX6220ASA50	-40°C to +125°C	8 SO	5.0

### **Typical Operating Circuit**

## 8V TO 40V INPUT REFERENCE OUT OUT MIXIM 2.2µF TRIM $2.2\mu F$ GND \*OPTIONAL (STABLE WITH ALL CAPACITIVE LOADS)

### **Pin Configuration**



MIXIM

Maxim Integrated Products 1

### **ABSOLUTE MAXIMUM RATINGS**

(Voltages Referenced to GND)	
IN	0.3V to +42V
OUT, TRIM	0.3V to +12V
NR	0.3V to +6V
OUT Short-Circuit to GND Duration (V <sub>IN</sub> ≤ 12V)	Continuous
OUT Short-Circuit to GND Duration (V <sub>IN</sub> ≤ 42V).	5s
OUT Short-Circuit to IN Duration (V <sub>IN</sub> ≤ 12V)	

Continuous Power	Dissipation ( $T_A = +1$	70°C)
8-Pin SO (derate	5.9mW/°C above +	70°C)471mW
Operating Temper	ature Range	40°C to +125°C
Storage Temperati	ure Range	65°C to +150°C
Lead Temperature	(soldering, 10s)	+300°C

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

### **ELECTRICAL CHARACTERISTICS—(+2.5V)**

 $(V_{IN} = +10V, I_{OUT} = 0mA, T_A = T_{MIN}$  to  $T_{MAX}$ , unless otherwise noted. Typical values are at  $T_A = +25^{\circ}C$ .)

PARAMETER	SYMBOL	CONDITIONS	TA	MIN	TYP	MAX	UNITS
Input Voltage Range	V <sub>IN</sub>			8		40	V
Output Voltage	Vout		+25°C	2.497	2.500	2.503	V
Output Voltage Temperature Coefficient (Note 1)	TCV <sub>OUT</sub>				2	20	ppm/°C
Line Regulation (Note 2)	ΔV <sub>OUT</sub> /	01/21/22/01/	+25°C		2	7	nnm/\/
Line Regulation (Note 2)	$\Delta V_{IN}$	8V ≤ V <sub>IN</sub> ≥ 40V				45	ppm/V
		Sourcing: 0mA ≤ I <sub>OUT</sub> ≤ 15mA	+25°C		1	6	ppm/mA
Load Regulation (Note 2)	ΔV <sub>OUT</sub> /					15	
	$\Delta$ lout	Sinking: -15mA ≤ I <sub>OUT</sub> ≤ 0mA	+25°C		1	6	
						30	
0 10 1	lu.		+25°C		1.8	2.9	mA
Supply Current	IIN					3.3	MA
Trim-Adjustment Range	ΔV <sub>OUT</sub>	(Figure 1)		±15	±25		mV
Turn-On Settling Time	ton	To ±0.01% of final value			5		μs
Output Noise Voltage (Note 3)		0.1Hz ≤ f ≥ 10Hz			1.5		µVр-р
	eN	10Hz ≤ f ≥ 1kHz			1.3	2.8	μV <sub>RMS</sub>
Temperature Hysteresis		(Note 4)			20		ppm
Long-Term Stability	ΔV <sub>OUT</sub>	$\Delta t = 1000 hr$			20		ppm

### **ELECTRICAL CHARACTERISTICS—(+4.096V)**

 $(V_{IN} = +10V, I_{OUT} = 0mA, T_A = T_{MIN} \text{ to } T_{MAX}, \text{ unless otherwise noted. Typical values are at } T_A = +25^{\circ}C.)$ 

PARAMETER	SYMBOL	CONDITIONS	TA	MIN	TYP	MAX	UNITS
Input Voltage Range	VIN			8		40	V
Output Voltage	Vout		+25°C	4.092	4.096	4.100	V
Output VoltageTemperature Coefficient (Note 1)	TCV <sub>OUT</sub>				2	20	ppm/°C
Line Degulation (Note 2)	ΔV <sub>OUT</sub> /	01/ 11/ 1 101/	+25°C		2	7	nnm//
Line Regulation (Note 2)	$\Delta V_{IN}$	8V ≤ V <sub>IN</sub> ≥ 40V				45	- ppm/V
	ΔV <sub>OUT</sub> / Δl <sub>OUT</sub>	Sourcing: 0mA ≤ I <sub>OUT</sub> ≤ 15mA	+25°C		1	6	ppm/mA
Load Regulation (Note 2)						15	
		Sinking: -15mA ≤ I <sub>OUT</sub> ≤ 0mA	+25°C		1	6	
						30	
0 10 1	1		+25°C		1.9	3.1	Λ
Supply Current	I <sub>IN</sub>					3.5	mA
Trim-Adjustment Range	ΔV <sub>OUT</sub>	(Figure 1)		±24	±40		mV
Turn-On Settling Time	ton	To ±0.01% of final value			5		μs
Output Noise Voltage (Note 3)	eN	0.1Hz ≤ f ≥ 10Hz			2.4		µVp-p
		10Hz ≤ f ≥ 1kHz			2.0	4.0	μV <sub>RMS</sub>
Temperature Hysteresis		(Note 4)			20		ppm
Long-Term Stability	ΔV <sub>OUT</sub>	$\Delta t = 1000 hr$			20		ppm

### **ELECTRICAL CHARACTERISTICS—(+5.0V)**

 $(V_{IN} = +10V, I_{OUT} = 0mA, T_A = T_{MIN} \text{ to } T_{MAX}, \text{ unless otherwise noted. Typical values are at } T_A = +25^{\circ}\text{C.})$ 

PARAMETER	SYMBOL	CONDITIONS	TA	MIN	TYP	MAX	UNITS
Input Voltage Range	VIN			8		40	V
Output Voltage	Vout		+25°C	4.995	5.000	5.005	V
Output Voltage Temperature Coefficient (Note 1)	TCV <sub>OUT</sub>				2	20	ppm/°C
Line Regulation (Note 2)	ΔV <sub>OUT</sub> /	01/ 51/ > 401/	+25°C		2	7	nnm//
Line Regulation (Note 2)	$\Delta V_{IN}$	8V ≤ V <sub>IN</sub> ≥ 40V				45	- ppm/V
Load Regulation (Note 2)		Sourcing: 0mA ≤ I <sub>OUT</sub> ≤ 15mA	+25°C		1	6	ppm/mA
	$\Delta V_{ ext{OUT}} /$					15	
	$\Delta$ l $_{ m OUT}$	Sinking: -15mA ≤ I <sub>OUT</sub> ≤ 0mA	+25°C		1	6	
						30	
0 10 1	line		+25°C		2.0	3.3	mA
Supply Current	I <sub>IN</sub>					3.7	MA
Trim-Adjustment Range	$\Delta V_{OUT}$	(Figure 1)		±30	±50		mV
Turn-On Settling Time	ton	To ±0.01% of final value			5		μs
Output Noise Voltage (Note 3)		0.1Hz ≤ f ≥ 10Hz			3.0		µVp-р
	eN	10Hz ≤ f ≥ 1kHz			2.5	5.0	μV <sub>RMS</sub>
Temperature Hysteresis		(Note 4)			20		ppm
Long-Term Stability	ΔV <sub>OUT</sub>	$\Delta t = 1000 hr$			20		ppm

Note 1: Temperature coefficient is measured by the box method; i.e., the maximum ΔV<sub>OUT</sub> is divided by ΔT x V<sub>OUT</sub>.

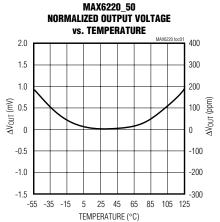
Note 2: Line regulation (ΔV<sub>OUT</sub> / (V<sub>OUT</sub> x ΔV<sub>IN</sub>)) and load regulation (ΔV<sub>OUT</sub> / (V<sub>OUT</sub> x ΔI<sub>OUT</sub>)) are measured with pulses and do not include output voltage changes due to die-temperature changes.

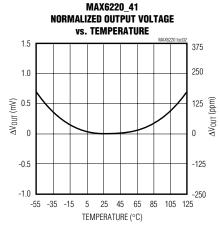
**Note 3:** Noise specifications are guaranteed by design.

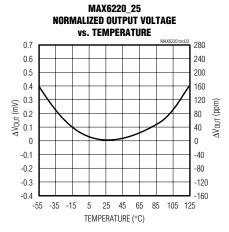
Note 4: Temperature hysteresis is specified at T<sub>A</sub> = +25°C by measuring V<sub>OUT</sub> before and after changing temperature by +25°C.

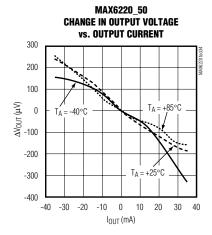
### **Typical Operating Characteristics**

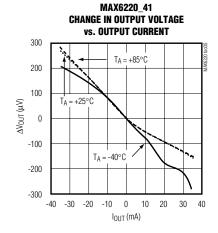
 $(V_{IN} = +10V, I_{OUT} = 0mA, T_A = +25$ °C, unless otherwise noted.)

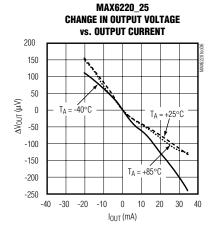


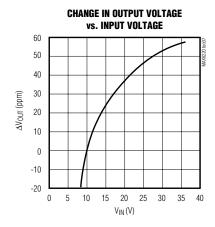


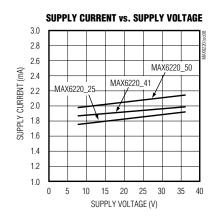


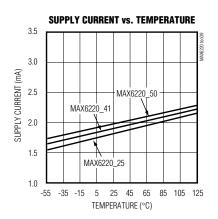






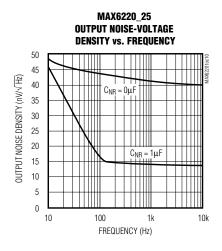


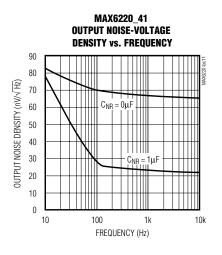


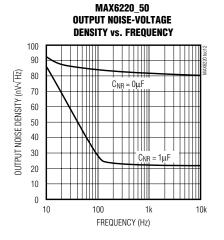


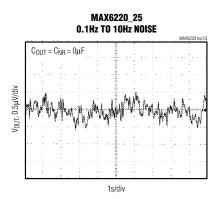
### Typical Operating Characteristics (continued)

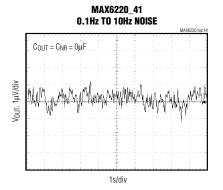
 $(V_{IN} = +10V, I_{OUT} = 0mA, T_A = +25$ °C, unless otherwise noted.)

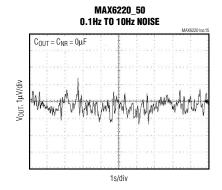


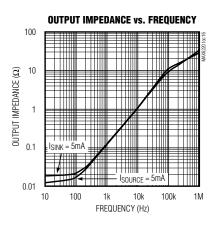


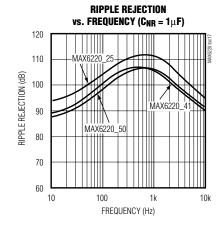


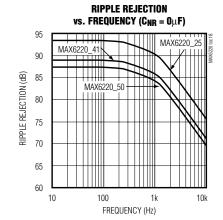






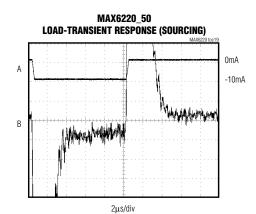






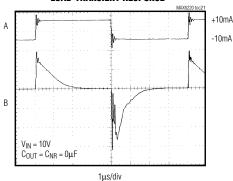
### Typical Operating Characteristics (continued)

 $(V_{IN} = +10V, I_{OUT} = 0mA, T_A = +25$ °C, unless otherwise noted.)



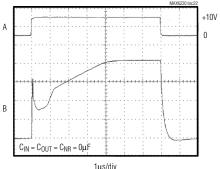
A:  $I_{OUT}$ , 10mA/div (SOURCING) B:  $V_{OUT}$ , 500 $\mu$ V/div

#### MAX6220\_50 Load-transient response



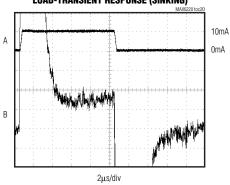
A:  $I_{OUT}$  ( $\pm 10$ mA SOURCE AND SINK), 20mA/div, AC COUPLED B:  $V_{OUT}$ , 20mV/div, AC COUPLED

#### MAX6220\_41 Turn-on and Turn-off transient response



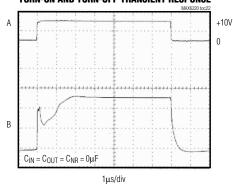
A: V<sub>IN</sub>, 10V/div B: V<sub>OUT</sub>, 1V/div

### MAX6220\_50 Load-transient response (sinking)



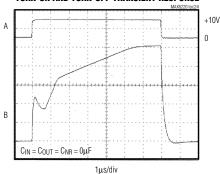
A:  $I_{OUT}$ , 10mA/div (SINKING) B:  $V_{OUT}$ ,  $500\mu\text{V/div}$ 

### TURN-ON AND TURN-OFF TRANSIENT RESPONSE



A: V<sub>IN</sub>, 10V/div B: V<sub>OUT</sub>, 1V/div

### MAX6220\_50 TURN-ON AND TURN-OFF TRANSIENT RESPONSE



A: V<sub>IN</sub>, 10V/div B: V<sub>OUT</sub>, 1V/div

### **Pin Description**

PIN	NAME FUNCTION	
1, 7, 8	I.C.	Internally Connected. <b>Do not use</b> .
2	IN	Positive Power-Supply Input
3	NR	Noise Reduction. Optional capacitor connection for wideband noise reduction. Leave open if not used (Figure 2).
4	GND	Ground
5	TRIM	External Trim Input. Allows ±1% output adjustment (Figure 1). Leave open if not used.
6	OUT	Voltage Reference Output

### **Detailed Description**

### **Temperature Stability**

The MAX6220 is a highly stable, low-noise voltage reference that uses a low-power temperature-compensation scheme to achieve laboratory-standard temperature stability. This produces a nearly flat temperature curve, yet does not require the power associated with heated references.

The output voltage can be trimmed 0.6% (min) by connecting a  $10k\Omega$  potentiometer between OUT and GND, and connecting its tap to the TRIM pin (Figure 1). The external trimming does not affect temperature stability.

#### **Noise Reduction**

To augment wideband noise reduction, add a 1µF capacitor to the NR pin (Figure 2). Larger values do not improve noise appreciably (see Typical Operating Characteristics).

Noise in the power-supply input can affect output noise, but can be reduced by adding an optional bypass capacitor to the IN pin and GND.

### **Bypassing**

The MAX6220 is stable with capacitive load values from 0µF to 100µF, for all values of load current. Adding an output bypass capacitor can help reduce noise and output glitching caused by load transients.

### **Applications Information**

### **Negative Regulator**

Figure 3 shows how both a +5V and -5V precision reference can be obtained from a single unregulated +5V supply. The MAX865 generates approximately  $\pm 9V$  to operate both the MAX6220 reference and the MAX480 inverting amplifier. The +5V is inverted by the MAX480. Resistor R1 is optional, and may be used to trim the  $\pm 5V$  references. R2 and R4 should be matched, both in absolute resistance and temperature coefficient. R3 is optional, and is adjusted to set the -5V reference.

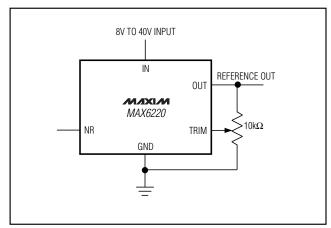


Figure 1. Output Voltage Adjustment

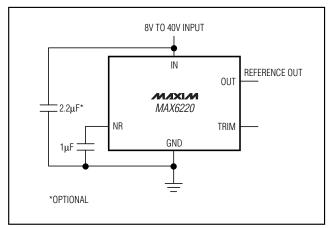


Figure 2. Noise-Reduction Capacitor

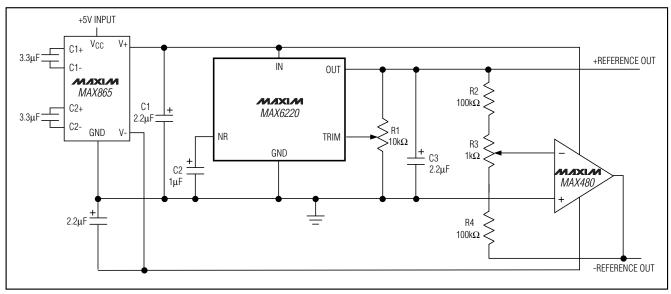
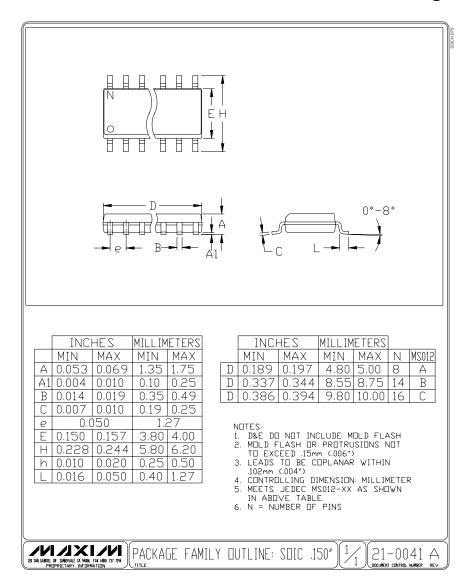


Figure 3. +5V and -5V References from a Single +5V Supply

**Chip Information** 

TRANSISTOR COUNT: 435

### Package Information



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