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Tel: +86-755-8981 8866 Fax: +86-755-8427 6832 Email & Skype: info@chipsmall.com Web: www.chipsmall.com Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China



General Description

The MAX6427–MAX6438 are single/dual-level battery monitors with internal hysteresis. These devices are offered with single (MAX6427/MAX6428/MAX6429, MAX6433/ MAX6434/MAX6435) or dual (MAX6430/MAX6431/ MAX6432, MAX6436/MAX6437/MAX6438) low-battery output indicators, which can be used to indicate three battery conditions: battery is good (operate system in normal mode), battery is weak (operate system in low-power mode), or battery is empty (disable the system).

These devices are ideal for monitoring single lithium-ion (Li+) cells or multicell alkaline/NiCd/NiMH power supplies. When the power-supply voltage drops below the specified low threshold, the low-battery output asserts. When the voltage rises above the specified high threshold, the low-battery output is deasserted following a 140ms minimum timeout period. The timeout period ensures that the supply voltages have stabilized before power-converter or micro-processor activity is enabled.

The MAX6427–MAX6438 family is available with several monitoring options. The MAX6427/MAX6428/MAX6429 offer factory-trimmed battery-monitor thresholds, with a single low-battery output. The MAX6430/MAX6431/MAX6432 also feature factory-trimmed thresholds, but have two low-battery outputs.

The MAX6433–MAX6438 have user-adjustable threshold voltages permitting the user to select the hysteresis range, and consequently the sensitivity of the system to noise. A wide hysteresis range also prevents chattering that can result due to battery recovery after load removal. Single low-battery outputs are supplied by the MAX6433/MAX6434/MAX6435 and dual low-battery outputs by the MAX6436/MAX6437/MAX6438.

The MAX6427–MAX6438 family is available with three output logic options for convenient interface with system power circuitry or microprocessors. The family is offered in small SOT23 and SOT143 packages and is fully specified over temperature.

Applications

Battery-Powered Systems (Single-Cell Li+ or Two/Three-Cell NiMH, NiCd, Alkaline)

Cell Phones/Cordless Phones	Pagers
Portable Medical Devices	PDAs
Electronic Toys	MP3 Players

Typical Operating Circuit appears at end of data sheet.

Features

 Factory-Trimmed or User-Adjustable Threshold Options

Factory-Trimmed Low Thresholds from 1.6V to 2.1V or 2.6V to 3.1V in 100mV Increments (MAX6427–MAX6432) Factory-Trimmed High Thresholds from 2.3V to 2.6V or 3.3V to 3.6V in 100mV Increments (MAX6427–MAX6432) User-Adjustable Low/High Thresholds with 615mV Internal Reference (MAX6433–MAX6438)

- Low Current (1µA, typ)
- Single or Dual Low-Battery Outputs
- Push-Pull LBO, Open-Drain LBO, and Open-Drain LBO Options
- ♦ 140ms Minimum LBO Timeout Period
- Immune to Short Battery Voltage Transients
- Guaranteed Valid LBO Logic State to BATT = 1.0V
- ♦ -40°C to +85°C Operating Temperature Range
- Small 3, 4, 5 and 6-Pin SOT Packages
- No External Components Required (MAX6427–MAX6432)

_Ordering Information

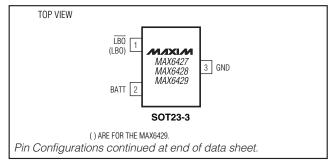
PART	TEMP RANGE	PIN-PACKAGE	
MAX6427UR-T	-40°C to +85°C	3 SOT23-3	

Ordering Information continued at end of data sheet.

[†]The MAX6427–MAX6432 are available with factory-trimmed battery-monitor thresholds. Select the desired thresholds using Tables 1 or 2 and enter the corresponding letters in the part number by replacing "__".

Devices are available in both leaded and lead-free packaging. Specify lead-free by replacing "-T" with "+T" when ordering. **Note:** There are 26 standard versions with a required order minimum of 2500 pieces. Sample stock is generally held on the standard versions only (see Standard Versions Table). Required order increment is 10,000 pieces for nonstandard versions. Contact factory for availability. All devices available in tape-and-reel only.

Pin Configurations



Maxim Integrated Products 1

For pricing, delivery, and ordering information, please contact Maxim/Dallas Direct! at 1-888-629-4642, or visit Maxim's website at www.maxim-ic.com.

ABSOLUTE MAXIMUM RATINGS

BATT, V _{DD} to GND0.3V to +6.0V HTHIN, LTHIN Inputs to GND0.3V to (V _{DD} + 0.3V)	
Open-Drain LBO/LBO, LBOH/LBOH,	
LBOL/LBOL to GND0.3V to +6.0V	
Push-Pull LBO/LBO, LBOH/LBOH,	
LBOL/LBOL to GND0.3V to (BATT or V _{DD} + 0.3V)	
Input Current (all pins)20mA	
Output Current (all pins)20mA	

Continuous Power Dissipation ($T_A = +70^{\circ}C$)
3-Pin SOT23-3 (derate 4.0mW/°C above +70°C)320mW
4-Pin SOT143-4 (derate 4.0mW/°C above +70°C)320mW
5-Pin SOT23-5 (derate 7.1mW/°C above +70°C)571mW
6-Pin SOT23-6 (derate 8.7mW/°C above +70°C)696mW
Operating Temperature Range40°C to +85°C
Junction Temperature+150°C
Storage Temperature Range65°C to +150°C
Lead Temperature (soldering, 10s)+300°C

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS

(BATT = 1.0V to 5.5V or V_{DD} = 1.6V to 5.5V, T_A = -40°C to +85°C, unless otherwise specified. Typical values are at T_A = +25°C.) (Note 1)

PARAMETER	SYMBOL	CONDITIONS	MIN	ТҮР	MAX	UNITS
Operating Voltage Bange		$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$			5.5	v
Operating Voltage Range	VBATT	$T_A = 0^{\circ}C \text{ to } +70^{\circ}C$	1.0		5.5	V
Operating Voltage Range	V _{DD}	$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$	1.6		5.5	V
BATT Supply Current	IBATT	V _{BATT} = 3.7V, no load (MAX6427–MAX6432)		1	1.5	μA
V _{DD} Supply Current	IDD	V _{DD} = 3.7V, no load (MAX6433–MAX6438)		1	1.5	μA
FACTORY-FIXED THRESHOLDS	(MAX6427-	MAX6432)				
HTH Threshold (Note 2)	Vнтн	Factory-fixed options, BATT rising, LBO deasserted when BATT rises above V _{HTH} (MAX6427/MAX6428/MAX6429)	-2.5		+2.5	%
LTH Threshold (Note 2)	VLTH	Factory-fixed options, BATT falling, LBO asserted when BATT falls below V _{LTH} -2.5 (MAX6427/MAX6428/MAX6429)		+2.5	%	
HTH- Threshold (Note 2)	V _{HTH-}	Factory-fixed options, BATT falling, LBOH asserted when BATT falls below V _{HTH-} (MAX6430/MAX6431/MAX6432)-2.5		+2.5	%	
HTH+ Threshold (Note 3)	V _{HTH+}	Factory-fixed options, BATT rising, LBOH deasserted when BATT rises above V _{HTH+} (MAX6430/MAX6431/MAX6432)	-2.5		+2.5	%
LTH- Threshold (Note 2)	V _{LTH-}	Factory-fixed options, BATT falling, LBOL asserted when BATT falls below V _{LTH-} -2.5 (MAX6430/MAX6431/MAX6432)			+2.5	%
LTH+ Threshold (Note 3)	V _{LTH+}	Factory-fixed options, BATT rising, LBOL deasserted when BATT rises above -2.5 V _{LTH+} (MAX6430/MAX6431/MAX6432)		+2.5	%	

ELECTRICAL CHARACTERISTICS (continued)

(BATT = 1.0V to 5.5V or V_{DD} = 1.6V to 5.5V, T_A = -40°C to +85°C, unless otherwise specified. Typical values are at T_A = +25°C.) (Note 1)

PARAMETER	SYMBOL	OL CONDITIONS		ТҮР	МАХ	UNITS		
JSER-ADJUSTABLE THRESHOLDS (MAX6433–MAX6438)								
HTHIN Threshold (Note 4)	Vhthin	User-adjustable options, HTHIN rising, LBO is deasserted when HTHIN rises above V _{HTHIN-} , V _{REF} = 615mV (MAX6433/MAX6434/MAX6435)	-2.5		+2.5	%		
LTHIN Threshold (Note 4)	VLTHIN	User-adjustable options, LTHIN falling, LBOH is asserted when LTHIN falls below V _{LTHIN-} , V _{REF} = 615mV (MAX6433/MAX6434/MAX6435)	-2.5		+2.5	%		
HTHIN+ Threshold (Note 4)	VHTHIN+	User-adjustable options, HTHIN rising, LBOH is deasserted when HTHIN rises above V _{HTHIN+} , VREF = 615mV (MAX6436/MAX6437/MAX6438)	-2.5		+2.5	%		
HTHIN- Threshold (Note 5)	Vhthin-	User-adjustable options, HTHIN falling, LBOH is asserted when HTHIN falls below VHTHIN-, VREF = 585mV (MAX6436/MAX6437/MAX6438)	-2.5		+2.5	%		
LTHIN+ Threshold (Note 4)	VLTHIN+	User-adjustable options, LTHIN rising, LBOL is deasserted when LTHIN rises above V _{LTHIN+} , V _{REF} = 615mV (MAX6436/MAX6437/MAX6438)	-2.5		+2.5	%		
LTHIN- Threshold (Note 5)	VLTHIN-	User-adjustable options, LTHIN falling, LBOL is asserted when LTHIN falls below V _{LTHIN-} , V _{REF} = 585mV (MAX6436/MAX6437/MAX6438)	-2.5		+2.5	%		
HTHIN/LTHIN Leakage Current (Note 6)	ILKG	VHTHIN or VLTHIN > 400mV			20	nA		

ELECTRICAL CHARACTERISTICS (continued)

(BATT = 1.0V to 5.5V or V_{DD} = 1.6V to 5.5V, T_A = -40°C to +85°C, unless otherwise specified. Typical values are at T_A = +25°C.) (Note 1)

PARAMETER	SYMBOL	CONDITIONS	MIN	ТҮР	MAX	UNITS	
LOW-BATTERY OUTPUTS (LBO, LBO, LBOH, LBOH, LBOL, LBOL)							
Timeout Period	tlbop	BATT or V _{DD} /HTHIN/LTHIN rising above threshold	140	210	280	ms	
Delay	t _{LBOD}	BATT or V _{DD} /HTHIN/LTHIN falling below threshold		100		μs	
		BATT or $V_{DD} \ge 1.6V$, $I_{SINK} = 100\mu A$, asserted low			0.3		
LBO/LBOL/LBOH Output LOW (Push-Pull or Open-Drain)	VOL	BATT or $V_{DD} \ge 2.7V$, $I_{SINK} = 1.2mA$, asserted low			0.3	V	
		BATT or $V_{DD} \ge 4.5V$, $I_{SINK} = 3.2mA$, asserted low			0.4	-	
		BATT or $V_{DD} \ge 1.6V$, $I_{SOURCE} = 200\mu A$, deasserted	0.8 × Vs	UPPLY			
LBO Output HIGH (Push-Pull Only) (Note 7)	V _{OH}	BATT or $V_{DD} \ge 2.7V$, $I_{SOURCE} = 500\mu A$, deasserted	0.8 × VSUPPLY			V	
		BATT or $V_{DD} \ge 4.5V$, $I_{SOURCE} = 800\mu A$, deasserted	0.8 × VSUPPLY				
LBO Output Leakage Current (Open-Drain Only)	ILKG	LBO deasserted			500	nA	
		BATT or $V_{DD} \ge 1.6V$, $I_{SINK} = 100\mu A$, deasserted low			0.3		
LBO/LBOL/LBOH Output LOW (Open-Drain Only)	Vol	BATT or V _{DD} ≥ 2.7V, I _{SINK} = 1.2mA, deasserted low			0.3	V	
		BATT or V _{DD} ≥ 4.5V, I _{SINK} = 3.2mA, deasserted low			0.4		
LBO Output Leakage Current (Open-Drain Only)	Ilkg	LBO asserted			500	nA	

Note 1: Production testing done at $T_A = +25^{\circ}C$, limits over temperature guaranteed by design only.

Note 2: Percentage of selected factory-fixed HTH- or LTH- threshold voltage.

Note 3: Percentage of 1.05 × (selected factory-fixed threshold voltage).

Note 4: Percentage of 615mV voltage reference.

Note 5: Percentage of 585mV voltage reference.

Note 6: Guaranteed by design.

Note 7: VSUPPLY is BATT or VDD.

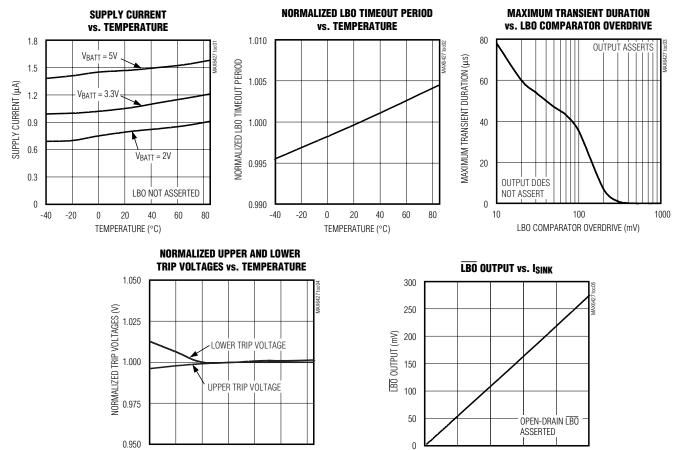
Typical Operating Characteristics

 $(T_A = +25^{\circ}C, \text{ unless otherwise noted.})$

-40 -20 0

20 40 60 80

TEMPERATURE (°C)



0

2

4

6

I_{SINK} (mA)

8

10

MAX6427-MAX6438

MAX6427-MAX6438

	PI				
Max6427/ Max6428/ Max6429	MAX6430/ MAX6431/ MAX6432	Max6433/ Max6434/ Max6435	Max6436/ Max6437/ Max6438	NAME	FUNCTION
1	_	4	_	LBO/ LBO	Low-Battery Output. \overline{LBO}/LBO is asserted when BATT drops below V _{LTH} (MAX6427/MAX6428/MAX6429) or when LTHIN (MAX6433/MAX6434/MAX6435) drops below V _{LTHIN} , and remains asserted for at least 140ms after BATT exceeds V _{HTH} (MAX6427/MAX6428/MAX6429) or V _{HTHIN} (MAX6433/MAX6435).
2	3	_	_	BATT	Battery-Voltage Input. Input for battery-voltage threshold monitors and device power supply for factory-trimmed threshold versions.
3	1	2	2	GND	Ground
_	2	_	5	LBOH/ LBOH	Low-Battery Output High. $\overline{\text{LBOH}}$ /LBOH is asserted when BATT drops below $V_{\text{HTH-}}$ (MAX6430/MAX6431/MAX6432) or when HTHIN drops below $V_{\text{HTHIN-}}$ (MAX6436/MAX6437/ MAX6438). It remains asserted for at least 140ms after BATT exceeds $V_{\text{HTH+}}$ (MAX6430/MAX6431/MAX6432) or HTHIN rises above $V_{\text{HTHIN+}}$ (MAX6436/MAX6437/MAX6438).
	4	_	4	LBOL/ LBOL	Low-Battery Output Low. $\overline{LBOL}/LBOL$ is asserted when BATT drops below V _{LTH-} (MAX6430/MAX6431/MAX6432) or when LTHIN drops below V _{LTHIN-} (MAX6436/MAX6437/MAX6438). It remains asserted for at least 140ms after BATT exceeds V _{LTH+} (MAX6430/MAX6431/MAX6432) or LTHIN rises above V _{LTHIN+} (MAX6436/MAX6437/MAX6438).
		1	1	HTHIN	HTH Threshold Monitor Input. A resistor-divider network sets the high threshold associated with LBOH/LBOH.
	_	3	3	LTHIN	LTH Threshold Monitor Input. A resistor-divider network sets the low threshold associated with LBOL/LBOL.
_	_	5	6	V _{DD}	V _{DD} Supply Voltage Input. Device power supply for adjustable threshold versions.

M/X/M

Pin Description

Detailed Description

The MAX6427–MAX6438 family is available with several monitoring options. The MAX6427/MAX6428/MAX6429 offer factory-trimmed battery-monitor thresholds, with a single low-battery output (see Figure 1). The MAX6430/MAX6431/MAX6432 also feature factory-trimmed thresholds, but have two low-battery outputs (see Figure 2). The factory-trimmed thresholds of the MAX6427–MAX6432 eliminate the requirement for external components.

The user sets the threshold voltages for the MAX6433–MAX6438, which permit the user to select the hysteresis range, and consequently the sensitivity of the system to noise. A wide hysteresis range prevents chattering that can result during battery recovery or load removal. The MAX6433/MAX6434/ MAX6435 have single low-battery outputs and the MAX6436/ MAX6437/MAX6438 have dual low-battery outputs (see Figures 3 and 4).

The MAX6427–MAX6438 combine a 615mV reference with two comparators, logic, and timing circuitry to provide the user with information about the charge state of the power-supply batteries. The MAX6427–MAX6438 monitor separate high-voltage and low-voltage thresholds to determine battery status. The output(s) can be used to signal when the battery is charged, when the battery is low, and when the battery is empty. Factory-

trimmed thresholds are ideal for monitoring single-cell Li+ or multicell alkaline/NiCd/NiMH power supplies. User-programmable versions are also available with thresholds determined using an external resistor-divider (see *Resistor-Value Selection*). The adjustable versions can monitor down to 0.62V, making them suitable for monitoring single-cell alkaline, NiMH, and NiCd applications (V_{DD} \geq 1.6V).

When the power-supply voltage drops below the specified low threshold, the low-battery output asserts (see *Low-Battery Output*). When the voltage rises above the specified high threshold following a 140ms (min) timeout period, the low-battery output is deasserted (see *Threshold Monitoring*). This ensures the supply voltage has stabilized before power-converter or microprocessor activity is enabled.

Low-Battery Output

The low-battery outputs are available in active-low (LBO, LBOH, LBOL, push-pull and open-drain) and active-high (LBO, LBOL, LBOH, open-drain) configurations. Push-pull outputs are referenced to BATT (for factory-trimmed versions) or V_{DD} (for adjustable threshold versions).

The open-drain devices can be pulled to a voltage independent of V_{DD} or BATT, up to 5.5V. This allows the

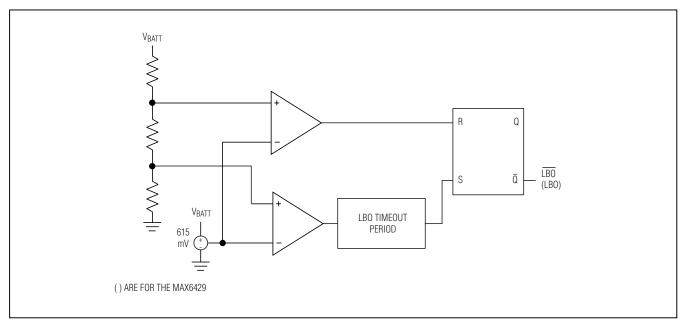


Figure 1. MAX6427/MAX6428/MAX6429 Functional Diagram

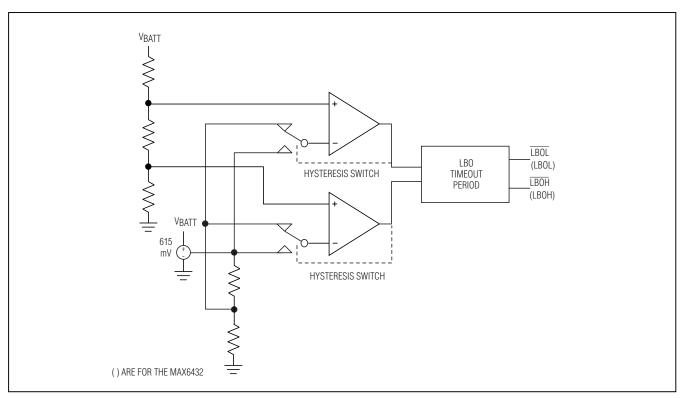


Figure 2. MAX6430/MAX6431/MAX6432 Functional Diagram

device to monitor and operate from direct battery voltage while interfacing to higher voltage microprocessors.

The MAX6427/MAX6428/MAX6429/MAX6433/MAX6434/ MAX6435 single-output voltage monitors provide a single low-battery output, LBO/LBO. The MAX6427/ MAX6428/MAX6429 fixed-threshold parts assert LBO/LBO when BATT drops below V_{LTH}. The MAX6433/MAX6434/MAX6435 adjustable-threshold parts assert LBO/LBO when the LTHIN input drops below the V_{LTHIN} threshold (615mV) (see Figure 5).

The MAX6430/MAX6431/MAX6432/MAX6436/MAX6437/ MAX6438 dual-output voltage monitors provide two lowbattery outputs, LBOH/LBOH and LBOL/LBOL. The MAX6430/MAX6431/MAX6432 fixed-threshold parts assert LBOH/LBOH when BATT drops below V_{HTH}, and assert LBOL/LBOL when BATT drops below V_{LTH}. The MAX6436/MAX6437/MAX6438 adjustable-threshold parts assert LBOH/LBOH when HTHIN drops below V_{HTHIN}-, and assert LBOL/LBOL when LTHIN drops below V_{LTHIN}- (see Figure 6).

Threshold Monitoring

The MAX6433–MAX6438 HTHIN and LTHIN inputs are high-impedance inputs to comparators. An external resistor-divider network is required between the monitored voltage, HTHIN, LTHIN, and GND to select the desired thresholds (see *Resistor-Value Selection*). The MAX6433/MAX6434/MAX6435 single-output voltage monitors assert LBO/LBO when LTHIN drops below the internal LTHIN reference (615mV). LBO/LBO is deasserted when HTHIN rises above the internal HTHIN reference level (615mV) for at least 140ms (see Figure 6).

The MAX6436/MAX6437/MAX6438 dual-output voltage monitors assert LBOL/LBOL when LTHIN drops below the LTHIN- threshold and deassert LBOL/LBOL when LTHIN rises above the LTHIN+ threshold for at least 140ms. LBOH/LBOH is asserted when HTHIN drops below the HTHIN- threshold and is deasserted when HTHIN rises above the HTHIN+ threshold for at least 140ms (see Figure 7).

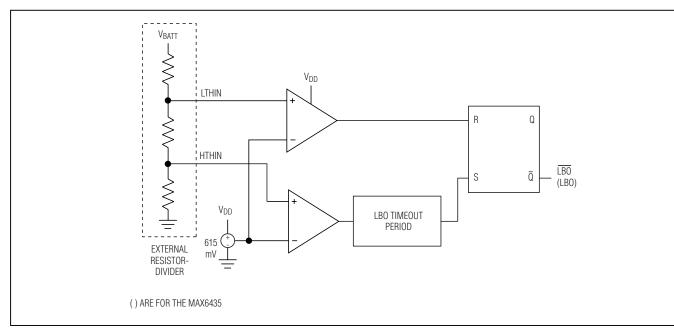


Figure 3. MAX6433/MAX6434/MAX6435 Functional Diagram

Hysteresis

Hysteresis increases the comparator's noise margin by increasing the upper threshold or decreasing the lower threshold (see Figure 8). The hysteresis prevents the output from oscillating (chattering) when BATT or V_{DD} is near the low-battery threshold. This is especially important for applications where the load on the battery creates significant fluctuations in battery voltages.

Fixed Hysteresis (MAX6427–MAX6432)

The MAX6427/MAX6428/MAX6429 total hysteresis is the difference between the factory-trimmed V_{LTH} and V_{HTH}. For the MAX6430/MAX6431/MAX6432, the total hysteresis is the difference between the factory-trimmed V_{HTH}- and V_{HTH}- and V_{LTH}- and V_{LT}- and V

Adjustable Hysteresis (MAX6433–MAX6438)

Figure 7 shows the correct connections for the external resistor-dividers. To adjust the low-battery threshold and hysteresis connect resistor R1 between V_{DD} and LTHIN, resistor R2 between LTHIN and HTHIN, and R3 between HTHIN and GND. The hysteresis for the MAX6433–MAX6438 is determined by an external resistor-divider network (see *Resistor-Value Selection* section of Applications Information). The hysteresis for the MAX6436/MAX6437/MAX6438 is the differences between V_{HTHIN} and V_{HTHIN+} (~5%) or V_{LTHIN} and V_{LTHIN+} (~5%).

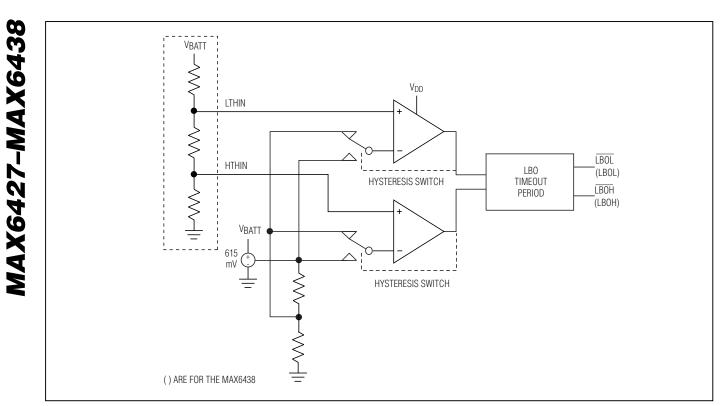


Figure 4. MAX6436/MAX6437/MAX6438 Functional Diagram

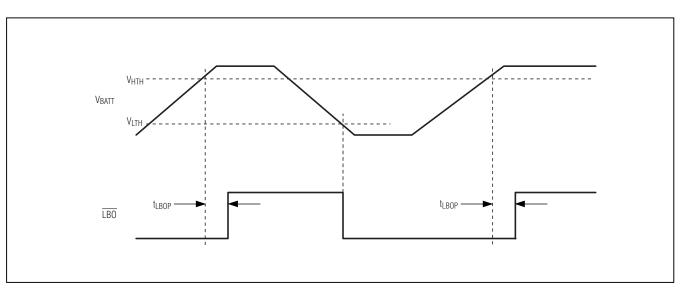


Figure 5. Single Low-Battery Output Timing

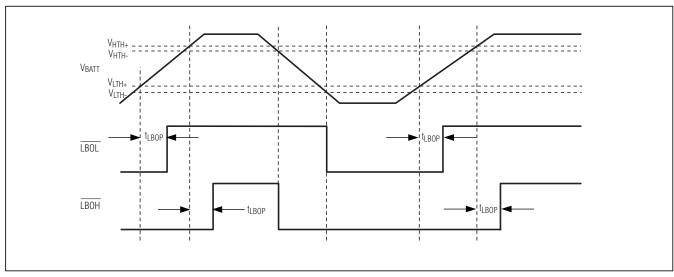


Figure 6. Dual Low-Battery Output Timing

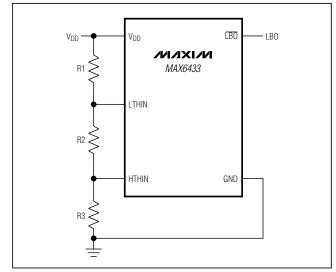


Figure 7. Adjustable Threshold Selection

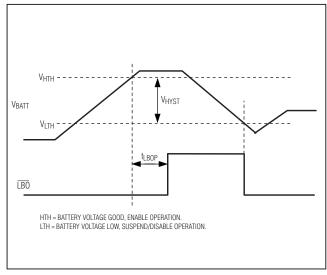


Figure 8. Hysteresis

_Applications Information

Resistor-Value Selection (Programming the Adjustable Thresholds) MAX6433/MAX6434/MAX6435

(See Figure 5)

$$V_{\text{REF}} = V_{\text{LTHIN}} = V_{\text{HTHIN}} = 615\text{mV}$$

$$V_{\text{TRIPLOW}} = V_{\text{LTH}} = V_{\text{REF}} \left(\frac{\text{R1} + \text{R2} + \text{R3}}{\text{R2} + \text{R3}}\right)$$

$$V_{\text{TRIPHIGH}} = V_{\text{HTL}} = V_{\text{REF}} \left(\frac{\text{R1} + \text{R2} + \text{R3}}{\text{R3}}\right)$$

$$R_{\text{TOTAL}} = \text{R1} + \text{R2} + \text{R3}$$

Use the following steps to determine values for R1, R2, and R3 of Figure 8.

- 1) Choose a value for RTOTAL, the sum of R1, R2, and R3. Because the MAX6433/MAX6434/MAX6435 have very high input impedances, RTOTAL can be up to 5M Ω .
- 2) Calculate R3 based on RTOTAL and the desired upper trip point:

$$R3 = \frac{V_{REF} \times R_{TOTAL}}{V_{TRIPHIGH}} = \frac{585mV \times R_{TOTAL}}{V_{TRIPHIGH}}$$

3) Calculate R2 based on RTOTAL, R3, and the desired lower trip point:

$$R2 = \frac{615mV \times R_{TOTAL}}{V_{TRIPLOW}} - R3$$

4) Calculate R1 based on RTOTAL, R3, and R2:

$$R1 = R_{TOTAL} - R2 - R3$$

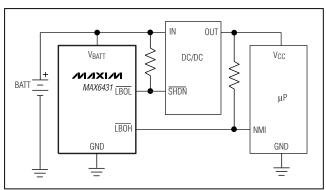


Figure 9. DC-to-DC Converter Application

MAX6436/MAX6437/MAX6438

(See Figure 6)

$$V_{REF} = V_{LTHIN-} = V_{HTHIN-} = 585mV$$

$$\overline{LBOL}/LBOL OUTPUT:$$

$$V_{TRIPLOW} = V_{LTH-} = V_{REF} \left(\frac{R1 + R2 + R3}{R2 + R3} \right)$$

$$\overline{LBOH}/LBOH OUTPUT:$$

$$V_{TRIPHIGH} = V_{HTL-} = V_{REF} \left(\frac{R1 + R2 + R3}{R3} \right)$$

$$R_{TOTAL} = R1 + R2 + R3$$

Use the following steps to determine values for R1, R2, and R3 of Figure 8.

- 1) Choose a value for RTOTAL, the sum of R1, R2, and R3. Because the MAX6436/MAX6437/MAX6438 have very high input impedances, RTOTAL can be up to 5M Ω .
- 2) Calculate R3 based on RTOTAL and the desired upper trip point:

$$R3 = \frac{V_{REF} \times R_{TOTAL}}{V_{TRIPHIGH}} = \frac{585mV \times R_{TOTAL}}{V_{TRIPHIGH}}$$

3) Calculate R2 based on RTOTAL, R3, and the desired lower trip point:

4) Calculate R1 based on RTOTAL, R3, and R2:

 $R1 = R_{TOTAL} - R2 - R3$

 $V_{HTH+} = V_{HTH-} \times 1.05, V_{LTH+} = V_{LTH-} \times 1.05$

DC-to-DC Converter Application

The MAX6430/MAX6431/MAX6432 and MAX6436/ MAX6437/MAX6438 dual-output battery monitors can be used in conjunction with a step-up DC-to-DC converter to power 5V microprocessor systems using a single Li+ cell or two to three alkaline/NiCd/NiMH cells (Figure 9). The LBOH output indicates that the battery voltage is weak, and is used to warn the microprocessor of potential problems. Armed with this information the microprocessor can reduce system power consumption. The LBOL output indicates the battery is empty, and system power should be disabled. By connecting LBOL to the SHDN pin of the DC-to-DC converter, power to the microprocessor is removed.



Microprocessor power will not return until the battery has recharged to a voltage greater than HTH+ (Figure 6).

Li+ Battery Charger Application

The MAX6430/MAX6431/MAX6432 and MAX6436/ MAX6437/MAX6438 dual-output battery monitors can be used in conjunction with a battery charger to provide a system with additional information about the battery charge state (Figure 10). Many battery chargers, such as the MAX1879, provide the user with a \overline{CHG} output, which tells the system that the battery is charged. The MAX6430/MAX6431/MAX6432 and MAX6436/MAX6437/MAX6438 dual-output battery monitors provide two outputs, which can be used to relay the battery condition to the system. This information is useful in determining which system resources can be powered by the battery at the current charge state.

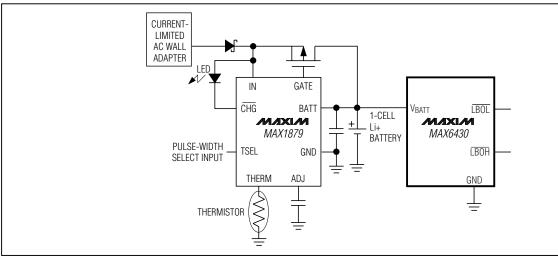
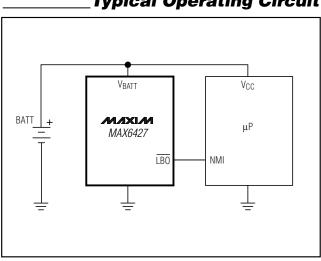


Figure 10. Lithium-Ion Cell Charger Application (using MAX1879)



Typical Operating Circuit

MAX6427-MAX6438

Ordering Information (continued)

PART	TEMP RANGE	PIN-PACKAGE
MAX6428UR-T	-40°C to +85°C	3 SOT23-3
MAX6429UR-T	-40°C to +85°C	3 SOT23-3
MAX6430US-T	-40°C to +85°C	4 SOT143-4
MAX6431US-T	-40°C to +85°C	4 SOT143-4
MAX6432US-T	-40°C to +85°C	4 SOT143-4
MAX6433 UK-T	-40°C to +85°C	5 SOT23-5
MAX6434 UK-T	-40°C to +85°C	5 SOT23-5
MAX6435 UK-T	-40°C to +85°C	5 SOT23-5
MAX6436UT-T	-40°C to +85°C	6 SOT23-6
MAX6437UT-T	-40°C to +85°C	6 SOT23-6
MAX6438UT-T	-40°C to +85°C	6 SOT23-6

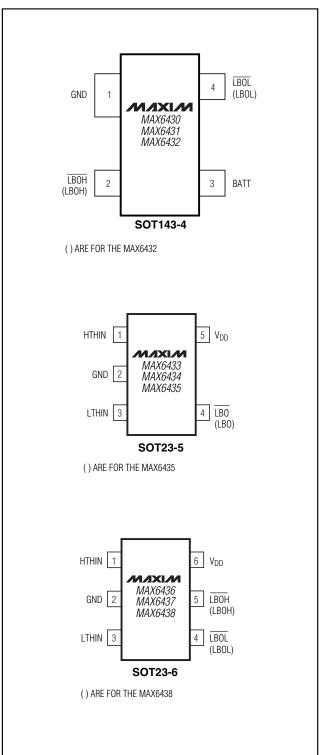
[†]The MAX6427–MAX6432 are available with factory-trimmed battery-monitor thresholds. Select the desired thresholds using Tables 1 or 2 and enter the corresponding letters in the part number by replacing "__".

Devices are available in both leaded and lead-free packaging. Specify lead-free by replacing "-T" with "+T" when ordering.

Note: There are 26 standard versions with a required order minimum of 2500 pieces. Sample stock is generally held on the standard versions only (see Standard Versions Table). Required order increment is 10,000 pieces for nonstandard versions. Contact factory for availability. All devices available in tape-and-reel only.

Chip Information

TRANSISTOR COUNT: 905 PROCESS TECHNOLOGY: BICMOS



Pin Configurations (continued)

Table 1. Factory-Trimmed Lower and Upper Threshold Combinations for Single-Cell Li+ or Three-Cell Alkaline/NiCd/NiMH Applications (MAX6427–MAX6432)

		UPPER THRESHOLD (HTH/HTH-)						
٥	VOLTAGES	3.3V	3.4V	3.5V	3.6V			
	2.6V	AG	AH	AI	AJ			
RESH LTH-)	2.7V	BG	BH	BI	BJ			
THRI	2.8V	CG	СН	CI	CJ			
	2.9V	DG	DH	DI	DJ			
OWER (LT	3.0V	EG	EH	EI	EJ			
	3.1V	FG	FH	FI	FJ			

Table 2. Factory-Trimmed Lower and Upper Threshold Combinations for Two-Cell Alkaline/NiCd/NiMH Applications (MAX6427–MAX6432)

		UPPER THRESHOLD (HTH/HTH-)					
0	VOLTAGES	2.3V	2.4V	2.5V	2.6V		
OLI	1.6V	KQ	KR	KS	KT		
RESH -TH-)	1.7V	LQ	LR	LS	LT		
THR	1.8V	MQ	MR	MS	MT		
ER 1 (LTI	1.9V	NQ	NR	NS	NT		
MO.	2.0V	OQ	OR	OS	OT		
	2.1V	PQ	PR	PS	PT		

Standard Versions

PART	TOP MARK
MAX6430MR	KAEL
MAX6430OR	KAEM
MAX6431DH	KAES
MAX6431EH	KAET
MAX6431EI	KAEU
MAX6431MR	KAEN
MAX6431OR	KAEO
MAX6433UK	ADVH
MAX6434UK	ADVI
MAX6435UK	ADVJ
MAX6436UT	ABAB
MAX6437UT	ABAC
MAX6438UT	ABAD

PART	TOP MARK			
MAX6427DH	FZKZ			
MAX6427EH	FZLA			
MAX6427EI	FZLB			
MAX6427MR	FZLF			
MAX6427OR	FZLG			
MAX6428DH	FZLC			
MAX6428EH	FZLD			
MAX6428EI	FZLE			
MAX6428MR	FZLH			
MAX6428OR	FZLI			
MAX6430DH	KAEP			
MAX6430EH	KAEQ			
MAX6430EI	KAER			

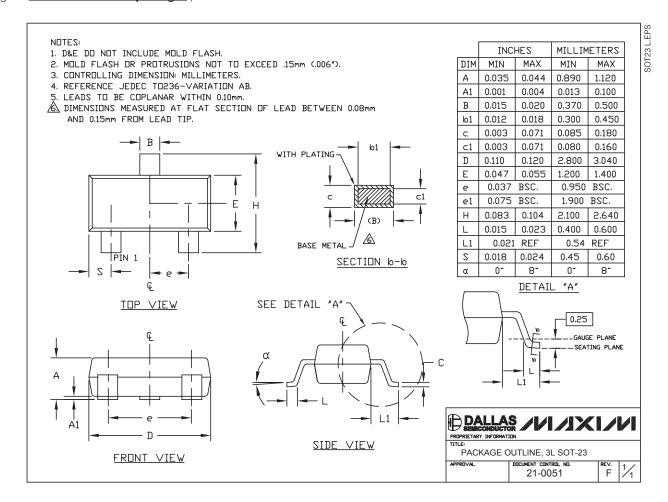
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MAX6427-MAX6438

						Selector Guide	
PART	FACTORY- FIXED THRESHOLDS	USER- ADJUSTABLE THRESHOLDS	ACTIVE- LOW PUSH- PULL OUTPUT	ACTIVE- LOW OPEN- DRAIN OUTPUT	ACTIVE- HIGH OPEN- DRAIN OUTPUT	SINGLE OUTPUT	DUAL OUTPUT
MAX6427	\checkmark	_	✓			\checkmark	
MAX6428	✓	_		✓		\checkmark	
MAX6429	✓				✓	\checkmark	
MAX6430	✓	_	✓				✓
MAX6431	✓	_		✓	_	_	✓
MAX6432	✓	_	—	—	✓	_	✓
MAX6433	_	✓	✓			\checkmark	
MAX6434		✓		~		\checkmark	
MAX6435	—	✓	_	_	✓	✓	_
MAX6436	—	✓	~		—	_	~
MAX6437	—	✓	—	~	—	_	✓
MAX6438	_	✓			✓		✓

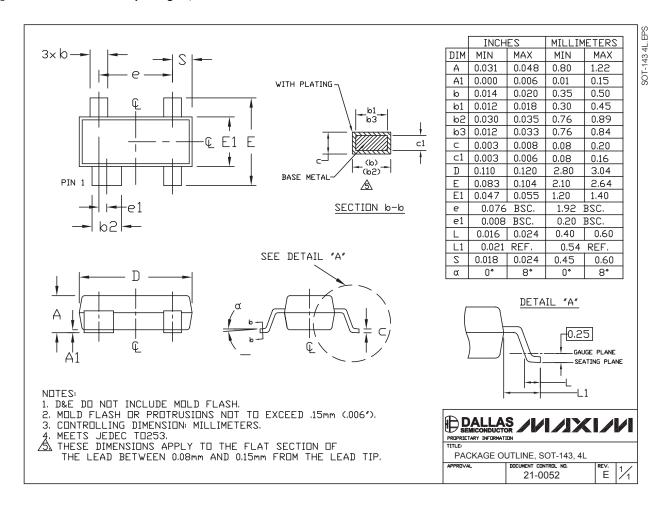
_Package Information

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_Package Information (continued)

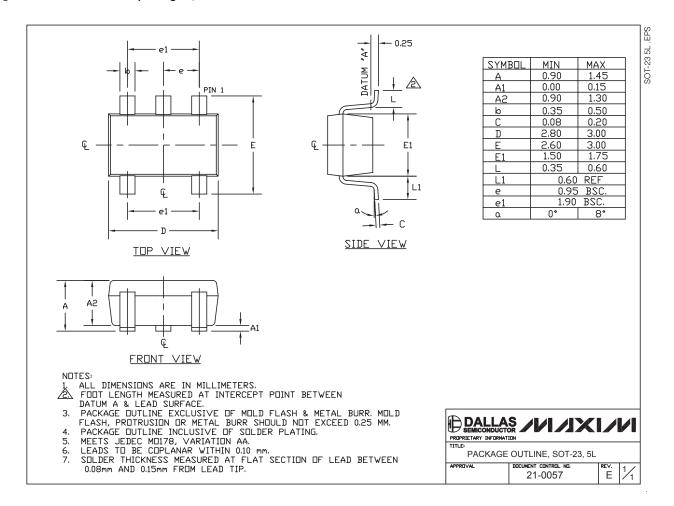
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M/X/M

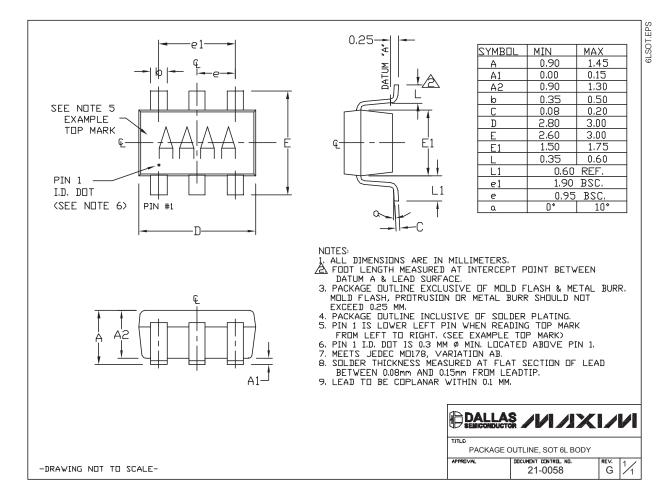
Package Information (continued)

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Package Information (continued)

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