# imall

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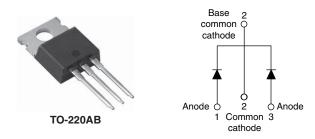
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**Vishay Semiconductors** 

### Schottky Rectifier, 2 x 15 A



PRODUCT SUMMARY					
Package	TO-220AB				
I <sub>F(AV)</sub>	2 x 15 A				
V <sub>R</sub>	45 V				
V <sub>F</sub> at I <sub>F</sub>	See Electrical table				
I <sub>RM</sub> max.	100 mA at 125 °C				
T <sub>J</sub> max.	150 °C				
Diode variation	Common cathode				
E <sub>AS</sub>	10 mJ				

#### **FEATURES**

- 150 °C T<sub>J</sub> operation
- Low forward voltage drop
- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Guard ring for enhanced ruggedness and long term reliability
- Compliant to RoHS Directive 2002/95/EC
- Designed and qualified according to JEDEC-JESD47
- Halogen-free according to IEC 61249-2-21 definition
  (-N3 only)

#### DESCRIPTION

This center tap Schottky rectifier has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS	VALUES	UNITS			
I <sub>F(AV)</sub>	Rectangular waveform (per device)	30	А			
V <sub>RRM</sub>		35/45	V			
I <sub>FRM</sub>	T <sub>C</sub> = 123 °C (per leg)	30	٨			
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	1020	А			
V <sub>F</sub>	20 A <sub>pk</sub> , T <sub>J</sub> = 125 °C	0.6	V			
TJ	Range	- 65 to 150	°C			

VOLTAGE RATINGS					
PARAMETER	SYMBOL	VS-MBR3045CTPbF	VS-MBR3045CT-N3	UNITS	
Maximum DC reverse voltage	V <sub>R</sub>	45	45	V	
Maximum working peak reverse voltage	V <sub>RWM</sub>	45	40	v	

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL TEST CONDITIONS		VALUES	UNITS		
Maximum average per leg		$T_{\rm C}$ = 123 °C, rated V <sub>B</sub>				
forward current per device	I <sub>F(AV)</sub>	$T_{\rm C} = 123$ O, lated $V_{\rm R}$		30		
Peak repetitive forward current per leg	I <sub>FRM</sub>	Rated V <sub>R</sub> , square wave, 20 kHz, T <sub>C</sub> = 123 $^{\circ}$ C		30		
Non-repetitive peak surge current	epetitive peak surge current		Following any rated load condition and with rated V <sub>RRM</sub> applied	1020	A	
	1 OM	Surge applied at rated load conditions halfwave, single phase, 60 Hz		200		
Non-repetitive avalanche energy per leg	E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 2 A, L = 5 mH		10	mJ	
Repetitive avalanche current per leg	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu s$ Frequency limited by $T_J$ maximum $V_A$ = 1.5 x $V_R$ typical		2	А	

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RoHS

COMPLIANT

HALOGEN

FREE



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ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONE	TEST CONDITIONS		UNITS	
Maximum forward voltage drop		30 A	T <sub>J</sub> = 25 °C	0.76		
	V <sub>FM</sub> <sup>(1)</sup>	20 A	T, = 125 °C	0.6	V	
		30 A	1j = 125 C	0.72		
Maximum instantaneous reverse current	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	Detect DC wells as	1	mA	
		T <sub>J</sub> = 125 °C	Rated DC voltage	100		
Threshold voltage	V <sub>F(TO)</sub>	T <sub>.1</sub> = T.1 maximum		0.29	V	
Forward slope resistance	r <sub>t</sub>	ij = ij maximum		13.6	mΩ	
Maximum junction capacitance	CT	$V_R = 5 V_{DC}$ (test signal range	100 kHz to 1 MHz) 25 °C	800	pF	
Typical series inductance	L <sub>S</sub>	Measured from top of termin	al to mounting plane	8.0	nH	
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>		10 000	V/µs	

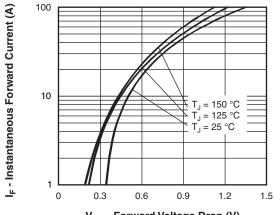
#### Note

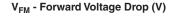
 $^{(1)}$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Maximum junction temperature range	TJ		- 65 to 150	၁°		
Maximum storage temperature range	T <sub>Stg</sub>		- 65 to 175			
Maximum thermal resistance, junction to case per leg	R <sub>thJC</sub>	DC operation	1.5			
Typical thermal resistance, case to heatsink	R <sub>thCS</sub>	Mounting surface, smooth and greased Only for TO-220	0.50 °C/W 50			
Maximum thermal resistance, junction to ambient	R <sub>thJA</sub>	DC operation For D <sup>2</sup> PAK and TO-262				
Approximate weight			2	g		
Approximate weight			0.07	OZ.		
Mounting torque		Non-lubricated threads	6 (5)	kgf ⋅ cm		
Mounting torque maximum	1	Non-Iudricated threads	12 (10)	(lbf · in)		
Marking device		Case style TO-220AB	MBR3045CT			



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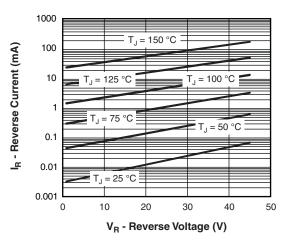


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

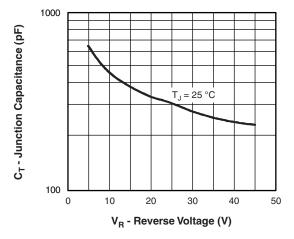


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

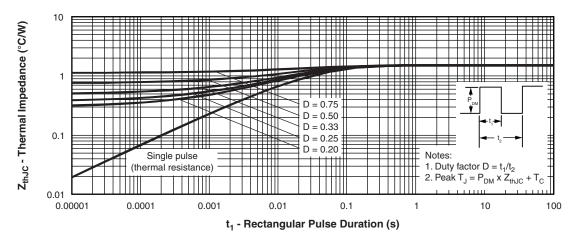
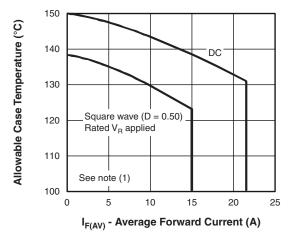


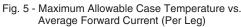
Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics (Per Leg)

Revision: 26-Aug-11 3 Document Number: 94292 For technical questions within your region: DiodesAmericas@vishay.com, DiodesAsia@vishay.com, DiodesEurope@vishay.com THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT www.vishay.com/doc?91000



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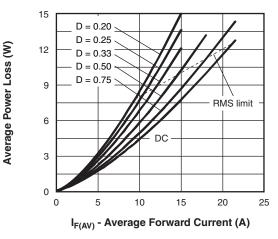


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

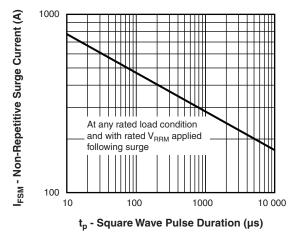


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

#### Note

- <sup>(1)</sup> Formula used:  $T_C = T_J (Pd + Pd_{REV}) \times R_{thJC}$ ;
  - $\begin{array}{l} \mathsf{Pd} = \mathsf{Forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \times \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{fig.} \ \mathsf{6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{Inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \times \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{Rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$



**Vishay Semiconductors** 

### **ORDERING INFORMATION TABLE**

**Device code** 

е	VS-	VS- MBR 30		45	СТ	PbF	
		2	3	4	5	6	

- 1 Vishay Semiconductors product
- 2 Schottky MBR series
  - Current rating (30 = 30 A)
  - Voltage ratings (045 = 45 V)
    - CT = Essential part number
  - Environmental digit

- PbF = Lead (Pb)-free and RoHS compliant
- -N3 = Halogen-free, RoHS compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)					
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION		
VS-MBR3045CTPbF	50	1000	Antistatic plastic tube		
VS-MBR3045CT-N3	50	1000	Antistatic plastic tube		

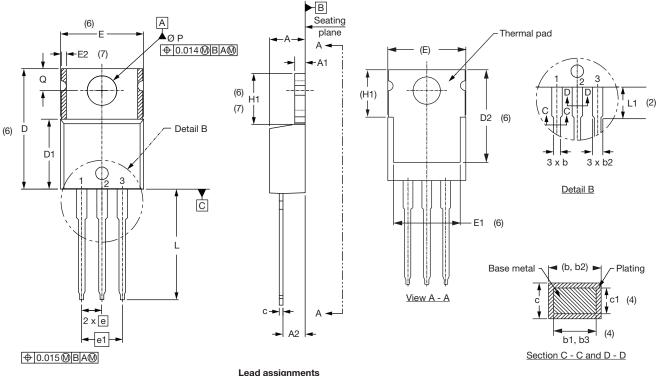
LINKS TO RELATED DOCUMENTS				
Dimensions		www.vishay.com/doc?95222		
Part marking information	TO-220AB PbF	www.vishay.com/doc?95225		
	TO-220AB -N3	www.vishay.com/doc?95028		

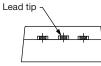


**Vishay Semiconductors** 

**TO-220AB** 

#### **DIMENSIONS** in millimeters and inches





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<u>Diodes</u> 1. - Anode/open 2. - Cathode

2.	-	Cathode
3.	-	Anode

SYMBOL	MILLIM	IETERS	INC	NOTES	Γ	
	MIN.	MAX.	MIN.	MAX.	NOTES	
A	4.25	4.65	0.167	0.183		
A1	1.14	1.40	0.045	0.055		
A2	2.56	2.92	0.101	0.115		]
b	0.69	1.01	0.027	0.040		1
b1	0.38	0.97	0.015	0.038	4	
b2	1.20	1.73	0.047	0.068		
b3	1.14	1.73	0.045	0.068	4	]
С	0.36	0.61	0.014	0.024		
c1	0.36	0.56	0.014	0.022	4	
D	14.85	15.25	0.585	0.600	3	
D1	8.38	9.02	0.330	0.355		
D2	11.68	12.88	0.460	0.507	6	

#### Notes

- <sup>(1)</sup> Dimensioning and tolerancing as per ASME Y14.5M-1994
- <sup>(2)</sup> Lead dimension and finish uncontrolled in L1
- <sup>(3)</sup> Dimension D, D1 and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- <sup>(4)</sup> Dimension b1, b3 and c1 apply to base metal only
- <sup>(5)</sup> Controlling dimensions: inches
- $^{\rm (6)}$  Thermal pad contour optional within dimensions E, H1, D2 and E1

MILLIMETERS INCHES SYMBOL NOTES MIN. MAX. MIN. MAX. 0.414 10.11 10.51 0.398 Е 3,6 E1 6.86 8.89 0.270 0.350 6 E2 0.76 0.030 7 --2.41 2.67 0.095 0.105 е 0.208 e1 4.88 5.28 0.192 H1 6.09 6.48 0.240 0.255 6,7 13.52 14.02 0.532 0.552 Т 3.32 3.82 0.131 0.150 2 L1 ØΡ 3.54 3.73 0.139 0.147 0.102 Q 2.60 3.00 0.118 90° to 93° 90° to 93° θ

Conforms to JEDEC outline TO-220AB

- $^{(7)}$  Dimensions E2 x H1 define a zone where stamping and singulation irregularities are allowed
- <sup>(8)</sup> Outline conforms to JEDEC TO-220, except A2 (maximum) and D2 (minimum) where dimensions are derived from the actual package outline



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