

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

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# **Switch-mode Power Rectifier** 100 V, 60 A

#### **Features and Benefits**

- Low Forward Voltage: 0.72 V @ 125°C
- Low Power Loss/High Efficiency
- High Surge Capacity
- 175°C Operating Junction Temperature
- 60 A Total (30 A Per Diode Leg)
- NRVB Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb-Free and are RoHS Compliant

### **Applications**

- Power Supply Output Rectification
- Power Management
- Instrumentation

### **Mechanical Characteristics:**

- Case: Epoxy, Molded
- Epoxy Meets UL 94 V-0 @ 0.125 in
- Weight (Approximately): 1.9 Grams (TO-220)

1.7 Grams (D<sup>2</sup>PAK-3)

- Finish: All External Surfaces Corrosion Resistant and Terminal Leads are Readily Solderable
- Lead Temperature for Soldering Purposes:

260°C Max. for 10 Seconds

Human Body Model = 3B• ESD Rating:

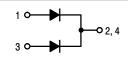
Machine Model = C



### ON Semiconductor®

www.onsemi.com

### SCHOTTKY BARRIER RECTIFIER 60 AMPERES, 100 VOLTS



TO-220

**CASE 221A** 

STYLE 6



### **MARKING DIAGRAM**

**AYWW** B60H100G AKA



D<sup>2</sup>PAK-3 **CASE 418B** STYLE 3



= Assembly Location

= Year WW = Work Week B60H100 = Device Code = Pb-Free Package G AKA = Polarity Designator

### ORDERING INFORMATION

Device	Package	Shipping <sup>†</sup>			
MBR60H100CTG	TO-220 (Pb-Free)	50 Units/Rail			
MBRB60H100CTT4G	D <sup>2</sup> PAK-3 (Pb-Free)	800/ Tape & Reel			
NRVBB60H100CTT4G	D <sup>2</sup> PAK-3 (Pb-Free)	800/ Tape & Reel			

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

### MAXIMUM RATINGS (Per Diode Leg)

Rating	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V <sub>RRM</sub> V <sub>RWM</sub> V <sub>R</sub>	100	V
Average Rectified Forward Current (T <sub>C</sub> = 155°C) Per Diode Per Device	I <sub>F(AV)</sub>	30 60	А
Peak Repetitive Forward Current (Square Wave, 20 kHz, T <sub>C</sub> = 151°C)	I <sub>FRM</sub>	60	Α
Nonrepetitive Peak Surge Current (Surge applied at rated load conditions halfwave, single phase, 60 Hz)	I <sub>FSM</sub>	350	Α
Operating Junction Temperature Range (Note 1)	TJ	- 55 to +175	°C
Storage Temperature Range	T <sub>stg</sub>	-65 to +175	°C
Voltage Rate of Change (Rated V <sub>R</sub> )	dV/dt	10,000	V/μs
Controlled Avalanche Energy (see test conditions in Figures 9 and 10)	W <sub>AVAL</sub>	400	mJ
ESD Ratings: Machine Model = C Human Body Model = 3B		> 400 > 8000	V

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

### THERMAL CHARACTERISTICS

Characteristic	Symbol	Value	Unit
Maximum Thermal Resistance			°C/W
Junction-to-Case (Min. Pad)	$R_{ heta JC}$	1.0	
Junction-to-Ambient (Min. Pad)	$R_{ hetaJA}$	70	

### **ELECTRICAL CHARACTERISTICS** (Per Diode Leg)

Characteristic	Symbol	Min	Тур	Max	Unit
Maximum Instantaneous Forward Voltage (Note 2) $ \begin{aligned} &(i_F=30\text{ A}, T_J=25^\circ\text{C})\\ &(i_F=30\text{ A}, T_J=125^\circ\text{C})\\ &(i_F=60\text{ A}, T_J=25^\circ\text{C})\\ &(i_F=60\text{ A}, T_J=125^\circ\text{C}) \end{aligned} $	VF	- - -	0.80 0.68 0.93 0.81	0.84 0.72 0.98 0.84	V
Maximum Instantaneous Reverse Current (Note 2) (Rated DC Voltage, $T_J = 125^{\circ}C$ ) (Rated DC Voltage, $T_J = 25^{\circ}C$ )	i <sub>R</sub>		2.0 0.0013	10 0.01	mA

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

<sup>1.</sup> The heat generated must be less than the thermal conductivity from Junction-to-Ambient:  $dP_D/dT_J < 1/R_{\theta JA}$ .

<sup>2.</sup> Pulse Test: Pulse Width = 300 μs, Duty Cycle ≤ 2.0%.

### TYPICAL CHARACTERISTICS

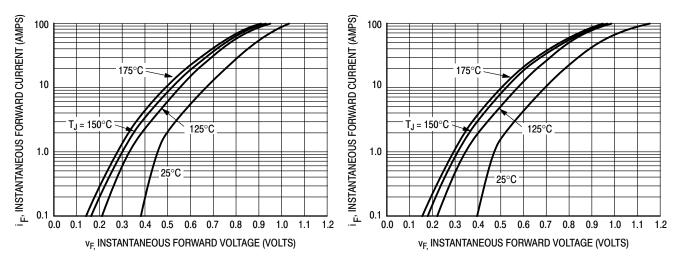
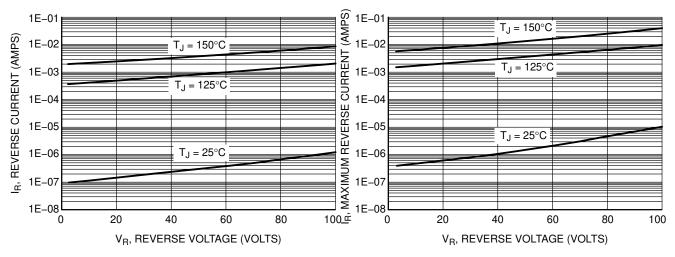


Figure 1. Typical Forward Voltage

Figure 2. Maximum Forward Voltage



**Figure 3. Typical Reverse Current** 

Figure 4. Maximum Reverse Current

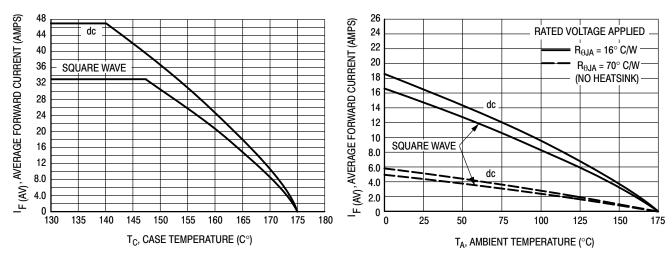
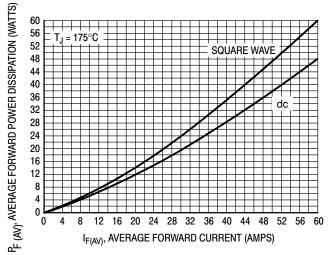


Figure 5. Current Derating, Case Per Leg

Figure 6. Current Derating, Ambient Per Leg

### **TYPICAL CHARACTERISTICS**



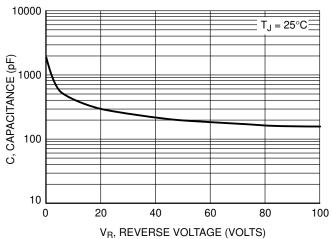


Figure 7. Forward Power Dissipation

Figure 8. Capacitance

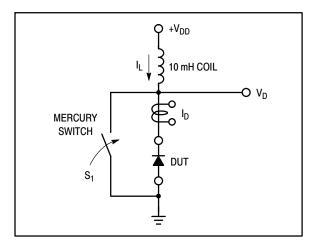


Figure 9. Test Circuit

The unclamped inductive switching circuit shown in Figure 9 was used to demonstrate the controlled avalanche capability of this device. A mercury switch was used instead of an electronic switch to simulate a noisy environment when the switch was being opened.

When  $S_1$  is closed at  $t_0$  the current in the inductor  $I_L$  ramps up linearly; and energy is stored in the coil. At  $t_1$  the switch is opened and the voltage across the diode under test begins to rise rapidly, due to di/dt effects, when this induced voltage reaches the breakdown voltage of the diode, it is clamped at  $BV_{DUT}$  and the diode begins to conduct the full load current which now starts to decay linearly through the diode, and goes to zero at  $t_2$ .

By solving the loop equation at the point in time when  $S_1$  is opened; and calculating the energy that is transferred to the diode it can be shown that the total energy transferred is equal to the energy stored in the inductor plus a finite amount of energy from the  $V_{DD}$  power supply while the diode is in breakdown (from  $t_1$  to  $t_2$ ) minus any losses due to finite component resistances. Assuming the component resistive

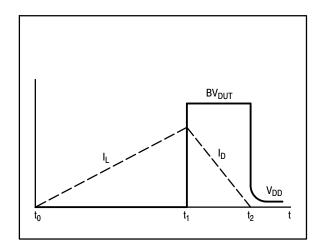


Figure 10. Current-Voltage Waveforms

elements are small Equation (1) approximates the total energy transferred to the diode. It can be seen from this equation that if the  $V_{DD}$  voltage is low compared to the breakdown voltage of the device, the amount of energy contributed by the supply during breakdown is small and the total energy can be assumed to be nearly equal to the energy stored in the coil during the time when  $S_1$  was closed, Equation (2).

### **EQUATION (1):**

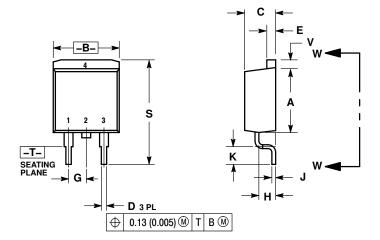
$$W_{AVAL} \approx \frac{1}{2} LI_{LPK}^2 \left( \frac{BV_{DUT}}{BV_{DUT} / BU_{DUT}} \right)$$

### **EQUATION (2):**

$$W_{AVAL} \approx \frac{1}{2}LI_{LPK}^2$$

### **PACKAGE DIMENSIONS**

D<sup>2</sup>PAK-3 CASE 418B-04 **ISSUE L** 



#### NOTES:

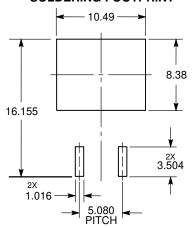
- NOTES:
  1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
  2. CONTROLLING DIMENSION: INCH.
  3. 418B-01 THRU 418B-03 OBSOLETE, NEW STANDARD 418B-04.

	INC	HES	MILLIN	IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.340	0.380	8.64	9.65
В	0.380	0.405	9.65	10.29
C	0.160	0.190	4.06	4.83
D	0.020	0.035	0.51	0.89
Е	0.045	0.055	1.14	1.40
F	0.310	0.350	7.87	8.89
G	0.100	BSC	2.54 BSC	
Η	0.080	0.110	2.03	2.79
7	0.018	0.025	0.46	0.64
K	0.090	0.110	2.29	2.79
L	0.052	0.072	1.32	1.83
М	0.280	0.320	7.11	8.13
N	0.197 REF 0.079 REF 0.039 REF		5.00 REF 2.00 REF 0.99 REF	
Р				
R				
S	0.575	0.625	14.60	15.88
٧	0.045	0.055	1.14	1.40



- PIN 1. ANODE 2. CATHODE 3. ANODE 4. CATHODE
- VARIABLE CONFIGURATION ZONE U М VIEW W-W VIEW W-W VIEW W-W

### **SOLDERING FOOTPRINT\***

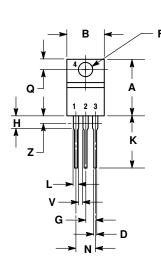


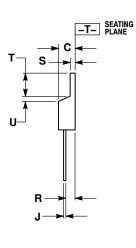
DIMENSIONS: MILLIMETERS

\*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

### PACKAGE DIMENSIONS

TO-220 CASE 221A-09 **ISSUE AH** 





- NOTES:
  1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. CONTROLLING DIMENSION: INCH.
- DIMENSION Z DEFINES A ZONE WHERE ALL BODY AND LEAD IRREGULARITIES ARE

	INCHES		MILLIN	IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.570	0.620	14.48	15.75
В	0.380	0.415	9.66	10.53
С	0.160	0.190	4.07	4.83
D	0.025	0.038	0.64	0.96
F	0.142	0.161	3.61	4.09
G	0.095	0.105	2.42	2.66
Н	0.110	0.161	2.80	4.10
J	0.014	0.024	0.36	0.61
K	0.500	0.562	12.70	14.27
L	0.045	0.060	1.15	1.52
N	0.190	0.210	4.83	5.33
Q	0.100	0.120	2.54	3.04
R	0.080	0.110	2.04	2.79
S	0.045	0.055	1.15	1.39
T	0.235	0.255	5.97	6.47
U	0.000	0.050	0.00	1.27
٧	0.045		1.15	
Z		0.080		2.04

STYLE 6:

PIN 1. ANODE

- 2. CATHODE
- ANODE CATHODE

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