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December 2009

MBRP1545N Schottky Barrier Rectifier

Features

- · Low forward voltage drop
- High frequency properties and switching speedGuard ring for over-voltage protection

Applications

- · Switched mode power supply
- Freewheeling diodes



Absolute Maximum Ratings $T_A=25$ °C unless otherwise noted

Symbol	Parameter	Value	Units
V_{RRM}	Maximum Repetitive Reverse Voltage	45	V
V_R	Maximum DC Reverse Voltage	45	V
I _{F(AV)}	Average Rectified Forward Current @ T _C = 100°C	15	Α
I _{FSM}	Non-repetitive Peak Surge Current (per diode) 60Hz Single Half-Sine Wave	150	А
T _{J,} T _{STG}	Operating Junction and Storage Temperature	-65 to +150	°C

Thermal Characteristics

Symbol	Parameter	Value	Units
$R_{ heta JC}$	Maximum Thermal Resistance, Junction to Case (per diode)	3.0	°C/W

Electrical Characteristics (per diode)

Symbol	Parameter	Value	Units	
V _{FM} *	$\label{eq:maximum Instantaneous Forward Voltage} $$I_F = 7.5A$$$I_F = 7.5A$$$I_F = 15A$$$I_F = 15A$$$$	$T_{C} = 25 ^{\circ}\text{C}$ $T_{C} = 125 ^{\circ}\text{C}$ $T_{C} = 25 ^{\circ}\text{C}$ $T_{C} = 125 ^{\circ}\text{C}$	0.65 0.57 0.80 0.65	V
I _{RM} *	Maximum Instantaneous Reverse Current @ rated V _R	$T_C = 25 ^{\circ}C$ $T_C = 125 ^{\circ}C$	1 40	mA

^{*} Pulse Test: Pulse Width=300µs, Duty Cycle=2%

Typical Performance Characteristics

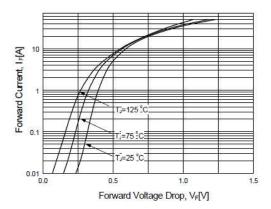


Figure 1. Typical Forward Voltage Characteristics (per diode)

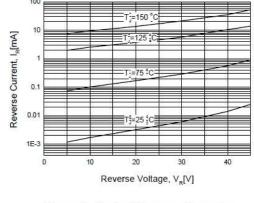


Figure 2. Typical Reverse Current vs. Reverse Voltage (per diode)

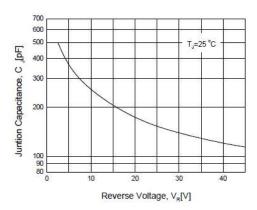


Figure 3. Typical Junction Capacitance (per diode)

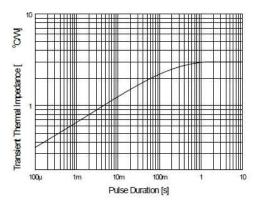


Figure 4. Thermal Impedance Characteristics (per diode)

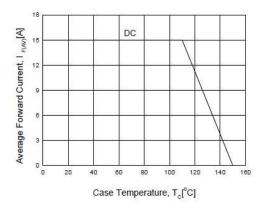


Figure 5. Forward Current Derating Curve

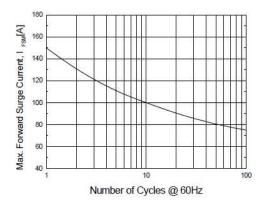
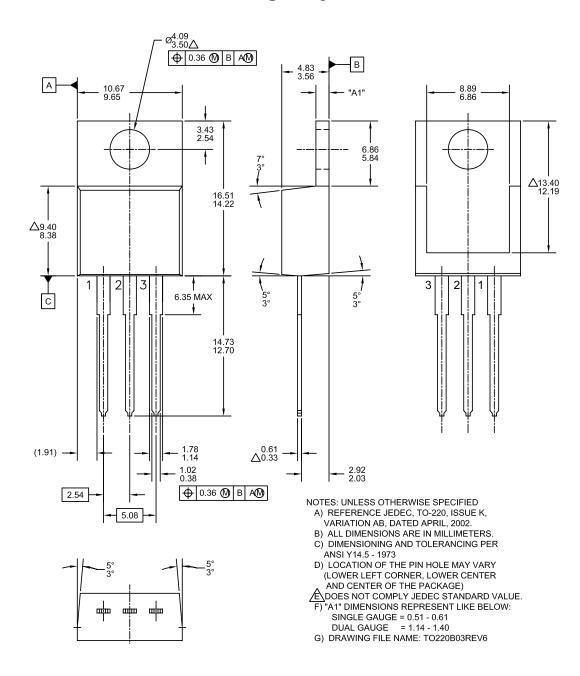


Figure 6. Non-Repetitive Surge Current (per diode)

Physical Dimensions

TO-220



Dimensions in Millimeters





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Definition of Terms				
Datasheet Identification	Product Status	Definition		
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Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.		
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