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+2, +4, +8 1.1 GHz Low Power Prescaler with Stand-By Mode

Description

The MC12093 is a single modulus prescaler for low power frequency division of a 1.1 GHz high frequency input signal. MOSAIC VTM technology is utilized to achieve low power dissipation of 6.75 mW at a minimum supply voltage of 2.7 V.

On-chip output termination provides output current to drive a 2.0 pF (typical) high impedance load. If additional drive is required for the prescaler output, an external resistor can be added parallel from the OUT pin to GND to increase the output power. Care must be taken not to exceed the maximum allowable current through the output.

Divide ratio control inputs SW1 and SW2 select the required divide ratio of $\div 2$, $\div 4$, or $\div 8$.

Stand-By mode is featured to reduce current drain to $50 \ \mu A$ typical when the standby pin SB is switched LOW disabling the prescaler.

Features

- 1.1 GHz Toggle Frequency
- Supply Voltage 2.7 V to 5.5 Vdc
- Low Power 3.0 mA Typical
- Operating Temperature = -40° C to 85° C
- Divide by 2, 4 or 8 Selected by SW1 and SW2 Pins
- On-Chip Termination
- These Devices are Pb-Free, Halogen Free and are RoHS Compliant

Table 1. FUNCTIONAL TABLE

SW	SW2	Divide Ratio
L	L	8
н	L	4
L	н	4
н	Н	2

1. SW1 & SW2: $H = (V_{CC} - 0.5 V)$ to V_{CC} ; L = Open.

2. SB: H = 2.0 V to V_{CC} , L = GND to 0.8 V.

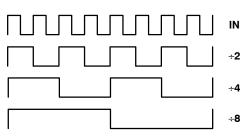


Figure 1. Function Chart



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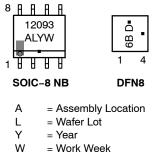


SOIC-8 NB D SUFFIX CASE 751-07



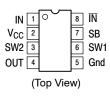
DFN8 MN SUFFIX CASE 506AA





= Pb-Free Package

PIN CONNECTIONS



A LOW on the Stand-By Pin 7 disables the device.

ORDERING INFORMATION

Device	Package	Shipping
MC12093DG	SOIC-8 NB (Pb-Free)	98 Units/Tube
MC12093DR2G	SOIC-8 NB (Pb-Free)	2500 Tape & Reel
MC12093MNR4G	DFN8 (Pb-Free)	1000 Tape & Reel

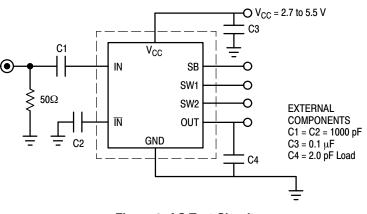




Table 2. ATTRIBUTES

Characteristics	Value			
Internal Input Pulldown Resistor	N/A			
Internal Input Pullup Resistor	N/A			
ESD Protection Human Body Model Machine Model Charged Device Model	> 4 kV > 200 V > 2 kV			
Moisture Sensitivity, Indefinite Time Out of Drypack (Note 1)	Pb-Free Pkg			
SOIC-8 NB DFN8	Level 1 Level 1			
Flammability Rating Oxygen Index: 28 to 34	UL 94 V-0 @ 0.125 in			
Transistor Count	125 Devices			
Meets or exceeds JEDEC Spec EIA/JESD78 IC Latchup Test				

1. For additional information, see Application Note AND8003/D.

Table 3. MAXIMUM RATINGS

Symbol	Rating	Value	Unit
V _{CC}	Power Supply Voltage, Pin 2	–0.5 to 6.0	Vdc
T _A	Operating Temperature Range	-40 to 85	°C
T _{stg}	Storage Temperature Range	–65 to 150	°C
Ι _Ο	Maximum Output Current, Pin 4	4.0	mA
θ _{JC}	Thermal Resistance (Junction-to-Case) (Note 1) DFN8	35 to 40	°C/W

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

NOTE: ESD data available upon request.

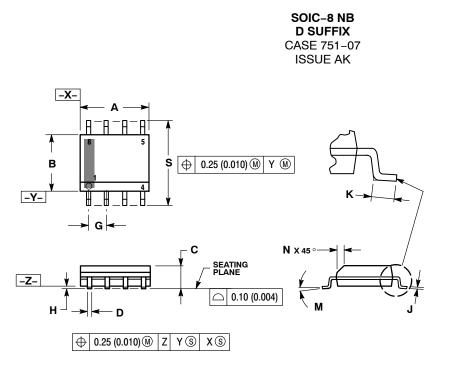
1. JEDEC standard multilayer board – 2S2P (2 signal, 2 power). For DFN8 only, thermal exposed pad must be connected to a sufficient thermal conduit. Electrically connect to the most negative supply (GND) or leave unconnected, floating open.

Symbol	Characteristic	Min	Тур	Max	Unit
ft	Toggle Frequency (Sine Wave)	0.1	1.4	1.1	GHz
I _{CC}	Supply Current	-	3.0	4.5	mA
ISB	Stand-By Current	-	120	200	μA
V _{IH1}	Stand-By Input HIGH (SB)	2.0	-	V _{CC}	V
V _{IL1}	Stand-By Input LOW (SB)	Gnd	-	0.8	V
V _{IH2}	Divide Ratio Control Input HIGH (SW1 & SW2)	V _{CC} – 0.5	V _{CC}	V _{CC} + 0.5	V
V _{IL2}	Divide Ratio Control Input LOW (SW1 & SW2)	OPEN	OPEN	OPEN	
V _{OUT}	Output Voltage Swing (2.0 pF Load) Output Frequency 12.5–350 MHz (Note 1) Output Frequency 350–400 MHz (Note 2) Output Frequency 400–450 MHz (Note 3) Output Frequency 450–550 MHz (Note 4)	0.6 0.5 0.4 0.3	0.80 0.70 0.55 0.45	- - -	V _{pp}
V _{IN}	Input Voltage Sensitivity 250–1100 MHz 100–250 MHz	100 400		1000 1000	mVpp

Table 4. ELECTRICAL CHARACTERISTICS (V $_{CC}$ = 2.7 to 5.5 V; T $_{A}$ = –40 to 85°C)

Input frequency 1.1 GHz, ÷8, minimum output frequency of 12.5 MHz.
Input frequency 700–800 MHz, ÷2.
Input frequency 800–900 MHz, ÷2.
Input frequency 900–1100 MHz, ÷2.

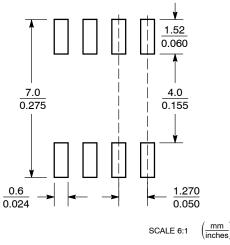
PACKAGE DIMENSIONS



- NOTES: 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. 2. CONTROLLING DIMENSION: MILLIMETER.
- З.
- DIMENSION A AND B DO NOT INCLUDE MOLD PROTRUSION. MAXIMUM MOLD PROTRUSION 0.15 (0.006) PER SIDE. 4.
- PER SIDE. DIMENSION D DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.127 (0.005) TOTAL IN EXCESS OF THE D DIMENSION AT MAXIMUM MATERIAL CONDITION. 751-01 THRU 751-06 ARE OBSOLETE. NEW STANDARD IS 751-07. 5.
- 6.

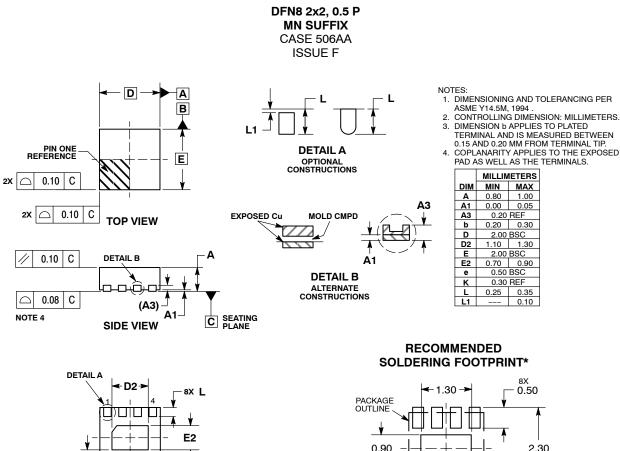
	MILLIMETERS		INCHES			
DIM	MIN	MAX	MIN MAX			
Α	4.80	5.00	0.189	0.197		
В	3.80	4.00	0.150	0.157		
С	1.35	1.75	0.053	0.069		
D	0.33	0.51	0.013	0.020		
G	1.27	1.27 BSC		0.050 BSC		
Н	0.10	0.25	0.004	0.010		
J	0.19	0.25	0.007	0.010		
κ	0.40	1.27	0.016	0.050		
М	0 °	8 °	0 °	8 °		
Ν	0.25	0.50	0.010	0.020		
s	5.80	6.20	0.228	0.244		

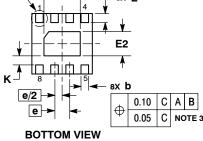
SOLDERING FOOTPRINT*

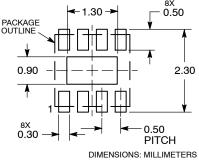


*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

PACKAGE DIMENSIONS







*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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