

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

Email & Skype: info@chipsmall.com Web: www.chipsmall.com

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China







Document Number: MC33288

Rev 6.0, 5/2006

Solid State Relay for Automotive Flasher Applications

This device is a Dual High-Side Power Switch dedicated for automotive flasher applications. In comparison with mechanical solutions, this device offers higher reliability as well as protection and diagnostic features.

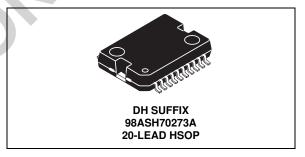
The device consists of two 25 m Ω Rdson fully protected high side switches, in a surface mount power package. It interfaces directly with a microcontroller and includes a current recopy function in order to allow the detection of a failed lamp in the application.

Features

- Junction Temperature Range from -40°C to 150°C
- Operating Voltage Range from 8.0 V to 35 V
- · Maximum Breakdown Voltage greater than 40 V
- 25 m Ω R_{DS(ON)} per Channel at 25°C
- 8.0 Amps Nominal Current per Channel
- · 1.2 W Warning Lamp Driver
- · Overtemperature Protection with Hysteresis
- Open Load Detection in ON State
- · Current Recopy to Monitor Lamp Output Current

33288

SOLID STATE RELAY



| ORDERING INFORMATION | | | | | |
|--|---------|--|--|--|--|
| Device Temperature Range (T _A) Package | | | | | |
| MC33288DDH/R2 | 20 HSOP | | | | |

Standby Current < 10 μA at V_{BAT} < 14 V

Simplified Application Diagram

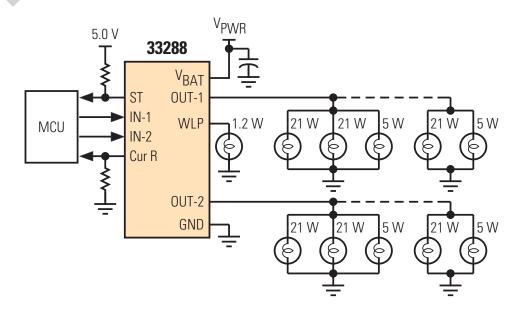
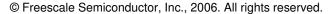


Figure 1. 33288 Simplified Internal Block Diagram

^{*} This document contains certain information on a new product. Specifications and information herein are subject to change without notice.







TERMINAL CONNECTIONS

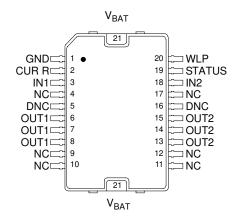


Figure 2. 33288 Terminal Connections

Table 1. 33288 Terminal Definitions

A functional description of each terminal can be found in the Functional Description section beginning on 7.

| Terminal Number | Terminal Name | Formal Name | Definition |
|-------------------------|------------------|-----------------------------|--|
| TAB | V _{BAT} | Supply Voltage | The backside TAB is connected to the power supply of the MC33288DH. |
| 2 | CUR R | Load Current Sense | The Current Sense terminal delivers a ratioed amount (1/1000) of the sum of the currents that can be used to generate signal ground referenced output voltages for use by the microcontroller. |
| 3 | IN1 | INPUT Channel 1 | These are the device input terminals which directly control their associated outputs. |
| 18 | IN2 | INPUT Channel 2 | |
| 4, 9, 10, 11, 12, 17 | NC | No Connect | These terminals are not used. |
| 5, 16 | DNC | Do Not Connect | These terminals must not be connected. |
| 6, 7, 8 | OUT1 | OUTPUT Channel 1 | They deliver current to the connected loads and are controlled via the IN1 terminals. |
| 13, 14, 15 | OUT2 | OUTPUT Channel 2 | They deliver current to the connected loads and are controlled via the IN2 terminals, |
| 11 | GND | Ground | This is the ground terminal of the device. |
| 19 | STATUS | Status for Both Channels | The Fault output is an open drain indication that goes active low when a fault mode (Openload, Overtemperature) is detected by the device. |
| 20 | WLP | Warning Lamp Output | It delivers current through the connected load. |



MAXIMUM RATINGS

Table 2. Maximum Ratings

All voltages are with respect to ground unless otherwise noted. Exceeding these ratings may cause a malfuncion or permanent damage to the device.

| Ratings | Symbol | Value | Unit |
|--|----------------------|-------------|------|
| ELECTRICAL RATINGS | | | |
| V _{BAT} Voltage with Respect to GND : Continuous/Pulse | V_{BAT} | -14 to 40 | V |
| OUT1 and OUT2 Voltage with Respect to GND : Continuous/Pulse | V _{OUT} | -0.3 to 40 | V |
| OUT1 to V _{BAT} and OUT2 to V _{BAT} Voltage : Continuous/Pulse | V _{OUT} | 40 | V |
| IN1, IN2, St DC Voltage : Continuous/Pulse | V _{IN} | -0.3 to 7.0 | V |
| CUR R Voltage : Continuous/Pulse | V _{CR} | -0.3 to 7.0 | V |
| ESD All Pins | | | V |
| Human Body Model (1) | V_{ESD1} | ±2000 | |
| Machine Model (2) | V_{ESD2} | ±200 | |
| OUT1, OUT2 Output Current : Pulse (3) | I _{OUTP} | 40 | Α |
| IN1, IN2, STATUS | I _{IN} | ±5.0 | mA |
| THERMAL RATINGS | | | |
| Junction Temperature | T _J | -40 to 150 | °C |
| Storage Temperature Range | T _{STORAGE} | -65 to 150 | °C |
| Thermal Resistance Junction to Case | $R_{	hetaJC}$ | 2.0 | °C/W |
| Thermal Resistance Junction to Ambient (4) | $R_{	hetaJA}$ | 25 | °C/W |
| Power Dissipation at Tcase 140°C (5) | P _D | 5.0 | W |

Notes

- 1. ESD1 testing is performed in accordance with the Human Body Model (C_{ZAP} =100 pF, R_{ZAP} =1500 Ω).
- 2. ESD2 testing is performed in accordance with the Machine Model (C_{ZAP} =200 pF, R_{ZAP} =0 Ω).
- 3. During lamp inrush current.
- 4. Device mounted on dual side printed circuit board with 70 μ m copper thickness and 10 cm² copper heat sink (2.5 cm² on top side and 7.5 cm² on down side).
- 5. Assuming a 150°C maximum junction temperature.



STATIC ELECTRICAL CHARACTERISTICS

Table 3. Static Electrical Charactersitics

Characteristics noted under conditions 4.5 V \leq V_{DD} \leq 5.5 V, 9.0 V \leq V_{PWR} \leq 16 V, -40°C \leq T_A \leq 125°C, unless otherwise noted. Typical values noted reflect the approximate parameter mean at T_A = 25°C under nominal conditions, unless otherwise noted.

| Characteristic | Symbol | Min | Тур | Max | Unit |
|--|-----------------------|-----|-----|-----|------|
| POWER INPUT | 1 | l | • | | I |
| Nominal Operating Voltage | V_{BAT} | 9 | _ | 16 | V |
| Functional Operating Voltage | V _{BAT} | 8 | _ | 35 | V |
| Under Voltage Threshold | V_{\muV} | 6 | 7 | 8 | V |
| V_{BAT} Standby Supply Current V_{BAT} < 14 V and V_{IN} = 0 V, T_J < 125°C | I _{STDBY} | _ | 1 | 10 | μА |
| Supply Current in ON State IN1 and IN2 @ 3.5 V, no fault, V_{BAT} < 14 V, T_{J} < 125°C | I _{ON} | _ | 8 | 20 | mA |
| Drain to Source on Resistance $I_{OUT} = 4.0 \text{ A}, V_{BAT} > 9.0 \text{ V} \text{ and } T_{J} = 25^{\circ}\text{C}$ | R _{DSON} | _ | 20 | 25 | mΩ |
| Drain to Source on Resistance I _{OUT} = 4.0 A, V _{BAT} > 9.0 V and T _J = 150°C | R _{DSON} | - | 30 | 40 | mΩ |
| High Current Limitation V _{OUT} > 1.0 V | I _{LIM} | _ | 30 | - | А |
| Short Circuit limitation $V_{OUT} < 1.0 \text{ V}, T_J = 25^{\circ}\text{C}$ | I _{LIM} | _ | 4 | - | А |
| Hot Openload Threshold | I _{OL} | - | 1 | _ | Α |
| V _{BAT} to Output Breakdown Voltage IN1 and IN2 @ 0 V, V _{OUT} = 0 V, I _{OUT} -0.25 mA | V _{DSS} | 40 | _ | _ | А |
| V_{BAT} to OUT1 : Leakage Current $V_{IN1} = 0 \text{ V}, V_{BAT} = 35 \text{ V}, V_{OUT1} = 0 \text{ V}$ | I _{OUT-LEAK} | _ | _ | 10 | μА |
| V_{BAT} to OUT2 : Leakage Current $V_{IN2} = 0 \text{ V}, V_{BAT} = 35 \text{ V}, V_{OUT2} = 0 \text{ V}$ | I _{OUT-LEAK} | _ | _ | 10 | μА |
| VIN1, VIN2 : Input Voltage Low Threshold | V _{IL} | - | - | 1.5 | V |
| VIN1, VIN2 : Input Voltage High Threshold | V _{IH} | 3.5 | _ | _ | V |
| VIN1, VIN2 : Input Voltage Hysteresis | V _{IH} | 0.4 | 0.7 | 0.9 | V |
| VIN1, VIN2 : Input Current V_{IN1} , $V_{IN2} = 3.5 \text{ V}$ | I _{IN} | _ | 18 | 40 | μА |
| Status Output Voltage Ist = 1 mA; Output in Fault | Vst | _ | _ | 0.5 | V |
| Thermal Shutdown | T _{SHUT} | 150 | - | _ | °C |
| Thermal Shutdown Hysteresis | T _{HYST} | _ | 10 | _ | °C |



Table 3. Static Electrical Charactersitics(continued)

Characteristics noted under conditions 4.5 V \leq V_{DD} \leq 5.5 V, 9.0 V \leq V_{PWR} \leq 16 V, -40°C \leq T_A \leq 125°C, unless otherwise noted. Typical values noted reflect the approximate parameter mean at T_A = 25°C under nominal conditions, unless otherwise noted.

| Characteristic | Symbol | Min | Тур | Max | Unit |
|--|---------------------|-----|--------|-----|------|
| Current Recopy Ratio $V_{OUT} > V_{BAT}$ -1.0 V, I_{OUT} from 2.0 A to 4.0 A T_{J} -40°C to 105°C, V_{BAT} 9.0 V to 16 V | C _R | _ | 1/1000 | _ | μА |
| Current Recopy Ratio Accuracy V _{OUT} > V _{BAT} -1.0 V, I _{OUT} from 2.0 A to 4.0 A T _J -40°C to 105°C, V _{BAT} 9.0 V to 16 V | C _{R-AC} | -10 | _ | 10 | % |
| Extended Current Recopy Ratio V _{OUT} > V _{BAT} -1.0 V, I _{OUT} from 1.0 A to 8.0 A | C _{REX} | _ | 1/1000 | _ | % |
| Extended Current Recopy Ratio Accuracy V _{OUT} > V _{BAT} -1.0 V, I _{OUT} from 1.0 A to 8.0 A | C _{REX-AC} | -20 | _ | 20 | % |
| Current Recopy Clamp Voltage At 10mA | V _{CLST} | 5.5 | _ | 7.0 | V |
| Nominal Flasher Frequency Operation | F _{FL} | _ | 1.5 | _ | Hz |
| Frequency Operation | F _{OP} | _ | - | 100 | Hz |
| Nominal Flasher Duty Cycle Operation | D _{CFL} | _ | 50 | - | % |
| WLP Output Drain to Source on Resistance I_{OUT} = 100 mA, V_{BAT} > 9.0 V and T_{J} = 25°C | R _{WON} | _ | - | 3.2 | Ω |
| WLP Output Drain to Source on Resistance I_{OUT} = 100 mA, V_{BAT} > 9.0 V and T_{J} = 125°C | R _{WON} | _ | - | 5.0 | Ω |
| WLP Output Current Limitation $V_{OUT} = 0 \ V$ | I _{WLIM} | _ | 500 | - | mA |
| WLP Thermal Shutdown | T _{WSHUT} | 150 | _ | - | °C |

DYNAMIC ELECTRICAL CHARACTERISTICS

Table 4. Dynamic Electrical Characteristics

Characteristics noted under conditions $4.5~V \le V_{DD} \le 5.5~V, 9.0~V \le V_{PWR} \le 16~V, -40^{\circ}C \le T_{A} \le 125^{\circ}C,$ unless otherwise noted. Typical values noted reflect the approximate parameter mean at $T_{A} = 25^{\circ}C$ under nominal conditions, unless otherwise noted.

| Characteristic | Symbol | Min | Тур | Max | Unit |
|--|-------------------|------|-----|-----|------|
| MICROCONTROLLER INTERFACE | • | | | | |
| Maximum Output Positive Slew Rate Load = 6.0Ω | t _R | 0.01 | 0.3 | 2.0 | V/µs |
| Maximum Output Negative Slew Rate Load = 6.0Ω | t _F | 0.01 | 0.3 | 2.0 | V/μs |
| Turn ON Delay Time Load = 6.0Ω , from $V_{IN/2}$ to 10% V_{OUT} | T _{DON} | 1.0 | 30 | 200 | μѕ |
| Turn OFF Delay Time Load = 6.0Ω , from $V_{IN/2}$ to 90% V_{OUT} | T _{DOFF} | 1.0 | 40 | 200 | μs |



FUNCTIONAL DESCRIPTION

INTRODUCTION

The MC33288DH is a dual high side switch with appropriate control, protection, and diagnostic features dedicated to automotive flasher applications. The device consists of two independent 25mW power switches and an additionnal $3.2\,\Omega$ high-side switch capable of controlling a $1.2\,\Omega$

W warning lamp. It interfaces directly with a microcontroller and the package used is a 20-HSOP power small outline, 20 terminals. The MC33288 incorporates a status terminal and a load current sense capability.

FUNCTIONAL TERMINAL DESCRIPTIONS

SUPPLY VOLTAGE (VBAT)

The backside TAB is connected to the power supply of the MC33288DH. In addition to its supply function, this terminal contributes to the thermal behaviour of the device by conducting the heat from the switching MOSFET to the printed circuit board.

LOAD CURRENT SENSE (CUR R)

The Current Sense terminal delivers a ratioed amount (1/1000) of the sum of the currents that can be used to generate signal ground referenced output voltages for use by the microcontroller.

INPUT CHANNEL 1/2 (IN1, IN2)

These are the device input terminals which directly control their associated outputs. The levels are CMOS compatible. When the input is a logic low, the associated output MOSFET is in the OFF state. When input is high, the MOSFET is turned ON and the load is activated. When both inputs are low, the device is in standby mode and its supply current is reduced. Each input terminal has an internal active pull-down, so that it will not float if disconnected.

OUTPUT CHANNEL (OUT1, OUT2)

Pins 6, 7, and 8 are the source of the output 1 25 m Ω MOSFET1. Pins 13, 14, and 15 are the source of the output 2 25 m Ω MOSFET2. They deliver current to the connected loads and are controlled via the IN1 and IN2 terminals, respectively . These outputs are current limited and thermally protected.

GROUND (GND)

This is the ground terminal of the device.

STATUS FOR BOTH CHANNELS (STATUS)

The Fault output is an open drain indication that goes active low when a fault mode (Openload, Overtemperature) is detected by the device on either one channel or both channels simultaneously. Its internal structure is an open drain architecture with an internal clamp at 6.0 V. An external pull-up resistor connected to V_{DD} (5.0 V) is needed.

WARNING LAMP OUTPUT (WLP)

This terminal is the source of a 3.2 Ω MOSFET. This output is current limited and thermally protected. It delivers current through the connected load when both IN1 and IN2 inputs are logic high. It is usually used as a warning lamp driver for Flasher application.



FUNCTIONAL DEVICE OPERATION

OPERATIONAL MODES

Power Supply

The MC33288 can be directly connected to the supply line. In standby mode (IN1 and IN2 @ OV), the supply current is less than 10 mA for V_{BAT} supply voltage below 14 V. This feature allows a very low supply current on the battery when the car is idle or parked.

During the OFF state, the current recopy terminal and the status terminals are in high impedance.

In case of loss of (digital) ground, the power MOSFETs turn off in order to safely tie the load current to zero.

Nominal Voltage and Transient Operation

The nominal operation voltage is from 9.0 V to 16 V. The MC33288 is functional over a larger voltage range from 8.0 V to 35 V. The technology used for both power and analog circuitry sustains 40 V DC voltage.

Reverse Battery

The MC33288 is self protected against reverse battery up to -14 V in a continuous mode. When a negative battery voltage arises, the MOSFETs are internally turned off. The load current flows in the body diode of each MOSFET. The power dissipation within the MC33288DH is then $V_F \times I_{LOAD}$, where I_{LOAD} is the total current set by the loads and V_F is the forward body diode voltage. V_F is 0.5 V typical at 150°C and decreases with temperature with a drop of -2.0 mV/°C.

Power Output Switches

The device has two independent high-side switches. Each switch is realized with an N-channel power MOSFET and includes current and temperature sensing circuitries. The MOSFET gates are driven by analog circuitry which includes charge pump and fault detection. A specific feature of this device is the output current recopy.

Each output has a 25 m Ω R_{DSON} maximum at 25°C. The R_{DSON} value reaches 40 m Ω at 150°C junction temperature. Each output has a parasitic drain to source diode, inherent to the technology. This diode can sustain as much current as the MOSFET. This diode is reversed biased during normal operation of the MOSFET and is forward biased during reverse battery or negative transient pulses.

The output MOSFET can each drive 8.0 A nominal. This maximum current condition occurs when up to three 21 W lamps and two small 5.0 W or 7.0 W lamps are connected on each channel, at higher V_{BAT} voltage specification. This is the case when a trailor is attached to the car. As the flasher can operate in warning or hazard operation mode with its two channels on, the MC33288 can drive 16 A total.

When lamps are turned on, the inrush current can reach up to 6 times the nominal lamp current. For this particular application, the total inrush current can reach $6 \times 8.0 \text{ A--}$ that

is up to 48 A per channel. The 48 A value will only occur at the first lamp turn-on. In order to optimise the total system (MC33288, printed circuit board, wiring, fuse etc.), the device incorporates a current limitation at 30 A typical. This means that the first in rush current will be clamped at 30 A. The second lamp turn-on of the flashing cycle will not see any clamping as the lamps have been heated up in the previous cycle.

IN1 and IN2 are the two inputs of the device, connected to a microcontroller I/O; they are CMOS compatible. When these inputs are in low state, the device is in standby mode. When any of the inputs are switched to a high level, the associated output MOSFET is turned on.

Status

The device has a single status terminal which reports an overtemperature and an openload condition in the device. This terminal is an open drain structure and needs an external pull-up resistor. The device incorporates overtemperature sense on each output MOSFET in order to independently protect each output. But, the report of the overtemperature is done through one single status terminal, called STATUS, in order to minimize the number of I/O connections of the MCU.

Open Load Function

The open load detection is active during the ON phase (hot openload detection). An amplifier measures the differential voltage between V_{BAT} and V_{OUT} .

When the load current is below 1.0 A, the openload circuitry will pull down the status output. If the openload condition is present before the device turn-on, the status will be pulled down after a delay due to the rise time of the output.

Current Recopy Function

Flasher application needs to detect failed lamp. The system must detect one 21 W lamp failing, in the flasher application only, without a trailor. The MC33288 has an output current recopy function which recopies on the CUR R terminal a portion of the output current. Each channel is connected to the same CUR R terminal as the system has to operate in the flasher mode only and not in the warning operation.

The MC33288 provides a current recopy of 1/1000 of the main output current, either output left or right. The recopy function has its higher accuracy of $\pm 10\%$ in the usual operation range and an accuracy of $\pm 20\%$ in the total operating range. This allows a precise detection of one 21 W lamp, but also the detection of additional lamps in case of trailor connection.



An external resistor must be connected to the CUR R terminal and then tied to a microcontroller A/D input for analog voltage measurement. The CUR R terminal is internally clamped to protect the MCU A/D input.

Warning Lamp Driver

The warning lamp driver is a 3.2 Ω R_{DSON} maximum high-side MOSFET to control the 1.2 W dashboard warning lamp. This output is current limited and thermally protected-activated only in the warning mode. It is turned on by activation of both right and left inputs (IN1 and IN2).

Package

The device is assembled into a power surface mount package. This package offers high thermal performances and high current capabilities. It offers 10 terminals on each package sides and an additional terminal which is the package heat sink , called terminal 21. The heak sink acts as the MC33288 power V_{BAT} connection.

Soldering Information

This device is packaged in a Surface Mount Power package indended to be soldered directly on the Printed Circuit Board.

This device was qualified according to JEDEC standards JESD22-A113-B and J-STD-020A with the reflow conditions applicable for packages with thickness above 2.5 mm:

Convection 220°C +5/-0°C

VPR 215-219°C

IR / Convection 220°C +5/-0°C

The maximum peak temperature during the soldering process should not exceed 220°C (+5°C/-0°C). The time at maximum temperature should range from 10 to 40s maximum.

Thermal Management

The junction to case thermal resistance is 2°C/W maximum. The junction to ambient thermal resistance is dependent on the mounting technology and the addition of heat sink. One of the most commonly used mounting techniques consists of using the printed circuit board and the copper lines as heat sink.

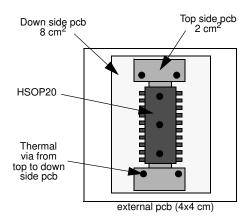


Figure 3. Printed Board Layout Example (not to scale)

Figure 1 shows an example of printed circuit board layout. It has a total of 10 cm² additional copper on two sides (2.5 cm² on the top side and 7.5 cm² on the down side).

With the above layout, thermal resistance junction to ambient of 25°C/W can be achieved, this value being split into:

• junction to case : $R_{\theta JC1} = R_{\theta JC2} = 2^{\circ}C/W$

• case to ambient : $R_{\theta CA} = 23^{\circ}C/W$

Lower value can be reached with the help of larger and thicker copper metal, higher number of thermal via from top to down side pcb and the use of additional thermal via from the circuit board to the module case.

Steady State Thermal Model

The junction to ambient thermal resistance of the circuit mounted on a printed circuit board can be split into two main parts: junction to case and case to ambient resistances.

A simplified sterring re 2.

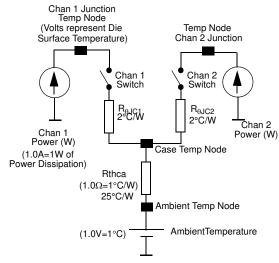


Figure 4. Simplified Thermal Model (Electrical Equivalent)

33288



Transient Thermal Model

A more complete model including thermal capacitance is proposed in Figure 3.

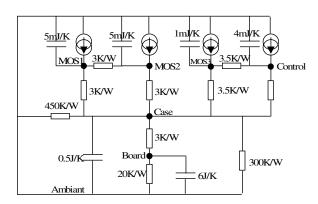


Figure 5. Transient Thermal Model

This gives a thermal impedance versus time (Figure 4), which has been determined with the printed circuit board shown in Figure 1.

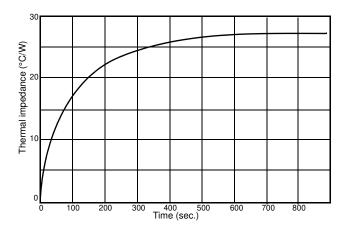


Figure 6. Junction to Ambiant Thermal Impedance

This figure shows that the steady state is reached after about 10 minutes. It also clearly shows that the device can dissipate almost twice the power within one minute compared to the maximum allowed power dissipation in steady state.



TYPICAL APPLICATIONS

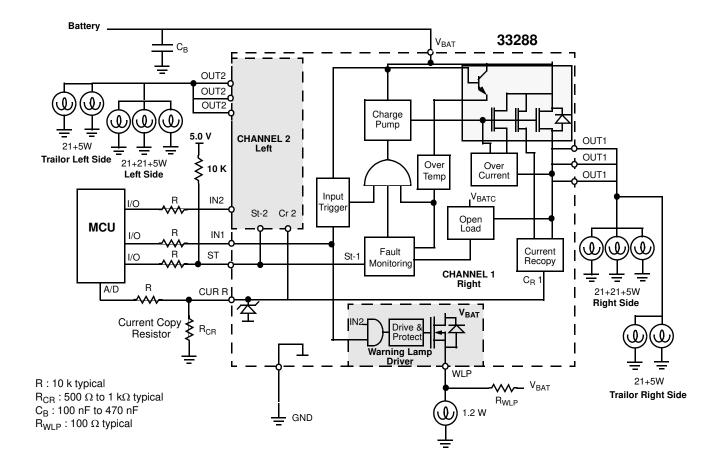


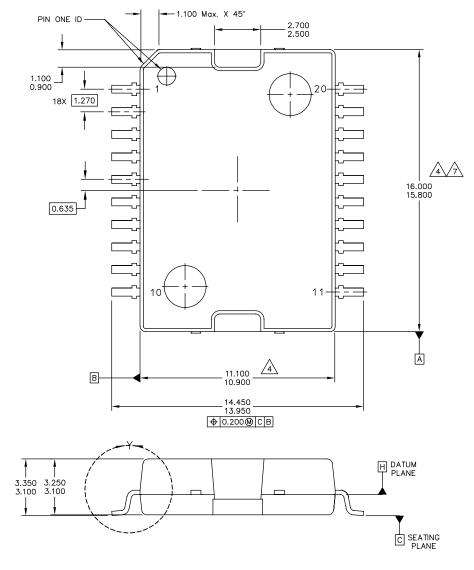
Figure 7. Typical Application - Automotive Flasher



PACKAGING

PACKAGING INFORMATION

DH SUFFIX 20-LEAD HSOP PLASTIC PACKAGE 98ASH70273A ISSUE C

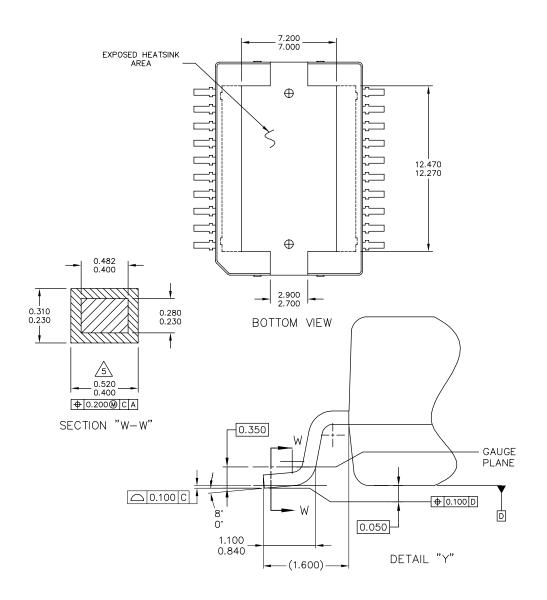


| © FREESCALE SEMICONDUCTOR, INC. ALL RIGHTS RESERVED. | MECHANICA | L OUTLINE | PRINT VERSION NO | T TO SCALE |
|---|-----------|--------------|------------------|-------------|
| TITLE: | | DOCUMENT NO |): 98ASH70273A | REV: E |
| 20 LEAD HSOP | | CASE NUMBER | ₹: 979–04 | 19 MAY 2005 |
| | | STANDARD: NO | DN-JEDEC | |



PACKAGING INFORMATION (CONTINUED)

DH SUFFIX 20-LEAD HSOP PLASTIC PACKAGE 98ASH70273A ISSUE C



| © FREESCALE SEMICONDUCTOR, INC. ALL RIGHTS RESERVED. | MECHANICA | L OUTLINE | PRINT VERSION NO | T TO SCALE |
|--|-----------|--------------|------------------|-------------|
| TITLE: | | DOCUMENT NO |): 98ASH70273A | REV: E |
| 20 LEAD HSOP | | CASE NUMBER | 2: 979–04 | 19 MAY 2005 |
| | | STANDARD: NO | N-JEDEC | |



REVISION HISTORY

| REVISION | DATE | DESCRIPTION OF CHANGES |
|----------|--------|---|
| 6.0 | 5/2005 | Implemed Revision History page Changed Part Number MC33288DH to MC33288DDH - Noelectrical changes. |



How to Reach Us:

Home Page:

www.freescale.com

support@freescale.com

USA/Europe or Locations Not Listed:

Freescale Semiconductor Technical Information Center, CH370 1300 N. Alma School Road Chandler, Arizona 85224 +1-800-521-6274 or +1-480-768-2130 support@freescale.com

Europe, Middle East, and Africa: Freescale Halbleiter Deutschland GmbH Technical Information Center Schatzbogen 7 81829 Muenchen, Germany +44 1296 380 456 (English) +46 8 52200080 (English) +49 89 92103 559 (German) +33 1 69 35 48 48 (French) support@freescale.com

Japan:

Freescale Semiconductor Japan Ltd. Headquarters ARCO Tower 15F 1-8-1, Shimo-Meguro, Meguro-ku, Tokyo 153-0064 Japan 0120 191014 or +81 3 5437 9125 support.japan@freescale.com

Asia/Pacific:

Freescale Semiconductor Hong Kong Ltd. Technical Information Center 2 Dai King Street Tai Po Industrial Estate Tai Po, N.T., Hong Kong +800 2666 8080 support.asia@freescale.com

For Literature Requests Only:

Freescale Semiconductor Literature Distribution Center P.O. Box 5405 Denver, Colorado 80217 1-800-441-2447 or 303-675-2140 Fax: 303-675-2150 LDCForFreescaleSemiconductor@hibbertgroup.com

RoHS-compliant and/or Pb-free versions of Freescale products have the functionality and electrical characteristics of their non-RoHS-compliant and/or non-Pb-free counterparts. For further information, see http://www.freescale.com or contact your Freescale sales representative.

For information on Freescale's Environmental Products program, go to http:// www.freescale.com/epp.

Information in this document is provided solely to enable system and software implementers to use Freescale Semiconductor products. There are no express or implied copyright licenses granted hereunder to design or fabricate any integrated circuits or integrated circuits based on the information in this document.

Freescale Semiconductor reserves the right to make changes without further notice to any products herein. Freescale Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does Freescale Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation consequential or incidental damages. "Typical" parameters that may be provided in Freescale Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals", must be validated for each customer application by customer's technical experts. Freescale Semiconductor does not convey any license under its patent rights nor the rights of others. Freescale Semiconductor products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the Freescale Semiconductor product could create a situation where personal injury or death may occur. Should Buyer purchase or use Freescale Semiconductor products for any such unintended or unauthorized application, Buyer shall indemnify and hold Freescale Semiconductor and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that Freescale Semiconductor was negligent regarding the design or manufacture of the part.

Freescale™ and the Freescale logo are trademarks of Freescale Semiconductor, Inc. All other product or service names are the property of their respective owners. © Freescale Semiconductor, Inc., 2006. All rights reserved.

