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# MC68HC908JB8

## MC68HC08JB8

## MC68HC08JT8

Technical Data

**M68HC08**  
**Microcontrollers**

MC68HC908JB8/D  
Rev. 2.3  
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# MC68HC908JB8

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# MC68HC08JT8

## Technical Data

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The following revision history table summarizes changes contained in this document. For your convenience, the page number designators have been linked to the appropriate location.

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## Revision History

Date	Revision Level	Description	Page Number(s)
September 2005	2.3	Added Pb-free parts.	267, 284
August 2005	2.2	Updated to meet Freescale identity guidelines.	Throughout
December 2003	2.1	<b>4.9 ROM-Resident Routines</b> — Removed block erase references for ROM-resident routines.	61
		<b>9.8.8 USB Control Register 3</b> — Clarified bit descriptions for OSTALL0 and ISTALL0.	149, 150
		<b>9.8.11 USB Status Register 1</b> — Clarified bit descriptions for TXACK, TXNAK, and TXSTL.	153
		<b>Section 19. Mechanical Specifications</b> — Replaced incorrect 44-pin QFP drawing, case 824E to case 824A.	263
February 2002	2	Corrected PTD6 and PTD7: not direct LED drive pins.	28, 210, 217
		Removed incorrect RX1E text from USB control register 1.	146
		Corrected <b>Figure 9-30</b> for USB module.	159
		Corrected timer discrepancies throughout <b>Section 11. Timer Interface Module (TIM)</b> .	177
		Added <b>Table 12-1 . Port Control Register Bits Summary</b> .	201
		Changed pullup resistor limits for D– and I/O ports in <b>18.6 DC Electrical Characteristics</b> .	256
		Added mechanical drawing for 20-pin SOIC package.	266
		Added <b>Appendix A. MC68HC08JB8</b> — ROM part.	269
		Added <b>Appendix B. MC68HC08JT8</b> — low-voltage ROM part.	277



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