

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



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Thyristor Module

= 2x 1400 V

18 A

 V_{τ} 1.57 V

Phase leg

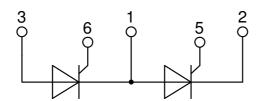
Part number

MCC19-14io8B



Backside: isolated





Features / Advantages:

- Thyristor for line frequency
- Planar passivated chip
- Long-term stability
- Direct Copper Bonded Al2O3-ceramic

Applications:

- Line rectifying 50/60 Hz
- Softstart AC motor control
- DC Motor control
- Power converter
- AC power control
- Lighting and temperature control

Package: TO-240AA

- Isolation Voltage: 3600 V~
- Industry standard outline
- RoHS compliant
- Soldering pins for PCB mounting
- Base plate: DCB ceramic
- · Reduced weight
- Advanced power cycling

Terms _Conditions of usage:

The data contained in this product data sheet is exclusively intended for technically trained staff. The user will have to evaluate the suitability of the product for the intended application and the completeness of the product data with respect to his application. The specifications of our components may not be considered as an assurance of component characteristics. The information in the valid application- and assembly notes must be considered. Should you require product information in excess of the data given in this product data sheet or which concerns the specific application of your product, please contact your local sales office.

Due to technical requirements our product may contain dangerous substances. For information on the types in question please contact your local sales office.

Should you intend to use the product in aviation, in health or life endangering or life support applications, please notify. For any such application we urgently recommend

to perform joint risk and quality assessments;
the conclusion of quality agreements;

- to establish joint measures of an ongoing product survey, and that we may make delivery dependent on the realization of any such measures.

IXYS reserves the right to change limits, conditions and dimensions.

Data according to IEC 60747 and per semiconductor unless otherwise specified

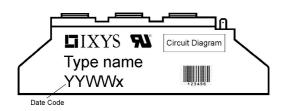
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Thyristo				" 	Ratings		ļ.
Symbol	Definition	Conditions		min.	typ.	max.	Ur
V _{RSM/DSM}	max. non-repetitive reverse/forwa	rd blocking voltage	$T_{VJ} = 25^{\circ}C$			1500	! !
V _{RRM/DRM}	max. repetitive reverse/forward bl	ocking voltage	$T_{VJ} = 25^{\circ}C$			1400	i !
R/D	reverse current, drain current	$V_{R/D} = 1400 \text{ V}$	$T_{VJ} = 25^{\circ}C$			100	μ
		$V_{R/D} = 1400 \text{ V}$	$T_{VJ} = 125^{\circ}C$			3	m
V _T	forward voltage drop	I _T = 40 A	$T_{VJ} = 25^{\circ}C$			1.56	
		$I_T = 80 \text{ A}$				2.05	
		$I_T = 40 \text{ A}$	T _{VJ} = 125°C			1.57	! !
		$I_{T} = 80 \text{ A}$				2.29	!
I _{TAV}	average forward current	$T_{c} = 85^{\circ}C$	T _{vJ} = 125°C			18	
I _{T(RMS)}	RMS forward current	180° sine				28	
V _{T0}	threshold voltage		T _{v.i} = 125°C			0.85	
r _T	slope resistance } for power lo	oss calculation only	VO			18	m!
R _{thJC}	thermal resistance junction to cas	Δ				1.3	1
R _{thCH}	thermal resistance case to heatsin				0.20		K/V
P _{tot}	total power dissipation	***	T _C = 25°C		0.20	77	V
	max. forward surge current	t = 10 ms; (50 Hz), sine	$T_{VI} = 45^{\circ}C$			400	
I _{TSM}	max. lorward surge current	t = 8,3 ms; (60 Hz), sine	$V_R = 0 V$			430	
		· · · · · · · · · · · · · · · · · · ·					
		t = 10 ms; (50 Hz), sine	$T_{VJ} = 125$ °C			340	į
101	valva fau fivalia a	t = 8,3 ms; (60 Hz), sine	$V_R = 0 V$			365	
l²t	value for fusing	t = 10 ms; (50 Hz), sine	$T_{VJ} = 45^{\circ}C$			800	A ²
		t = 8,3 ms; (60 Hz), sine	$V_R = 0 V$			770	A ²
		t = 10 ms; (50 Hz), sine	$T_{VJ} = 125$ °C			580	A ²
		t = 8,3 ms; (60 Hz), sine	$V_R = 0 V$			555	A ²
C,	junction capacitance	$V_R = 400 V$ $f = 1 MHz$	$T_{VJ} = 25^{\circ}C$		22		р
P_{GM}	max. gate power dissipation	t _P = 30 μs	$T_{c} = 125^{\circ}C$			10	٧
		t _P = 300 μs				5	٧
P_{GAV}	average gate power dissipation					0.5	٧
(di/dt) _{cr}	critical rate of rise of current	$T_{VJ} = 125 ^{\circ}\text{C}; f = 50 \text{ Hz}$ re	petitive, $I_T = 75 A$			150	Α/μ
		$t_P = 200 \mu s; di_G/dt = 0.45 A/\mu s;$					1
		$I_{G} = 0.45 A; V = \frac{2}{3} V_{DRM}$ no	on-repet., $I_{T} = 18 A$			500	Α/μ
(dv/dt) _{cr}	critical rate of rise of voltage	$V = \frac{2}{3} V_{DRM}$	T _{vJ} = 125°C			1000	V/μ
		R _{GK} = ∞; method 1 (linear voltaç	ge rise)				i ! !
V _{GT}	gate trigger voltage	V _D = 6 V	$T_{VJ} = 25^{\circ}C$			1.5	,
.		<u> </u>	T _{v.i} = -40°C			1.6	,
I _{GT}	gate trigger current	$V_D = 6 \text{ V}$	$T_{VJ} = 25^{\circ}C$			100	m
-01			$T_{VJ} = -40$ °C			200	m
V _{GD}	gate non-trigger voltage	$V_D = \frac{2}{3} V_{DRM}$	$T_{VJ} = 125^{\circ}C$			0.2	,
I _{GD}	gate non-trigger current	P /3 CDHW	. v3 — 120 G			5	m
'GD 	latching current	t _p = 10 μs	T _{vJ} = 25°C			450	m
I _L	latering current					450	1117
	h alalina a suuraat	$I_G = 0.45 \text{ A}; \text{ di}_G/\text{dt} = 0.45 \text{ A}/\mu\text{s}$				000	
I _H	holding current	$V_D = 6 V R_{GK} = \infty$	$T_{VJ} = 25^{\circ}C$			200	m
t _{gd}	gate controlled delay time	$V_D = \frac{1}{2} V_{DRM}$	$T_{VJ} = 25 ^{\circ}C$			2	μ
	$I_{G} = 0.45 A; di_{G}/dt = 0.45 A/\mu s$						1 1 1
t _q	turn-off time	$V_R = 100 \text{ V}; I_T = 20 \text{ A}; V = \frac{2}{3}$			150		μ
		$di/dt = 10 A/\mu s dv/dt = 20 V/$	$\mu s t_{p} = 200 \mu s$				ļ.



Package	TO-240AA				ı	Ratings	S	
Symbol	Definition	Conditions			min.	typ.	max.	Unit
RMS	RMS current	per terminal					200	Α
T _{VJ}	virtual junction temperature				-40		125	°C
Top	operation temperature				-40		100	°C
T _{stg}	storage temperature				-40		125	°C
Weight						81		g
M _D	mounting torque				2.5		4	Nm
$\mathbf{M}_{_{T}}$	terminal torque				2.5		4	Nm
d _{Spp/App}	creepage distance on surface	striking distance through air	terminal to terminal	13.0	9.7			mm
d _{Spb/Apb}	creepage distance on surface	Striking distance through an	terminal to backside	16.0	16.0			mm
V _{ISOL}	isolation voltage	isolation voltage t = 1 second			3600			٧
.002	t = 1 minute		50/60 Hz, RMS; lisoL ≤ 1 mA		3000			٧



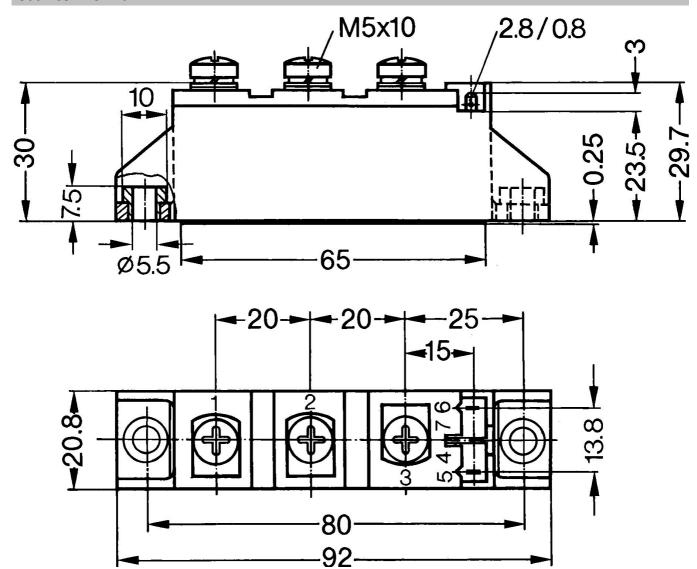
Ordering	Ordering Number	Marking on Product	Delivery Mode	Quantity	Code No.
Standard	MCC19-14io8B	MCC19-14io8B	Box	36	457817

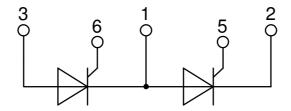
Similar Part	Package	Voltage class
MCMA25P1600TA	TO-240AA-1B	1600
MCMA35P1600TA	TO-240AA-1B	1600

Equiva	alent Circuits for	Simulation	* on die level	T _{vJ} = 125 °C
$I \rightarrow V_0$)— <u>R</u> o	Thyristor		
V _{0 max}	threshold voltage	0.85		V
$R_{0 \text{ max}}$	slope resistance *	16.8		$m\Omega$



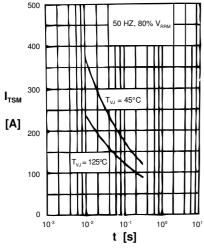
Outlines TO-240AA

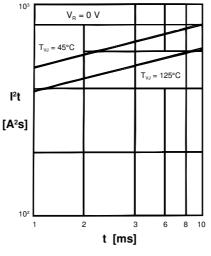






Thyristor





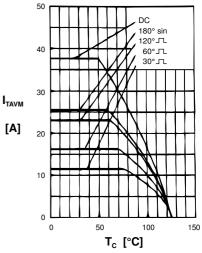
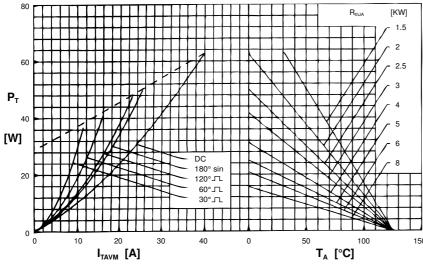


Fig. 1 Surge overload current I_{TSM} : Crest value, t: duration

Fig. 2 I^2t versus time (1-10 ms)

Fig. 3 Max. forward current at case temperature



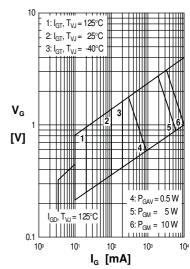
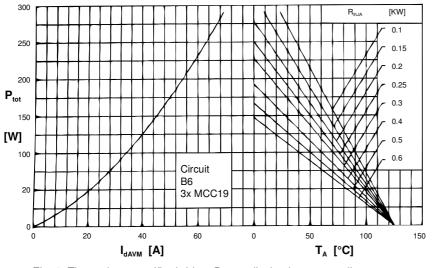


Fig. 4 Power dissipation versus onstate current & ambient temp. (per thyristor)

Fig. 5 Gate trigger charact.



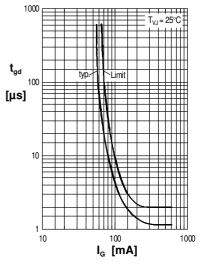


Fig. 6 Three phase rectifier bridge: Power dissipation versus direct output current and ambient temperature

Fig. 7 Gate trigger delay time



Thyristor

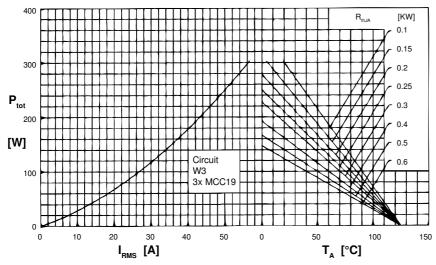


Fig. 8 Three phase AC-controller: Power dissipation vs. RMS output current and ambient temperature

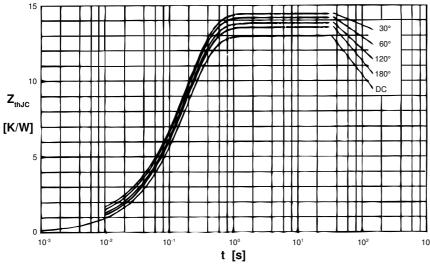


Fig. 9 Transient thermal impedance junction to case (per thyristor)

 $\boldsymbol{R}_{\text{thJC}}$ for various conduction angles d:

JC . • . •	a
d	R _{thJC} [K/V
DC	1.30
180°	1.35
120°	1.39
60°	1.42
30°	1.45

Constants for \mathbf{Z}_{thJC} calculation:

i I	R _{thi} [K/W]	t _, [s]
1	0.018	0.0033
2	0.041	0.0216
3	1.241	0.1910

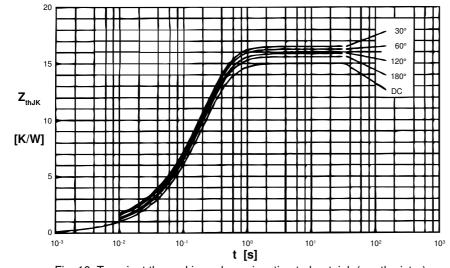


Fig. 10 Transient thermal impedance junction to heatsink (per thyristor)

 \boldsymbol{R}_{thJK} for various conduction angles d:

d	R _{thJK} [K/V
DC	1.50
180°	1.55
120°	1.59
60°	1.62
30°	1.65

Constants for \mathbf{Z}_{thJK} calculation:

i	R _{thi} [K/W]	t, [s]
1	0.018	0.0033
2	0.041	0.0216
3	1.241	0.1910
1	0.200	0.4600

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