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With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



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# **Thyristor Module**

=2x 800 V

49 A

 $V_{\tau}$ 1.34 V

### Phase leg

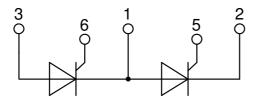
#### Part number

#### MCC44-08io8B



Backside: isolated

**F1** E72873



#### Features / Advantages:

- Thyristor for line frequency
- Planar passivated chip
- Long-term stability
- Direct Copper Bonded Al2O3-ceramic

#### **Applications:**

- Line rectifying 50/60 Hz
- Softstart AC motor control
- DC Motor control
- Power converter
- AC power control
- Lighting and temperature control

#### Package: TO-240AA

- Isolation Voltage: 3600 V~
- Industry standard outline
- RoHS compliant
- Soldering pins for PCB mounting
- Base plate: DCB ceramic
- · Reduced weight
- Advanced power cycling

#### Terms \_Conditions of usage:

The data contained in this product data sheet is exclusively intended for technically trained staff. The user will have to evaluate the suitability of the product for the intended application and the completeness of the product data with respect to his application. The specifications of our components may not be considered as an assurance of component characteristics. The information in the valid application- and assembly notes must be considered. Should you require product information in excess of the data given in this product data sheet or which concerns the specific application of your product, please contact your local sales office.

Due to technical requirements our product may contain dangerous substances. For information on the types in question please contact your local sales office.

Should you intend to use the product in aviation, in health or life endangering or life support applications, please notify. For any such application we urgently recommend

to perform joint risk and quality assessments;
the conclusion of quality agreements;

- to establish joint measures of an ongoing product survey, and that we may make delivery dependent on the realization of any such measures.

IXYS reserves the right to change limits, conditions and dimensions.

Data according to IEC 60747 and per semiconductor unless otherwise specified

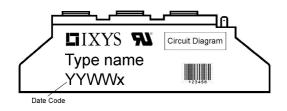
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Thyristo				<b>"</b>	Ratings	<b>&gt;</b>	į.
Symbol	Definition	Conditions		min.	typ.	max.	Un
V <sub>RSM/DSM</sub>	max. non-repetitive reverse/forwa	rd blocking voltage	$T_{VJ} = 25^{\circ}C$			900	
V <sub>RRM/DRM</sub>	max. repetitive reverse/forward bl	ocking voltage	$T_{VJ} = 25^{\circ}C$			800	į
R/D	reverse current, drain current	$V_{R/D} = 800 V$	$T_{VJ} = 25^{\circ}C$			100	μ
		$V_{R/D} = 800 V$	$T_{VJ} = 125$ °C			5	m
V <sub>T</sub>	forward voltage drop	I <sub>T</sub> = 100 A	$T_{VJ} = 25^{\circ}C$			1.34	
		$I_T = 200 A$				1.75	
		$I_{T} = 100 \text{ A}$	T <sub>VJ</sub> = 125°C			1.34	
		$I_{T} = 200 \text{ A}$				1.80	
I <sub>TAV</sub>	average forward current	T <sub>C</sub> = 85°C	T <sub>vJ</sub> = 125°C			49	
T(RMS)	RMS forward current	180° sine				77	
V <sub>T0</sub>	threshold voltage		T <sub>vJ</sub> = 125°C			0.85	
r <sub>T</sub>	slope resistance	oss calculation only				3.7	m!
R <sub>thJC</sub>	thermal resistance junction to cas	e				0.53	K/V
R <sub>thCH</sub>	thermal resistance case to heatsi				0.20		K/V
P <sub>tot</sub>	total power dissipation		T <sub>C</sub> = 25°C		0.20	180	٧
I <sub>TSM</sub>	max. forward surge current	t = 10 ms; (50 Hz), sine	$T_{V,I} = 45^{\circ}C$			1.15	<u> </u>
-15M		t = 8.3  ms; (60 Hz), sine	$V_R = 0 V$			1.24	;
		t = 10  ms; (50 Hz), sine	$T_{VJ} = 125^{\circ}C$			980	
		t = 8.3  ms; (60 Hz), sine	$V_R = 0 V$			1.06	k
l²t	value for fusing	t = 10 ms; (50 Hz), sine	$T_{VJ} = 45^{\circ}C$			6.62	
-ι	value for fashing	t = 8,3 ms; (60 Hz), sine	$V_{R} = 0 V$			6.40	i .
		t = 0.5  ms; (50 Hz), sine	$T_{VJ} = 125^{\circ}C$			4.80	<u>.                                    </u>
		t = 8,3 ms; (60 Hz), sine	$V_R = 0 V$			4.63	ĺ
C <sub>J</sub>	junction capacitance	$V_{R} = 400 \text{ V}$ f = 1 MHz	$V_R = 0 V$ $T_{VJ} = 25^{\circ}C$		54	4.03	р
			$T_{\rm VJ} = 25  \rm C$ $T_{\rm C} = 125  \rm ^{\circ} \rm C$		54	10	<del></del>
P <sub>GM</sub>	max. gate power dissipation	t <sub>P</sub> = 30 μs	1 <sub>C</sub> = 125 C			10 5	٧
<b>.</b>		$t_{P} = 300  \mu s$				_	!
P <sub>GAV</sub>	average gate power dissipation	T 40500 ( 50 H	1 450.4			0.5	٧
(di/dt) <sub>cr</sub>	critical rate of rise of current	•	petitive, $I_T = 150 A$			150	Α/μ
		$t_P = 200 \mu s; di_G/dt = 0.45 A/\mu s;$					İ
			on-repet., $I_T = 49 A$			500	i
(dv/dt) <sub>cr</sub>	critical rate of rise of voltage	$V = \frac{2}{3} V_{DRM}$	$T_{VJ} = 125$ °C			1000	V/μ
		R <sub>GK</sub> = ∞; method 1 (linear voltaç					
$V_{GT}$	gate trigger voltage	$V_D = 6 V$	$T_{VJ} = 25^{\circ}C$			1.5	,
			$T_{VJ} = -40$ °C			1.6	'
I <sub>GT</sub>	gate trigger current	$V_D = 6 V$	$T_{VJ} = 25^{\circ}C$			100	m
			$T_{VJ} = -40$ °C			200	m
$V_{ t GD}$	gate non-trigger voltage	$V_D = \frac{2}{3} V_{DRM}$	$T_{VJ} = 125^{\circ}C$			0.2	,
I <sub>GD</sub>	gate non-trigger current					10	m
l <sub>L</sub>	latching current	t <sub>p</sub> = 10 μs	$T_{VJ} = 25^{\circ}C$			450	m
		$I_{G} = 0.45 A;  di_{G}/dt = 0.45 A/\mu s$	;				
I <sub>H</sub>	holding current	V <sub>D</sub> = 6 V R <sub>GK</sub> = ∞	$T_{VJ} = 25$ °C			200	m
t <sub>gd</sub>	gate controlled delay time	$V_D = \frac{1}{2} V_{DRM}$	$T_{VJ} = 25^{\circ}C$			2	μ
		$I_G = 0.45 A;  di_G/dt = 0.45 A/\mu s$	<b>;</b>				
t <sub>q</sub>	turn-off time	$V_R = 100 \text{ V}; I_T = 150 \text{ A}; V = \frac{2}{3}$			150		μ
7		$di/dt = 10 \text{ A/}\mu\text{s} \text{ dv/dt} = 20 \text{ V/}$					



Package TO-240AA			Ratings					
Symbol	Definition	Conditions			min.	typ.	max.	Unit
IRMS	RMS current	per terminal					200	Α
T <sub>VJ</sub>	virtual junction temperature				-40		125	°C
Top	operation temperature				-40		100	°C
T <sub>stg</sub>	storage temperature				-40		125	°C
Weight						81		g
M <sub>D</sub>	mounting torque				2.5		4	Nm
$\mathbf{M}_{_{T}}$	terminal torque				2.5		4	Nm
d <sub>Spp/App</sub>	creepage distance on surface	striking distance through air	terminal to terminal 13.0 terminal to backside 16.0		9.7			mm
$d_{Spb/Apb}$	creepage distance on surface	striking distance through an			16.0			mm
V <sub>ISOL</sub>	isolation voltage	t = 1 second	50/60 Hz, RMS; IsoL ≤ 1 mA		3600			V
.002		t = 1 minute			3000			٧



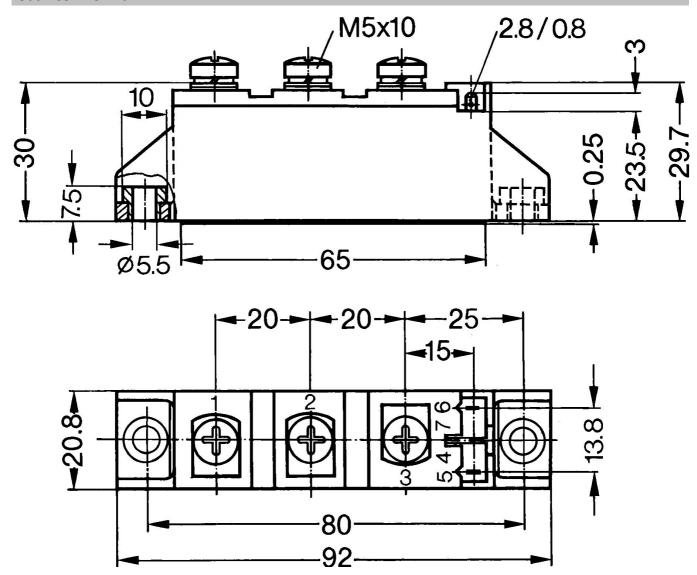
Ordering	Ordering Number	Marking on Product	Delivery Mode	Quantity	Code No.
Standard	MCC44-08io8B	MCC44-08io8B	Box	36	452947

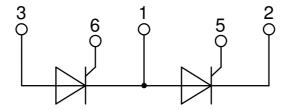
Similar Part	Package	Voltage class
MCMA50P1200TA	TO-240AA-1B	1200
MCMA65P1200TA	TO-240AA-1B	1200

Equiva	alent Circuits for	Simulation	* on die level	T <sub>vJ</sub> = 125 °C
$I \rightarrow V_0$	)— <u>R</u> o	Thyristor		
V <sub>0 max</sub>	threshold voltage	0.85		V
$R_{0 \text{ max}}$	slope resistance *	4.1		mΩ



### Outlines TO-240AA







#### **Thyristor**

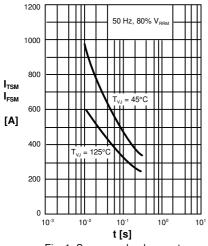


Fig. 1 Surge overload current  $I_{TSM}$ ,  $I_{FSM}$ : Crest value, t: duration

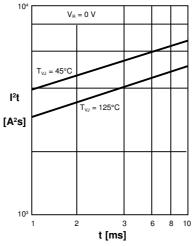


Fig. 2 I2t versus time (1-10 ms)

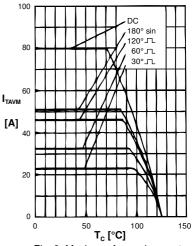


Fig. 3 Maximum forward current at case temperature

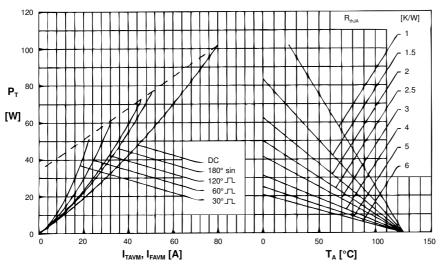


Fig. 4 Power dissipation vs. onstate current and ambient temperature (per thyristor/diode)

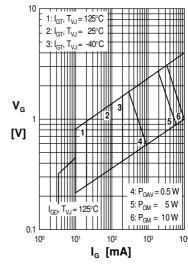


Fig. 5 Gate trigger characteristics

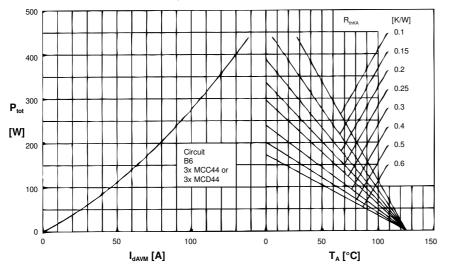


Fig. 6 Three phase rectifier bridge: Power dissipation versus direct output current and ambient temperature

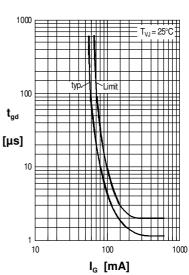


Fig. 7 Gate trigger delay time



### **Thyristor**

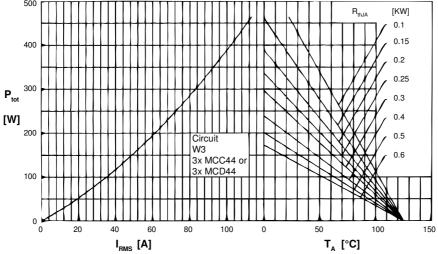


Fig. 8 Three phase AC-controller: Power dissipation versus RMS output current and ambient temperature

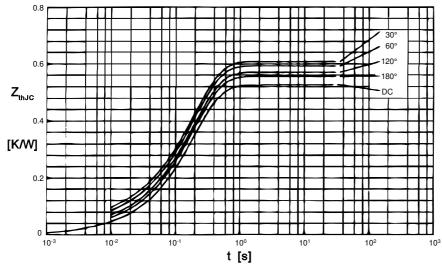


Fig. 9 Transient thermal impedance junction to case (per thyristor)

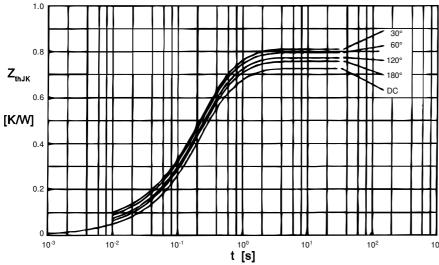


Fig. 10 Transient thermal impedance junction to heatsink (per thyristor)

R<sub>th,IC</sub> for various conduction angles d:

d	R <sub>th</sub>	<sub>Jc</sub> [K∕M
DC	;	0.53
180	0	0.55
120	0	0.58
60	0	0.60
30	0	0.62

Constants for  $Z_{thJC}$  calculation:

i F	R <sub>thi</sub> [K/W]	tˌ[s]
1	0.015	0.0035
2	0.026	0.0200
3	0.489	0.1950

R<sub>thJK</sub> for various conduction angles d:

d	R <sub>thJK</sub> [K/W]
DC	0.73
180°	0.75
120°	0.78
60°	0.80
30°	0.82

Constants for Z<sub>thJK</sub> calculation:

i h	⊰ <sub>thi</sub> [K/W]	t <sub>i</sub> [s]
1	0.015	0.0035
2	0.026	0.0200
3	0.489	0.0195
4	0.200	0.6800