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With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



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# **Thyristor Module**

= 2x 1600 V

60 A

 $V_{\tau}$ 1.24 V

### Phase leg

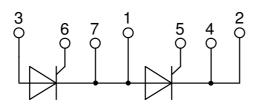
#### Part number

#### MCC56-16io1B



Backside: isolated





#### Features / Advantages:

- Thyristor for line frequency
- Planar passivated chip
- Long-term stability
- Direct Copper Bonded Al2O3-ceramic

#### **Applications:**

- Line rectifying 50/60 Hz
- Softstart AC motor control
- DC Motor control
- Power converter
- AC power control
- Lighting and temperature control

### Package: TO-240AA

- Isolation Voltage: 3600 V~
- Industry standard outline
- RoHS compliant
- Soldering pins for PCB mounting
- Base plate: DCB ceramic
- · Reduced weight
- Advanced power cycling

#### Terms \_Conditions of usage:

The data contained in this product data sheet is exclusively intended for technically trained staff. The user will have to evaluate the suitability of the product for the intended application and the completeness of the product data with respect to his application. The specifications of our components may not be considered as an assurance of component characteristics. The information in the valid application- and assembly notes must be considered. Should you require product information in excess of the data given in this product data sheet or which concerns the specific application of your product, please contact your local sales office.

Due to technical requirements our product may contain dangerous substances. For information on the types in question please contact your local sales office.

Should you intend to use the product in aviation, in health or life endangering or life support applications, please notify. For any such application we urgently recommend

to perform joint risk and quality assessments;
the conclusion of quality agreements;

- to establish joint measures of an ongoing product survey, and that we may make delivery dependent on the realization of any such measures.

IXYS reserves the right to change limits, conditions and dimensions.

Data according to IEC 60747 and per semiconductor unless otherwise specified

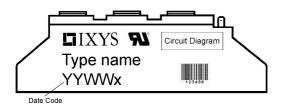
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Thyristo		Canditions		l	Ratings		
Symbol	Definition	Conditions	T 0500	min.	typ.	max.	Un
V <sub>RSM/DSM</sub>	max. non-repetitive reverse/forwa		$T_{VJ} = 25^{\circ}C$			1700	í I
V <sub>RRM/DRM</sub>	max. repetitive reverse/forward bl		$T_{VJ} = 25^{\circ}C$			1600	,
I <sub>R/D</sub>	reverse current, drain current	$V_{R/D} = 1600 \text{ V}$	$T_{VJ} = 25^{\circ}C$			200	μ
		$V_{R/D} = 1600 \text{ V}$	$T_{VJ} = 125$ °C			5	m
V <sub>T</sub>	forward voltage drop	$I_T = 100 A$	$T_{VJ} = 25^{\circ}C$			1.26	,
		$I_{T} = 200 \text{ A}$				1.57	'
		$I_T = 100 A$	$T_{VJ} = 125$ °C			1.24	,
		$I_{T} = 200 \text{ A}$				1.62	,
I <sub>TAV</sub>	average forward current	$T_{c} = 85^{\circ}C$	$T_{VJ} = 125$ °C			60	
I <sub>T(RMS)</sub>	RMS forward current	180° sine				94	
V <sub>T0</sub>	threshold voltage		T <sub>vJ</sub> = 125°C			0.85	,
r <sub>T</sub>	slope resistance } for power lo	oss calculation only				3.7	m
R <sub>thJC</sub>	thermal resistance junction to cas	e				0.45	K/V
R <sub>thCH</sub>	thermal resistance case to heatsi	nk			0.20		K/V
P <sub>tot</sub>	total power dissipation		T <sub>C</sub> = 25°C			222	٧
I <sub>TSM</sub>	max. forward surge current	t = 10 ms; (50 Hz), sine	$T_{v.i} = 45^{\circ}C$			1.50	k
TOM	-	t = 8,3 ms; (60 Hz), sine	$V_R = 0 V$			1.62	k
		t = 10 ms; (50 Hz), sine	T <sub>v.i</sub> = 125°C			1.28	k,
		t = 8,3 ms; (60 Hz), sine	$V_R = 0 V$			1.38	k
l²t	value for fusing	t = 10 ms; (50 Hz), sine	$T_{VJ} = 45^{\circ}C$			11.3	ļ.,,,,,,,
		t = 8,3  ms; (60  Hz),  sine	$V_R = 0 V$			10.9	1
		t = 0.5  ms; (50 Hz), sine	$T_{VJ} = 125^{\circ}C$			8.13	į
		t = 8,3  ms; (60  Hz),  sine	$V_R = 0 V$			7.87	į
C,	junction capacitance	$V_{\rm R} = 400  \text{V}  \text{f} = 1  \text{MHz}$	$T_{VJ} = 25^{\circ}C$		74	7.07	pl
		$t_{P} = 30 \mu s$	$T_{\rm VJ} = 25 \rm C$ $T_{\rm C} = 125 \rm ^{\circ} C$		74	10	V
P <sub>GM</sub>	max. gate power dissipation	·	1 <sub>C</sub> = 125 C			5	۷
<b>D</b>		$t_{P} = 300  \mu s$				_	!
P <sub>GAV</sub>	average gate power dissipation	T 10500 ( 5011	1 450.4			0.5	V
(di/dt) <sub>cr</sub>	critical rate of rise of current	$T_{VJ} = 125 ^{\circ}\text{C}; f = 50 \text{Hz}$ re	•			150	Α/μ
		$t_P = 200 \mu\text{s}; di_G/dt = 0.45 A/\mu\text{s}; -$					
			on-repet., $I_T = 60 \text{ A}$				A/μ
(dv/dt) <sub>cr</sub>	critical rate of rise of voltage	$V = \frac{2}{3} V_{DRM}$	$T_{VJ} = 125$ °C			1000	V/μ
		R <sub>GK</sub> = ∞; method 1 (linear volta	<u> </u>				
$V_{GT}$	gate trigger voltage	$V_D = 6 V$	$T_{VJ} = 25^{\circ}C$			1.5	'
			$T_{VJ} = -40$ °C			1.6	١
I <sub>GT</sub>	gate trigger current	$V_D = 6 V$	$T_{VJ} = 25^{\circ}C$			100	m
			$T_{VJ} = -40$ °C			200	m
$V_{GD}$	gate non-trigger voltage	$V_D = \frac{2}{3} V_{DRM}$	$T_{VJ} = 125^{\circ}C$			0.2	١
I <sub>GD</sub>	gate non-trigger current					10	m
I <sub>L</sub>	latching current	t <sub>p</sub> = 10 μs	T <sub>VJ</sub> = 25°C			450	m
-		$I_{\rm G} = 0.45  \text{A};  \text{di}_{\rm G}/\text{dt} = 0.45  \text{A}/\mu \text{s}$	3				-
I <sub>H</sub>	holding current	$V_D = 6 \text{ V } R_{GK} = \infty$	T <sub>vJ</sub> = 25°C			200	m
t <sub>gd</sub>	gate controlled delay time	$V_D = \frac{1}{2} V_{DRM}$	$T_{VJ} = 25$ °C			2	μ
gu	<u> </u>	$I_{\rm G} = 0.45  \text{A};  \text{di}_{\rm G}/\text{dt} = 0.45  \text{A}/\mu \text{s}$				_	٣
t <sub>q</sub>	turn-off time				150		μ



Package	Package TO-240AA			Ratings				
Symbol	Definition	Conditions			min.	typ.	max.	Unit
I <sub>RMS</sub>	RMS current	per terminal					200	Α
T <sub>vJ</sub>	virtual junction temperature				-40		125	°C
Top	operation temperature				-40		100	°C
T <sub>stg</sub>	storage temperature				-40		125	°C
Weight						81		g
M <sub>D</sub>	mounting torque				2.5		4	Nm
$\mathbf{M}_{_{T}}$	terminal torque				2.5		4	Nm
d <sub>Spp/App</sub>	creepage distance on surface   striking distance through		terminal to terminal	13.0	9.7			mm
$d_{\text{Spb/Apb}}$	creepage distance on surface	Striking distance through an	terminal to backside 16.0		16.0			mm
V <sub>ISOL</sub>	isolation voltage	t = 1 second	50/60 Hz, RMS; IsoL ≤ 1 mA		3600			٧
.002		t = 1 minute			3000			٧



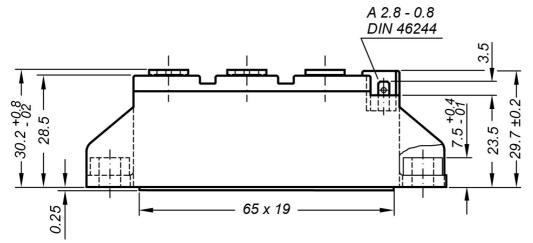
Ordering	Ordering Number	Marking on Product	Delivery Mode	Quantity	Code No.
Standard	MCC56-16io1B	MCC56-16io1B	Box	36	452769

Similar Part	Package	Voltage class
MCMA65P1600TA	TO-240AA-1B	1600
MCMA85P1600TA	TO-240AA-1B	1600

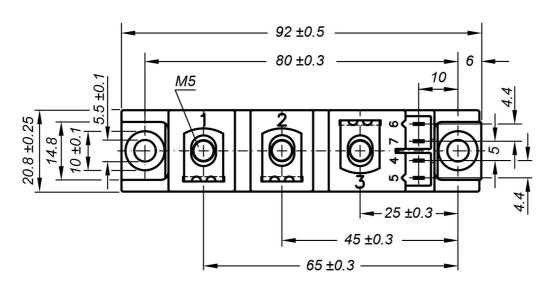
Equiva	alent Circuits for	Simulation	* on die level	T <sub>vJ</sub> = 125 °C
$I \rightarrow V_0$	)—[R <sub>0</sub> ]-	Thyristor		
V <sub>0 max</sub>	threshold voltage	0.85		V
$R_{0 max}$	slope resistance *	2.5		mΩ



### Outlines TO-240AA

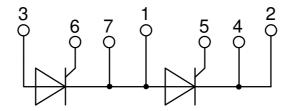


General tolerance: DIN ISO 2768 class "c"



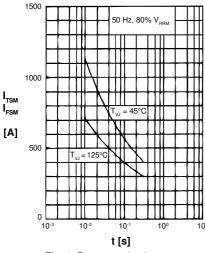
Optional accessories: Keyed gate/cathode twin plugs Wire length: 350 mm, gate = white, cathode = red UL 758, style 3751

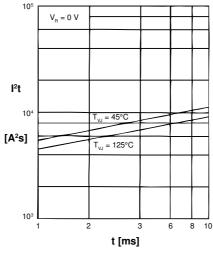
Type **ZY 200L** (L = Left for pin pair 4/5) Type **ZY 200R** (R = Right for pin pair 6/7)





#### **Thyristor**





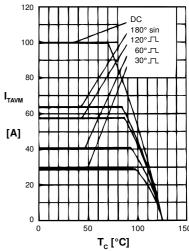
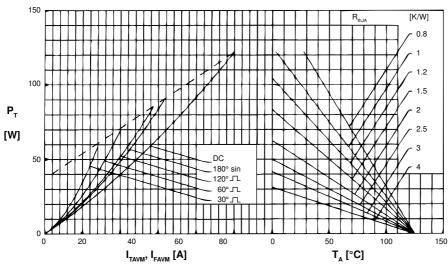


Fig. 1 Surge overload current  $I_{TSM}$ ,  $I_{FSM}$ : Crest value, t: duration

Fig. 2 I2t versus time (1-10 ms)

Fig. 3 Maximum forward current at case temperature



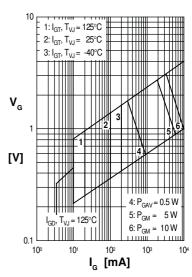
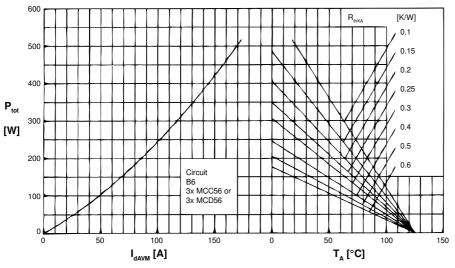


Fig. 4 Power dissipation vs. onstate current and ambient temperature (per thyristor/diode)

Fig. 5 Gate trigger charact.



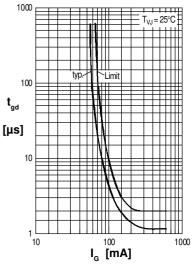


Fig. 6 Three phase rectifier bridge: Power dissipation versus direct output current and ambient temperature

Fig. 7 Gate trigger delay time



### **Thyristor**

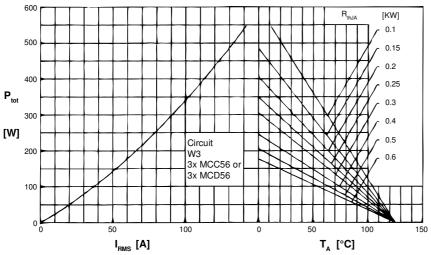


Fig. 8 Three phase AC-controller: Power dissipation vs. RMS output current and ambient temperature

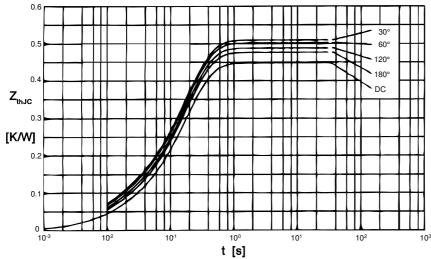


Fig. 9 Transient thermal impedance junction to case (per thyristor)

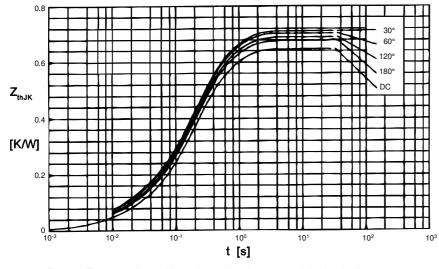


Fig. 10 Transient thermal impedance junction to heatsink (per thyristor)

 $R_{thJC}$  for various conduction angles d:

thJC	a
d	R <sub>thJC</sub> [K/W
DC	0.450
180°	0.470
120°	0.490
60°	0.505
30°	0.520

Constants for  $Z_{\text{thJC}}$  calculation:

i I	R <sub>thi</sub> [K/W]	t, [s]
1	0.014	0.0150
2	0.026	0.0095
3	0.410	0.1750

 $R_{thJK}$  for various conduction angles d:

d	$R_{thJK}$ [K/W
DC	0.650
180°	0.670
120°	0.690
60°	0.705
30°	0.720

Constants for  $\mathbf{Z}_{_{\text{thJK}}}$  calculation:

i F	R <sub>thi</sub> [K/W]	t <sub>,</sub> [s]
1	0.014	0.0150
2	0.026	0.0095
3	0.410	0.1750
4	0.200	0.6700