# imall

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# **MCH5908**

# N-Channel JFET 15V, 10 to 32mA, 35mS, Dual MCPH5



# **Features**

- Composite type with 2 J-FET contained in a MCPH5 package currently in use, improving the mounting efficiency greatly
- The MCH5908 is formed with two chips, being equivalent to the 2SK3557, placed in one package

# **Specifications**

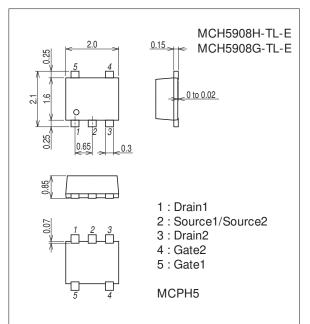
#### Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	VDSX		15	V
Gate-to-Drain Voltage	VGDS		-15	V
Gate Current	IG		10	mA
Drain Current	ID		50	mA
Allowable Power Dissipation	PD	1 unit	200	mW
Total Power Dissipation	PT		300	mW
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

### **Package Dimensions**

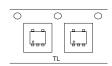
unit : mm (typ) 7021A-009



# **Product & Package Information**

- Package
- : MCPH5
- JEITA, JEDEC
- : SC-88A, SC-70-5, SOT-353
- Minimum Packing Quantity : 3,000 pcs./reel

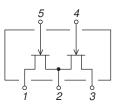
### Packing Type : TL





Marking

### **Electrical Connection**



#### Electrical Characteristics at Ta=25°C

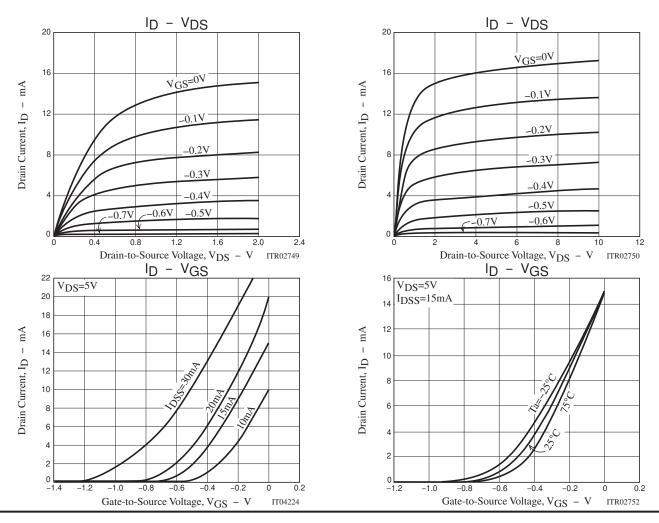
Parameter	Symbol	Conditions	Ratings			Unit	
Faidifieter	Symbol	Conditions	min	typ	max	Unit	
Gate-to-Drain Breakdown Voltage	V(BR)GDS	IG=-10μA, VDS=0V	-15			V	
Gate-to-Source Leakage Current	IGSS	V <sub>GS</sub> =-10V, V <sub>DS</sub> =0V			-1.0	nA	
Cutoff Voltage	V <sub>GS</sub> (off)	V <sub>DS</sub> =5V, I <sub>D</sub> =100μA	-0.3	-0.7	-1.5	V	
Zero-Gate Voltage Drain Current	IDSS	V <sub>DS</sub> =5V, V <sub>GS</sub> =0V	10.0*		32.0*	mA	
Forward Transfer Admittance	yfs	VDS=5V, VGS=0V, f=1kHz	24	35		mS	
Input Capacitance	Ciss			10.5		pF	
Reverse Transfer Capacitance	Crss	V <sub>DS</sub> =5V, V <sub>GS</sub> =0V, f=1MHz		3.5		pF	
Noise Figure	NF	$V_{DS}$ =5V, Rg=1k $\Omega$ , ID=1mA, f=1kHz		1.0		dB	

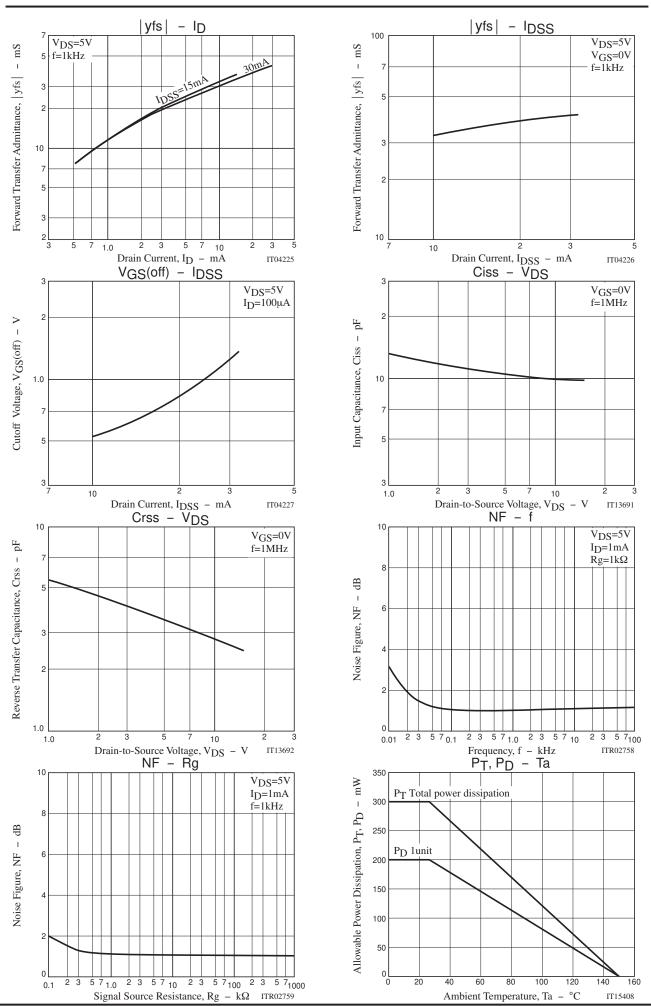
The specifications shown above are for each individual J-FET.

$^{\star}$ : The MCH5908 is classified by IDSS as follows (unit : mA).						
Rank	G	Н				
IDSS	10 to 20	16 to 32				

## **Ordering Information**

Device	Package	Shipping	memo	
MCH5908H-TL-E	MCPH5	3,000pcs./reel	Pb Free	
MCH5908G-TL-E	MCPH5	3,000pcs./reel	FD Flee	





## Taping Specification MCH5908H-TL-E, MCH5908G-TL-E

1. Packing Format

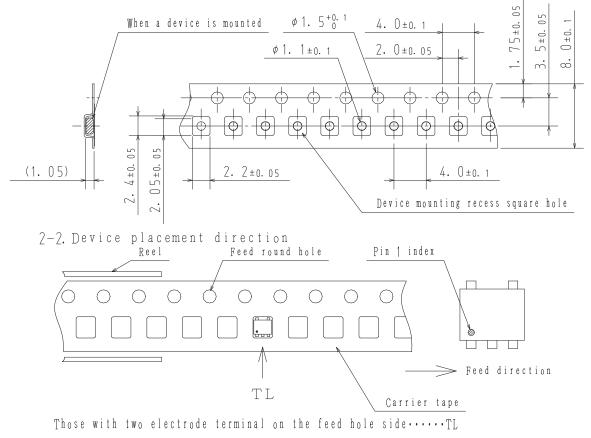
Package Name	Carrier Tape	Maximum Number of devices contained (pcs)			Packing format			
	Туре	Reel Inn	ner box	Outer box	Inner BOX (C-1)		(C-1)	Outer BOX (A-7)
MCPH5	MCP4	3,00015	5,000	90,000	5 reels contained			6 inner boxes contained
					Dime	nsions:mm (	external)	Dimensions:mm (external)
					18	$3 \times 72 \times$	185	440×195×210
<u>Reel label, Inner box label</u> <u>Outer box label</u>								
Packing met	h o d			(u 1	nit:n	nm)	The for	label at the time of factory shipments, m of a label may change in physical ution process.
°	>			6	59	>	<	108
	Type LOT Quan Orig Reel la	No. — tity —	> (11 > (00 (0) (0) (0) (0) (0) (0) (0) (0) (0)	e LEAD FI	IIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIII		C C C C C C C C C C C C C C C C C C C	TYPE CODE
				Label		JEITA	Phase	
				LEAD FRE	EE 3	JEITA P	hase 3A	

LEAD FREE 4

JEITA Phase 3

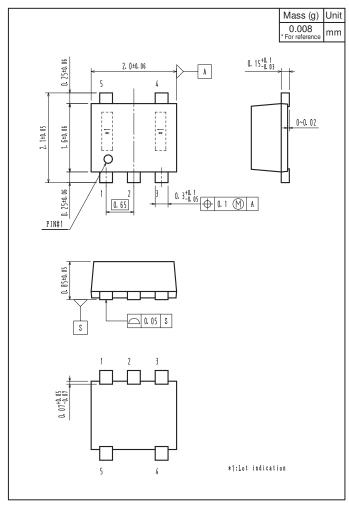
2. Taping configuration

2-1. Carrier tape size (unit:mm)

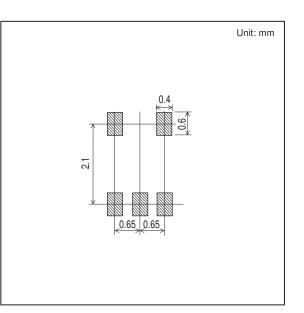


# Outline Drawing

MCH5908H-TL-E, MCH5908G-TL-E



## Land Pattern Example



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