imall

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

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MCMA140P1200TA

=

=

 V_{RRM}

I _{tav}

VT

= 2x 1200 V

140 A

1.28 V

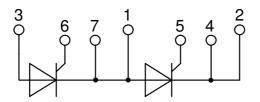
Phase le	эg
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Part number

MCMA140P1200TA



Backside: isolated **E**72873



Features / Advantages:

- Thyristor for line frequency
- Planar passivated chip
- Long-term stability
- Direct Copper Bonded Al2O3-ceramic

Applications:

- Line rectifying 50/60 Hz
- Softstart AC motor control
- DC Motor control
- Power converter
- AC power control
- Lighting and temperature control

Package: TO-240AA

- Isolation Voltage: 4800 V~
- Industry standard outline
- RoHS compliant
- Soldering pins for PCB mounting
- Base plate: DCB ceramic
- Reduced weight
- Advanced power cycling

Terms Conditions of usage:

The data contained in this product data sheet is exclusively intended for technically trained staff. The user will have to evaluate the suitability of the product for the intended application and the completeness of the product data with respect to his application. The specifications of our components may not be considered as an assurance of component characteristics. The information in the valid application and assembly notes must be considered. Should you require product information in excess of the data given in this product data sheet or which concerns the specific application of your product, please contact your local sales office. Due to technical requirements our product may contain dangerous substances. For information on the types in question please contact your local sales office. Should you intend to use the product in aviation, in health or life endangering or life support applications, please notify. For any such application we urgently recommend

to perform joint risk and quality assessments;
the conclusion of quality agreements;

- to establish joint measures of an ongoing product survey, and that we may make delivery dependent on the realization of any such measures.

IXYS reserves the right to change limits, conditions and dimensions.

Data according to IEC 60747and per semiconductor unless otherwise specified

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MCMA140P1200TA

Thyristo				1	Ratings		
Symbol	Definition	Conditions		min.	typ.	max.	Un
V _{RSM/DSM}	max. non-repetitive reverse/forwa	rd blocking voltage	$T_{vJ} = 25^{\circ}C$			1300	1
V _{RRM/DRM}	max. repetitive reverse/forward ble		$T_{vJ} = 25^{\circ}C$			1200	١
R/D	reverse current, drain current	$V_{R/D} = 1200 V$	$T_{vJ} = 25^{\circ}C$			100	μ/
		V _{R/D} = 1200 V	$T_{vJ} = 140^{\circ}C$			10	m/
VT	forward voltage drop	$I_{T} = 150 \text{ A}$	$T_{VJ} = 25^{\circ}C$			1.29	١
		$I_{T} = 300 \text{ A}$				1.63	١
		I _T = 150 A	$T_{VJ} = 125 \degree C$			1.28	١
		I _T = 300 A				1.70	١
ITAV	average forward current	$T_c = 85^{\circ}C$	T _{vJ} = 140°C			140	/
I _{T(RMS)}	RMS forward current	180° sine				220	1
V _{T0}	threshold voltage		T _{v1} = 140°C			0.85	١
r _τ	slope resistance } for power lo	ess calculation only	vo			2.8	m۵
R _{thJC}	thermal resistance junction to case	٥				0.22	K/W
R _{thCH}	thermal resistance case to heatsir				0.20	•	K/W
	total power dissipation		$T_c = 25^{\circ}C$		0.20	520	Ŵ
	max. forward surge current	t = 10 ms; (50 Hz), sine	$T_{c} = 23^{\circ} C$ $T_{v_{1}} = 45^{\circ} C$			2.40	k/
I _{TSM}	max. Iorward burge burrent	t = 8,3 ms; (60 Hz), sine	$V_{\rm R} = 0 V$			2.59	k/
							k/
		t = 10 ms; (50 Hz), sine	$T_{VJ} = 140 ^{\circ}C$			2.04	
101	under for funitor	t = 8,3 ms; (60 Hz), sine	$\frac{V_{R} = 0 V}{T_{R} + 1500}$			2.21	k/
l²t	value for fusing	t = 10 ms; (50 Hz), sine	$T_{VJ} = 45^{\circ}C$			28.8	1
		t = 8,3 ms; (60 Hz), sine	$V_{R} = 0 V$			27.9	kA ²
		t = 10 ms; (50 Hz), sine	$T_{vJ} = 140 ^{\circ}\text{C}$			20.8	kA ²
		t = 8,3 ms; (60 Hz), sine	$V_R = 0 V$			20.2	kA ²
C	junction capacitance	$V_R = 400 V$ f = 1 MHz	$T_{vJ} = 25^{\circ}C$		119		pl
P _{GM}	max. gate power dissipation	t _P = 30 μs	$T_c = 140^{\circ}C$			10	۷
		t _P = 300 μs				5	v
P _{GAV}	average gate power dissipation					0.5	V
(di/dt) _{cr}	critical rate of rise of current	$T_{vJ} = 140 ^{\circ}C; f = 50 Hz$ re	epetitive, $I_{T} = 450 \text{ A}$			150	A/μ
		t_{P} = 200 µs; di_G/dt = 0.45 A/µs; -					
		$I_{G} = 0.45 \text{ A}; \text{ V} = \frac{2}{3} \text{ V}_{DRM}$ no	on-repet., $I_{\tau} = 150 \text{ A}$			500	A/μ
(dv/dt) _{cr}	critical rate of rise of voltage	$V = \frac{2}{3} V_{DBM}$	T _{vJ} = 140°C			1000	V/µ
		$R_{GK} = \infty$; method 1 (linear volta	ge rise)				
V _{gt}	gate trigger voltage	$V_{\rm D} = 6 \text{ V}$	$T_{vJ} = 25^{\circ}C$			1.5	١
ai			T _{vJ} = -40°C			1.6	١
I _{GT}	gate trigger current	$V_{\rm D} = 6 \text{ V}$	$T_{VJ} = 25^{\circ}C$			150	m/
G	g		$T_{vJ} = -40^{\circ}C$			200	
V _{gd}	gate non-trigger voltage	$V_{\rm D} = \frac{2}{3} V_{\rm DBM}$	$T_{VJ} = -40^{\circ} \text{C}$ $T_{VJ} = 140^{\circ} \text{C}$			0.2	1
	gate non-trigger current	VD - 73 VDRM	101 - 140 0			10	ļ
		10.00	Τ				
I.	latching current	$t_p = 10 \ \mu s$	$T_{vJ} = 25 °C$			200	m/
_		$I_{\rm G} = 0.45 \text{A}; \text{di}_{\rm G}/\text{dt} = 0.45 \text{A}/\mu\text{s}$					
I _H	holding current	$V_{\rm D} = 6 \ V \ R_{\rm GK} = \infty$	$T_{VJ} = 25 \degree C$			200	m/
t _{gd}	gate controlled delay time	$V_{D} = \frac{1}{2} V_{DRM}$	$T_{vJ} = 25 ^{\circ}C$			2	μ
		$I_{G} = 0.45 \text{ A}; \ di_{G}/dt = 0.45 \text{ A}/\mu s$					
tq	turn-off time	$V_{R} = 100 \text{ V}; I_{T} = 150 \text{ A}; \text{ V} = \frac{2}{7}$	⅓ V _{DRM} T _{vJ} =125 °C		185		μ
		di/dt = 10 A/µs dv/dt = 20 V	/us_t_ = 200 us				

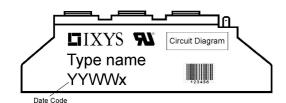
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MCMA140P1200TA

Package TO-240AA			Ratings					
Symbol	Definition	Conditions			min.	typ.	max.	Unit
I _{RMS}	RMS current	per terminal					200	Α
\mathbf{T}_{v_J}	virtual junction temperature				-40		140	°C
T _{op}	operation temperature				-40		125	°C
T _{stg}	storage temperature				-40		125	°C
Weight						81		g
M _D	mounting torque				2.5		4	Nm
M _T	terminal torque				2.5		4	Nm
d _{Spp/App}	creepage distance on surface striking distance th		terminal to terminal	13.0	9.7			mm
d _{Spb/Apb}	creepage ustance on suna		terminal to backside	16.0	16.0			mm
V	isolation voltage t =	t = 1 second			4800			V
	t = 1 minute		50/60 Hz, RMS; liso∟ ≤ 1 mA		4000			V



Part description

M = Module C = Thyristor (SCR) M = Thyristor A = (up to 1800V) 140 = Current Rating [A] P = Phase leg 1200 = Reverse Voltage [V] TA = TO-240AA-1B

Ordering	Ordering Number	Marking on Product	Delivery Mode	Quantity	Code No.
Standard	MCMA140P1200TA	MCMA140P1200TA	Box	36	512625

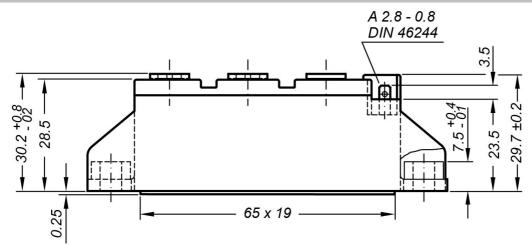
Similar Part	Package	Voltage class
MCMA140P1400TA	TO-240AA-1B	1400

Equiva	lent Circuits for	Simulation	* on die level	T _{vj} = 140 °C
	- R _o -	Thyristor		
V _{0 max}	threshold voltage	0.85		V
$\mathbf{R}_{0 \text{ max}}$	slope resistance *	1.6		mΩ

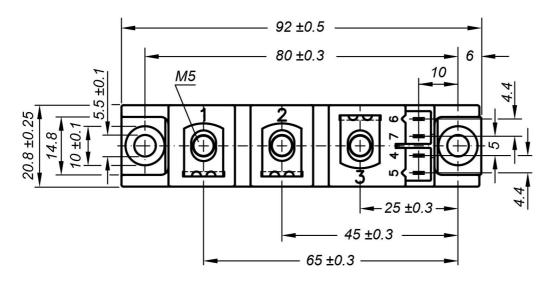
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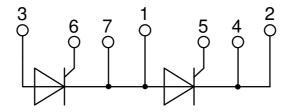
Outlines TO-240AA



General tolerance: DIN ISO 2768 class "c"



Optional accessories: Keyed gate/cathode twin plugs Wire length: 350 mm, gate = white, cathode = red UL 758, style 3751 Type **ZY 200L** (L = Left for pin pair 4/5) Type **ZY 200R** (R = Right for pin pair 6/7)



MCMA140P1200TA

140

4 5 6 7 8 10

°('

105

l²t

[A²s]

104

10³

200

1

50 Hz, 80% V

= 45°C

0.1

t [s]

 I_{TSM} : crest value, t: duration

T_{VJ} =

125

tvp

1.00

I_G [A]

Fig. 2 Surge overload current

1

 $V_{R} = 0 V$

 $T_{VJ} = 45^{\circ}C$

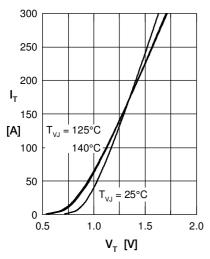
2

3

Fig. 3 I²t versus time (1-10 s)

t [ms]

Thyristor



2000

1600

1200

800

100.0

10.0

1.0

0.1

0.01

0.10

t_{gd}

[µs]

0.01

 $T_{VJ} = 140^{\circ}C$

I_{TSM}

[A]

Fig. 1 Forward characteristics

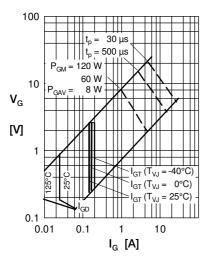


Fig. 4 Gate voltage & gate current

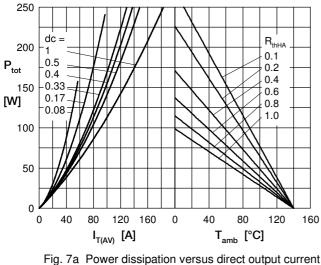


Fig. 7b and ambient temperature

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