

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

Email & Skype: info@chipsmall.com Web: www.chipsmall.com

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China









High Voltage Thyristor Module

2000 V

600 A

 V_{T} 1.06 V

Single Thyristor

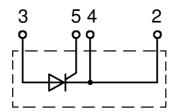
Part number

MCO600-20io1



Backside: isolated





Features / Advantages:

- Thyristor for line frequency
- Planar passivated chip
- Long-term stability
- Direct Copper Bonded Al2O3-ceramic

Applications:

- Line rectifying 50/60 Hz
- Softstart AC motor control
- DC Motor control
- Power converter
- AC power control
- Lighting and temperature control

Package: Y1

- Isolation Voltage: 3600 V~
- Industry standard outline
- RoHS compliant
- Soldering pins for PCB mounting
- Base plate: Copper internally DCB isolated
- Advanced power cycling

Terms _Conditions of usage:

The data contained in this product data sheet is exclusively intended for technically trained staff. The user will have to evaluate the suitability of the product for the intended application and the completeness of the product data with respect to his application. The specifications of our components may not be considered as an assurance of component characteristics. The information in the valid application- and assembly notes must be considered. Should you require product information in excess of the data given in this product data sheet or which concerns the specific application of your product, please contact your local sales office.

Due to technical requirements our product may contain dangerous substances. For information on the types in question please contact your local sales office.

Should you intend to use the product in aviation, in health or life endangering or life support applications, please notify. For any such application we urgently recommend

to perform joint risk and quality assessments;
the conclusion of quality agreements;

- to establish joint measures of an ongoing product survey, and that we may make delivery dependent on the realization of any such measures.

IXYS reserves the right to change limits, conditions and dimensions.

Data according to IEC 60747 and per semiconductor unless otherwise specified

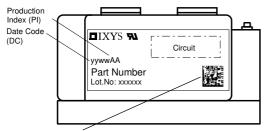
20170112e



Thyristo		O a sa aliai a sa a			Ratings	1	1
Symbol	Definition	Conditions	T 0500	min.	typ.	max.	Un
V _{RSM/DSM}	max. non-repetitive reverse/forwa		$T_{VJ} = 25^{\circ}C$			2100	<u> </u>
V _{RRM/DRM}	max. repetitive reverse/forward bloom		$T_{VJ} = 25^{\circ}C$			2000	1 1 1 1
I _{R/D}	reverse current, drain current	$V_{R/D} = 2000 \text{ V}$	$T_{VJ} = 25^{\circ}C$			2	m
		$V_{R/D} = 2000 \text{ V}$	$T_{VJ} = 125^{\circ}C$			40	m.
V _T	forward voltage drop	$I_T = 600 A$	$T_{VJ} = 25^{\circ}C$			1.12	,
		I _⊤ =1200 A				1.34	<u> </u>
		$I_{T} = 600 \text{ A}$	$T_{VJ} = 125$ °C			1.06	
		I _T =1200 A				1.33	!
I _{TAV}	average forward current	$T_C = 85^{\circ}C$	$T_{VJ} = 140$ °C			600	-
I _{T(RMS)}	RMS forward current	180° sine				940	-
V _{T0}	threshold voltage		T _{vJ} = 140°C			0.81	,
r _T	slope resistance } for power lo	oss calculation only				0.4	m۵
R _{thJC}	thermal resistance junction to cas	e				0.065	K/V
R _{thCH}	thermal resistance case to heatsin				0.020		K/V
P _{tot}	total power dissipation		T _C = 25°C			1770	٧
I _{TSM}	max. forward surge current	t = 10 ms; (50 Hz), sine	$T_{V,I} = 45^{\circ}C$			15.0	1
- 15W	5	t = 8.3 ms; (60 Hz), sine	$V_R = 0 V$			16.2	k/
		t = 10 ms; (50 Hz), sine	T _{v,i} = 140°C			12.8	1
		t = 8.3 ms; (60 Hz), sine	$V_R = 0 V$			13.8	į
l²t	value for fusing	t = 10 ms; (50 Hz), sine	$T_{VJ} = 45^{\circ}C$			1.13	1
	value for rushing	t = 8.3 ms; (60 Hz), sine	$V_R = 0 V$			1.09	i .
		t = 6.5 ms; (60 Hz), sine t = 10 ms; (50 Hz), sine	$V_R = 0 V$ $T_{VJ} = 140 ^{\circ}C$			812.8	
							į
_	iunation consoltance	t = 8,3 ms; (60 Hz), sine	$V_R = 0 V$		400	788.8	ļ
C,	junction capacitance	V _R = 700 V f = 1 MHz	$T_{VJ} = 25^{\circ}C$		469	100	p
P_{GM}	max. gate power dissipation	$t_P = 30 \mu s$	$T_{C} = 140^{\circ}C$			120	۷
_		$t_{P} = 300 \mu s$				60	V
P _{GAV}	average gate power dissipation					20	V
(di/dt) _{cr}	critical rate of rise of current		epetitive, $I_T = 1800 A$			100	A/μ
		$t_P = 200 \mu s; di_G/dt = 1 A/\mu s; -$					1
			on-repet., $I_T = 600 A$				A/µ
$(dv/dt)_{cr}$	critical rate of rise of voltage	$V = \frac{2}{3} V_{DRM}$	$T_{VJ} = 140$ °C			1000	V/µ
		R _{GK} = ∞; method 1 (linear volta	ge rise)				i 1 1
V _{GT}	gate trigger voltage	$V_D = 6 V$	$T_{VJ} = 25^{\circ}C$			2	١
			$T_{VJ} = -40$ °C			3	1
I _{GT}	gate trigger current	$V_D = 6 V$	$T_{VJ} = 25^{\circ}C$			300	m
			$T_{VJ} = -40$ °C			400	m/
V _{GD}	gate non-trigger voltage	$V_D = \frac{2}{3} V_{DRM}$	$T_{VJ} = 140^{\circ}C$			0.25	١
I _{GD}	gate non-trigger current					10	m
I _L	latching current	t _p = 30 μs	$T_{VJ} = 25$ °C			400	m
	-	$I_G = 1 \text{ A}; \text{ di}_G/\text{dt} = 1 \text{ A}/\mu \text{s}$					
I _H	holding current	$V_D = 6 \text{ V } R_{GK} = \infty$	$T_{VJ} = 25$ °C			300	m
т _{gd}	gate controlled delay time	$V_{D} = \frac{1}{2} V_{DRM}$	$T_{VJ} = 25 ^{\circ}\text{C}$			2	Î
∙ gd	gato controlled dolay time	$I_G = 1A$; $di_G/dt = 1A/\mu s$					μ
							1
t _q	turn-off time	$V_R = 100 \text{ V}; I_T = 600 \text{ A}; V = \frac{2}{3}$	/ \/ T +0E • \		350		μ



Package Y1					Ratings			
Symbol	Definition	Conditions		min.	typ.	max.	Unit	
I _{RMS}	RMS current	per terminal				600	Α	
T _{VJ}	virtual junction temperature			-40		140	°C	
T _{op}	operation temperature			-40		125	°C	
T _{stg}	storage temperature					125	°C	
Weight					650		g	
M _D	mounting torque			4.5		7	Nm	
$\mathbf{M}_{\scriptscriptstyleT}$	terminal torque			11		13	Nm	
d _{Spp/App}	creepage distance on surface striking distanc	istance through air	terminal to terminal	16.0			mm	
d _{Spb/Apb}		stance through an	terminal to backside	25.0			mm	
V _{ISOL}	isolation voltage	t = 1 second		3600			V	
.002	t = 1 minute	50/60 Hz, RMS; IISOL ≤ 1 mA	3000			٧		



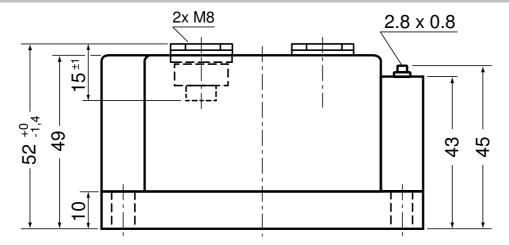
Data Matrix: part no. (1-19), DC + PI (20-25), lot.no.# (26-31), blank (32), serial no.# (33-36)

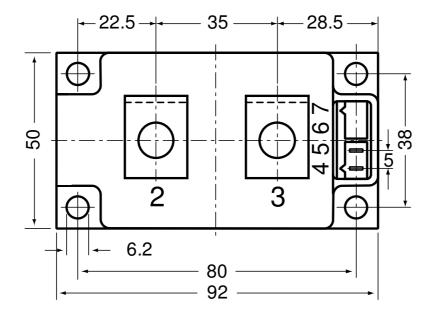
Ordering	Ordering Number	Marking on Product	Delivery Mode	Quantity	Code No.
Standard	MCO600-20io1	MCO600-20io1	Box	3	474320

Equiv	alent Circuits for	Simulation	* on die level	T _{vJ} = 140 °C
$I \rightarrow V_0$	R_0	Thyristor		
V _{0 max}	threshold voltage	0.81		V
$R_{0 max}$	slope resistance *	0.22		$m\Omega$



Outlines Y1



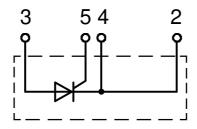


Optional accessories for modules

Keyed gate/cathode twin plugs with wire length = 350 mm, gate = white, cathode = red

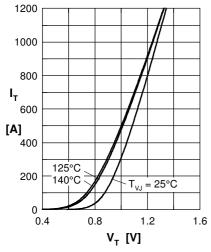
Type ZY 180L (L = Left for pin pair 4/5)
Type ZY 180R (R = Right for pin pair 6/7)

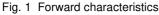
UL 758, style 3751





Thyristor





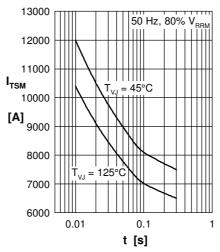


Fig. 2 Surge overload current

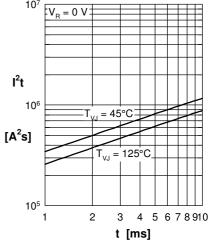


Fig. 3 I²t versus time (1-10 ms)

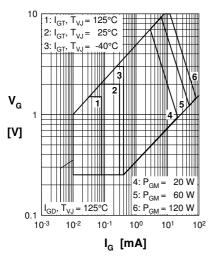


Fig. 4 Gate trigger characteristics

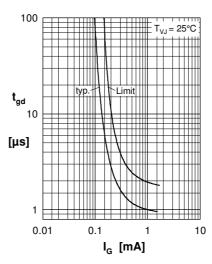


Fig. 5 Gate controlled delay time

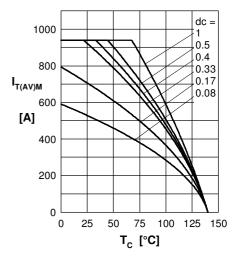


Fig. 6 Max. forward current at case temperature

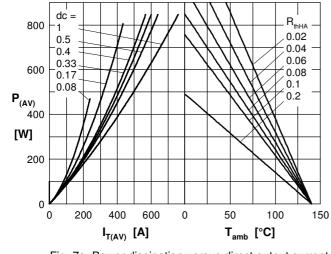


Fig. 7a Power dissipation versus direct output current Fig. 7b and ambient temperature

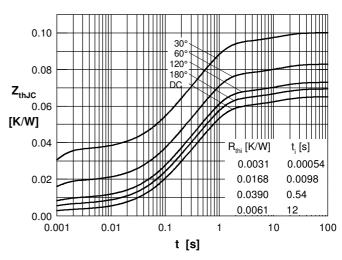


Fig. 8 Transient thermal impedance





IGBT