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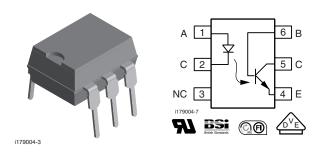






### Vishay Semiconductors

# Optocoupler, Phototransistor Output, Low Input Current, with Base Connection



#### **DESCRIPTION**

The MCT5211 is a optocoupler with a high efficiency AlGaAs LED optically coupled to a NPN phototransistor. The high performance LED makes operation at low input currents practical. The coupler is housed in a six pin DIP package. Isolation test voltage is  $5300\ V_{RMS}$ .

Because these parts have guaranteed CTRs at 1 mA and 3 mA, they are ideally suitable for interfacing from CMOS to TTL or LSTTL to TTL. They are also ideal for telecommunications applications such as ring or off-hook detection.

#### **FEATURES**

• Saturation CTR - MCT5211, > 100 % at  $I_F = 1.6 \text{ mA}$ 



- High isolation voltage, 5300 V<sub>RMS</sub>
- Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC



#### **AGENCY APPROVALS**

- UL1577, file no. E52744 system code H, double protection
- BSI IEC 60950; IEC 60065
- DIN EN 60747-5-2 (VDE 0884)/DIN EN 60747-5-5 (pending), available with option 1
- CSA 93751

ORDERING INFORMATION								
M C T 5 2 1  PART NUMBER	1 - X 0 # # T  PACKAGE OPTION TAPE AND REEL Option 7 Option 9 > 0.7 mm							
AGENCY CERTIFIED/PACKAGE	CTR (%)							
AGENCT CENTIFIED/FACKAGE	1 mA							
UL, BSI, CSA	> 110							
DIP-6	MCT5211							
SMD-6, option 7	MCT5211-X007T <sup>(1)</sup>							
SMD-6, option 9	MCT5211-X009T <sup>(1)</sup>							
UL, BSI, CSA, VDE	> 110							
SMD-6, option 7	MCT5211-X017T							

#### Note

- Additional options may be possible, please contact sales office.
- (1) Also available in tubes, do not put T on the end.

# VISHAY.

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<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT				
INPUT								
Peak reverse voltage		V <sub>R</sub>	6	V				
Forward continuos current		I <sub>F</sub>	40	mA				
Power dissipation		P <sub>diss</sub>	75	mW				
Derate linearly from 25 °C			1	mW/°C				
OUTPUT								
Collector emitter breakdown voltage		BV <sub>CEO</sub>	30	V				
Emitter collector breakdown voltage		BV <sub>ECO</sub>	7	V				
Collector base breakdown voltage		BV <sub>CBO</sub>	70	V				
Power dissipation		P <sub>diss</sub>	200	mW				
Derate linearly from 25 °C			2.6	mW/°C				
COUPLER								
Isolation test voltage		V <sub>ISO</sub>	5300	V <sub>RMS</sub>				
Total package dissipation (LED and detector)		P <sub>tot</sub>	260	mW				
Derate linearly from 25 °C			3.5	mW/°C				
Creepage distance			≥ 7	mm				
Clearance distance			≥ 7	mm				
Comparative tracking index per DIN IEC 112/VDE 0303, part 1		СТІ	175					
Isolation resistance	V <sub>IO</sub> = 500 V, T <sub>amb</sub> = 25 °C	R <sub>IO</sub>	≥ 10 <sup>12</sup>	Ω				
ISOIALIOTI TESISLATICE	V <sub>IO</sub> = 500 V, T <sub>amb</sub> = 100 °C	R <sub>IO</sub>	≥ 10 <sup>11</sup>	Ω				
Operating temperature		T <sub>amb</sub>	- 55 to + 100	°C				
Storage temperature		T <sub>stg</sub>	- 55 to + 150	°C				

#### Note

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
maximum ratings for extended periods of the time can adversely affect reliability.

<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT							
Forward voltage	$I_F = 5 \text{ mA}$		$V_{F}$		1.2	1.5	V
Reverse voltage	I <sub>R</sub> = 10 μA		$V_R$	6			V
OUTPUT							
DC forward current gain	$V_{CE} = 5 \text{ V}, I_{C} = 100 \mu\text{A}$		h <sub>FE</sub>	100	200		
Collector emitter breakdown voltage	I <sub>C</sub> = 100 μA		BV <sub>CEO</sub>	30			V
Emitter collector breakdown voltage	I <sub>E</sub> = 100 μA		BV <sub>ECO</sub>	7			V
Collector base breakdown voltage	I <sub>E</sub> = 10 μA		BV <sub>CBO</sub>	70			V
Collector emitter leakage voltage	V <sub>CE</sub> = 10 V		I <sub>CEO</sub>		5	100	nA
COUPLER							
Saturation voltage	I <sub>F</sub> = 1.6 mA, I <sub>C</sub> = 1.6 mA	MCT5211	V <sub>CEsat</sub>		0.25	0.4	V

#### Note

Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering
evaluation. Typical values are for information only and are not part of the testing requirements.



## Optocoupler, Phototransistor Output, Low Input Current, with Base Connection Vishay Semiconductors

CURRENT TRANSFER RATIO (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
Current transfer ratio (collector emitter saturated)	$V_{CE} = 0.4 \text{ V}, I_F = 1.6 \text{ mA}$	MCT5211	CTR <sub>CEsat</sub>	100	200		%
	V <sub>CE</sub> = 0.4 V, I <sub>F</sub> = 1 mA	MCT5211	CTR <sub>CEsat</sub>	75	150		%
Current transfer ratio	$V_{CE} = 5 \text{ V}, I_F = 1.6 \text{ mA}$	MCT5211	CTR	150	300		%
	V <sub>CE</sub> = 5 V, I <sub>F</sub> = 1 mA	MCT5211	CTR	110	225		%
Current transfer ratio (collector base)	$V_{CE} = 4.3 \text{ V}, I_F = 1.6 \text{ mA}$	MCT5211	CTR <sub>CB</sub>	0.3	0.6		%
	$V_{CE} = 4.3 \text{ V}, I_F = 1 \text{ mA}$	MCT5211	CTR <sub>CB</sub>	0.25	0.5		%

<b>SWITCHING CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
Propagation delay high to low	$R_L = 750~\Omega$ , $I_F = 1.6~mA$ , $V_{CC} = 5~V$	MCT5211	t <sub>PHL</sub>		20		μs
	$R_L = 1.5 \text{ k}\Omega$ , $I_F = 1 \text{ mA}$ , $V_{CC} = 5 \text{ V}$	MCT5211	t <sub>PHL</sub>		40		μs
Propagation delay low to high	$R_L$ = 750 $\Omega$ , $I_F$ = 1.6 mA, $V_{CC}$ = 5 $V$	MCT5211	t <sub>PLH</sub>		20		μs
	$R_L = 1.5 \text{ k}\Omega$ , $I_F = 1 \text{ mA}$ , $V_{CC} = 5 \text{ V}$	MCT5211	t <sub>PLH</sub>		40		μs

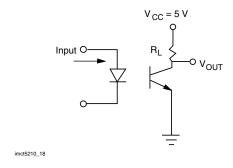


Fig. 1 - Switching Schematic

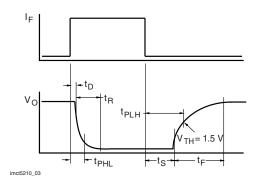


Fig. 2 - Switching Waveform

### Vishay Semiconductors Optocoupler, Phototransistor Output, Low Input Current, with Base Connection



### TYPICAL CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

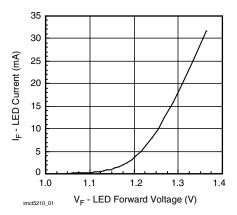


Fig. 3 - Forward Current vs. Forward Voltage

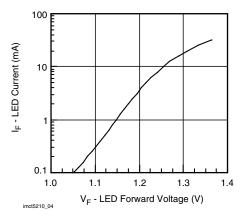


Fig. 4 - LED Forward Current vs. Forward Voltage

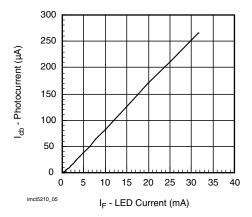


Fig. 5 - Collector Base Photocurrent vs. LED Current

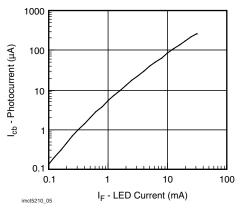


Fig. 6 - Photocurrent vs. LED Current

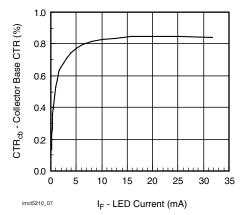


Fig. 7 - Collector Base CTR vs. LED Current

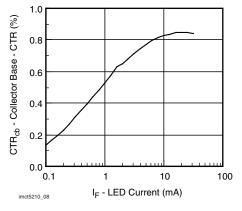


Fig. 8 - Collector Base CTR vs. LED Current



### Optocoupler, Phototransistor Output, Low Vishay Semiconductors Input Current, with Base Connection

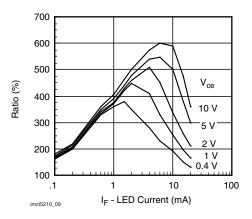


Fig. 9 - CTR vs. LED Current

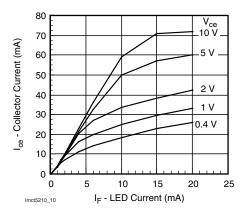


Fig. 10 - Collector Current vs. LED Current

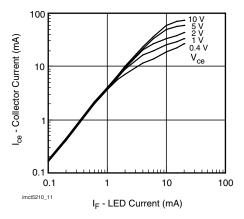


Fig. 11 - Collector Current vs. LED Current

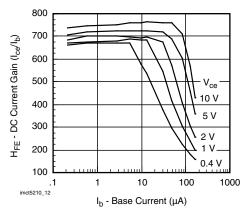


Fig. 12 - Transistor Current Gain vs. Base Current

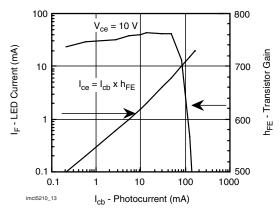


Fig. 13 - Transfer Curve

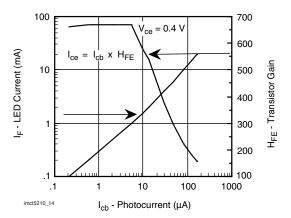


Fig. 14 - Transfer Curve

### Vishay Semiconductors Optocoupler, Phototransistor Output, Low Input Current, with Base Connection



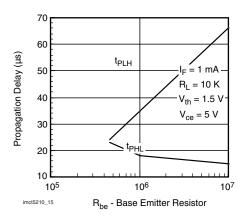


Fig. 15 - Propagation Delay vs. Base Emitter Resistor

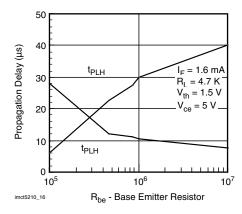


Fig. 16 - Propagation Delay vs. Base Emitter Resistor

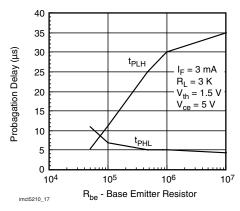
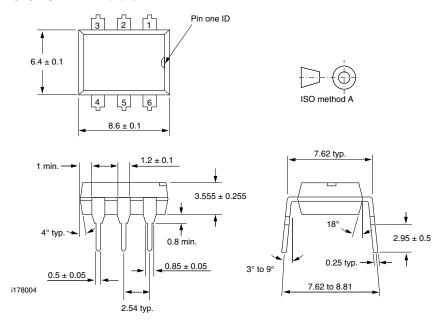


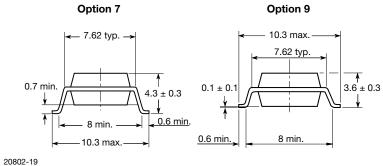
Fig. 17 - Propagation Delay vs. Base Emitter Resistor

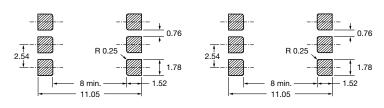


### Optocoupler, Phototransistor Output, Low Vishay Semiconductors Input Current, with Base Connection

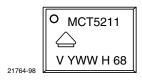
### **PACKAGE DIMENSIONS** in millimeters







### **PACKAGE MARKING** (example)



#### Notes

- Only option 7 is reflected in the package marking.
- The VDE logo is only marked on option 1 parts.
- Tape and reel suffix (T) is not part of the package marking.





Vishay

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